

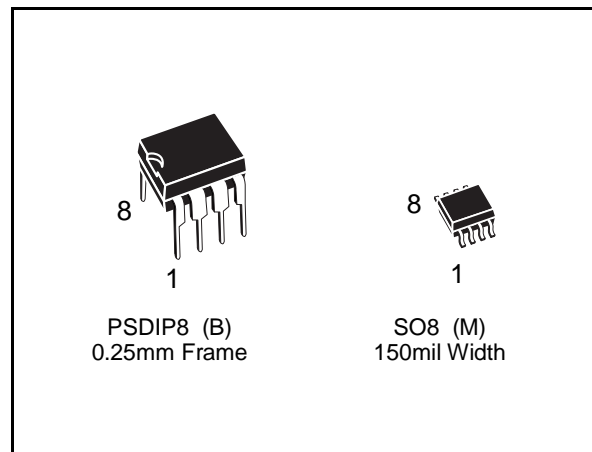


# M2201

## 2-Wires 1 Kbit (x8) Serial EEPROM

### DATA BRIEFING

- TWO WIRE SERIAL INTERFACE
- 100.000 ERASE/WRITE CYCLES with 100 YEARS DATA RETENTION at 55°C
- SINGLE SUPPLY VOLTAGE:
  - 4.5V to 5.5V for M2201 version
  - 2.7V to 5.5V for M2201V version
- HARDWARE WRITE CONTROL
- 100 KBIT TRANSFER RATE
- BYTE WRITE
- PAGE WRITE (up to 4 BYTES)
- SELF TIMED PROGRAMMING CYCLE
- AUTOMATIC ADDRESS INCREMENTING
- ENHANCED ESD/LATCH UP



### DESCRIPTION

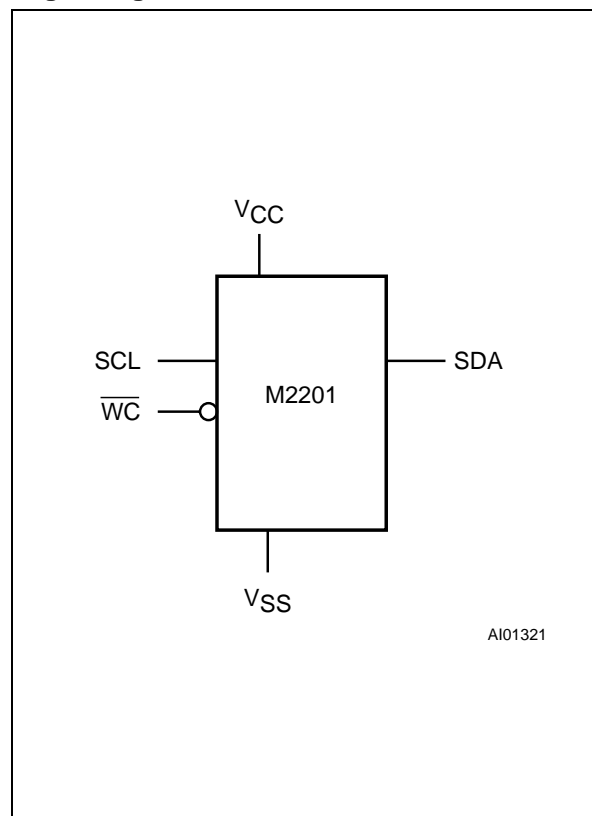
The M2201 is a simplified 2-wire bus 1 Kbit electrically erasable programmable memory (EEPROM), organized as 128 x8 bits. It is manufactured in STMicroelectronics's Hi-Endurance Advanced CMOS technology which guarantees a data retention of 100 years at 55°C.

Both Plastic Dual-in-Line and Plastic Small Outline packages are available.

The memory is compatible with a two wire serial interface which uses a bi-directional data bus and serial clock. Read and write operations are initiated by a START condition generated by the bus master and ended by a STOP condition.

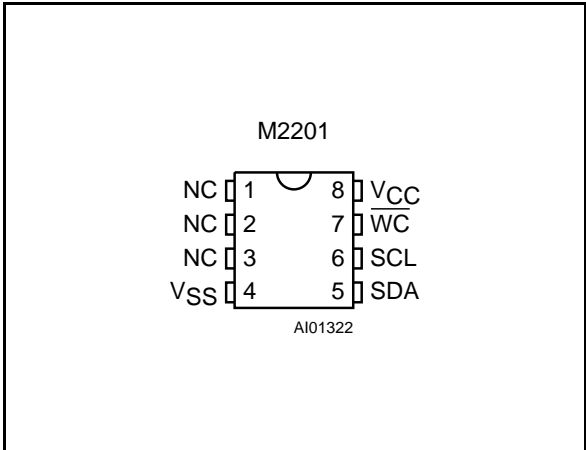
Address bits and  $\overline{RW}$  bit are defined in one single byte, instead of two (or three) bytes for the standard I<sup>2</sup>C protocol.

### Logic Diagram



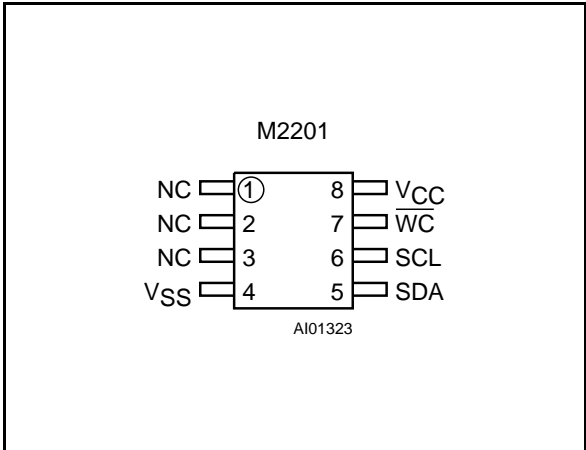
M2201

DIP Pin Connections



Warning: NC = Not Connected.

SO Pin Connections



Warning: NC = Not Connected.

Signal Names

SDA	Serial Data Input/Output
SCL	Serial Clock
WC	Write Control
VCC	Supply Voltage
VSS	Ground

Ordering Information Scheme

For a list of available options or for further information on any aspect of this device, please contact the STMicroelectronics Sales Office nearest to you.

Example: M2201 V M 1 TR

Operating Voltage	
blank	4.5V to 5.5V
V	2.7V to 5.5V
Package	
B	PSDIP8 0.25mm Frame
M	SO8 150mil Width
Temp. Range	
1	0 to 70 °C
6	-40 to 85 °C
Option	
TR	Tape & Reel Packing

**Note:** Devices are shipped from the factory with the memory content set at all "1's" (FFh).