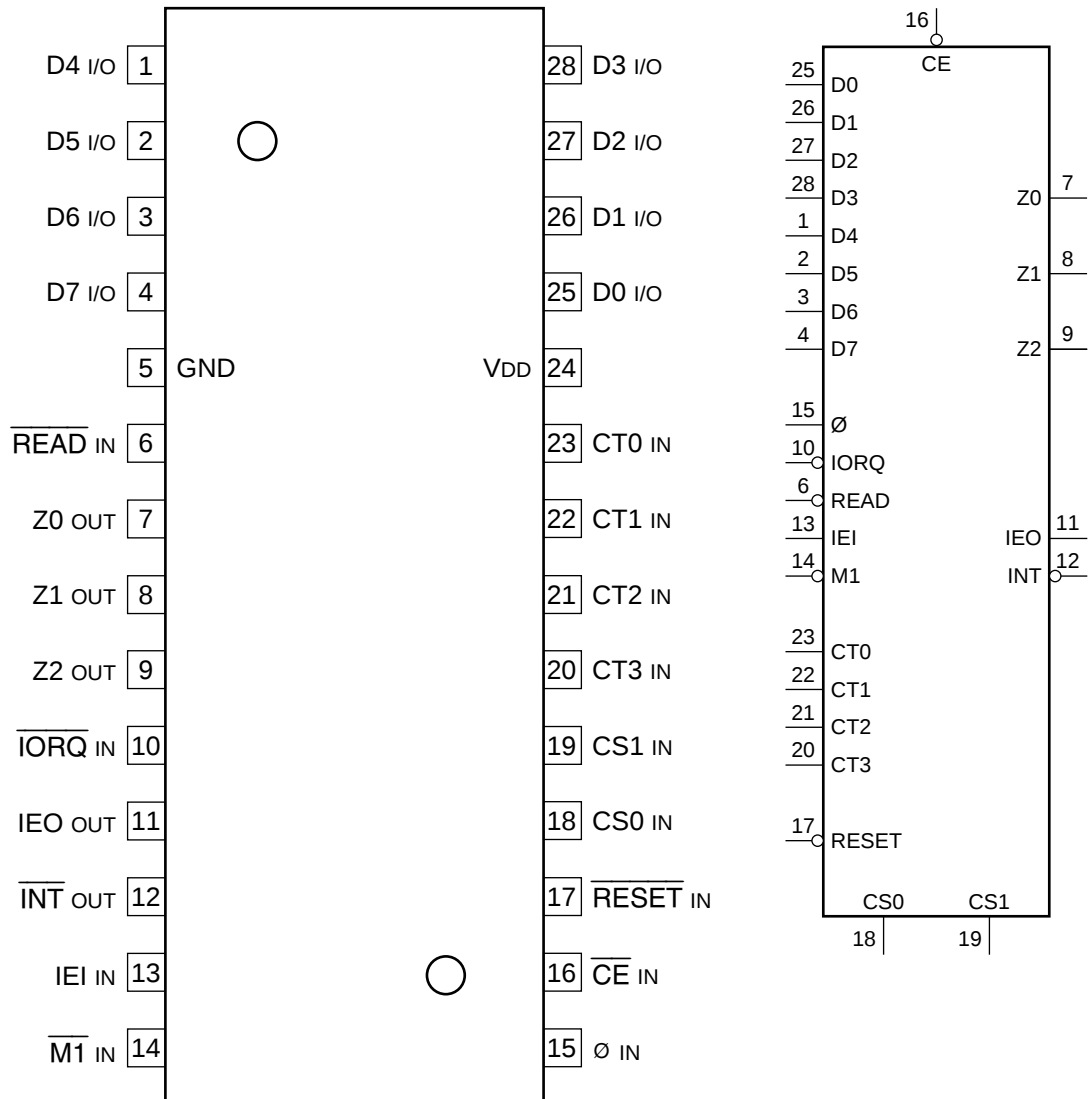


## C-MOS COUNTER TIMER CIRCUIT

—TOP VIEW—

**INPUT**

CE : CHIP ENABLE  
 CS0, CS1 : CHANNEL SELECT  
 CT0 - CT3 : EXTERNAL CLOCK/TIMER TRIGGER  
 IEI : INTERRUPT ENABLE  
 M1 : MACHINE CYCLE 1  
 Ø : SYSTEM CLOCK

**OUTPUT**

IEO : INTERRUPT ENABLE  
 INT : INTERRUPT REQUEST (OPEN DRAIN)

**INPUT/OUTPUT**

D0 - D7 : 3-STATE DATA  
 IORQ : I/O REQUEST  
 READ : READ CYCLE STATUS

**CHANNEL SELECT FUNCTION**

| CS1 | CS0 | SELECTED CHANNEL |
|-----|-----|------------------|
| 0   | 0   | 0                |
| 0   | 1   | 1                |
| 1   | 0   | 2                |
| 1   | 1   | 3                |

0 : LOW LEVEL

1 : HIGH LEVEL

