SHARP		<u>No. RD-97X2</u>
- -		
	<u>,</u>	
RELIABIL	ITY TEST REPO	ORT
• •		· · · · ·
Product Type Boot I	Block Smart voltage 8Mbit Fla	ish Memory
Model No. :	LH28F800BGB	
Package :	48Pin CSP (CSP048-P-0	808)
	- · · · · ·	
Date :	OCT. 22,1997	
		-
	GENERAL MANAGER M.NAKA	
	QUALITY ASSURANCE DEPAR QUALITY & RELIABILITY CON FUKUYAMA IC GROUP SHARP CORPORATION	TMENT NTROL CENTER

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1. Quality Assurance And Reliability Test During New Product Development

New product development begins with establishing reliability targets during the planning stage. During this stage the end applications functions and requirements are also considered in addition to the reliability targets.

Quality and reliability are built into the product from the start by having design and reliability review sessions in the development and design stages.

This insures that quality and reliability levels are maintained at the preproduction and mass production stages.

2. Reliability Test Methods

Reliability tests should always have good reproducibility. Thus, reliability tests for IC devices are based upon standardized test methods. Such uniform testing standards include those established by JIS(Japanese Industrial Standard) MIL-STD(U.S.MILitary Standard), EIAJ(Electronic Industries Association of Japan) and IEC(International Electrotechnical Commission). Sharp has based its own testing methods on these standards.

3. Evaluation Results

The results attached show that Sharp has met the high quality and reliability targets which are required by the above standards.

Note ; This evaluation has been performed upon a representative product which is selected from a series of related products with the same basic design, all packaged in the same package type.

Therefore, these evaluation results are applicable for the following Sharp models:

LH28F800BGB

4. Other Considerations

Please confirm that the specifications of this product meet the requirements of the applications.

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1. ENDURANCE TEST

No.		Conditions	Reference Standards	Number of Samples	Nu	LTPD		
	High	Ta=125°C	ЛS C 7022:В-1		240h	500h	1000h	
1	Temperature	Vcc/Vpp=6_5V	MIL-STD-883C	153	0	0	0	1.5%
_	Operation	1000h	1005.6					I
	High	Ta=140°C	JIS C 7022:B-3		240h	500h	1000h	
	Temperature		MIL-STD-883C	45	0	0	0	5%
	Storage	1000 h	1008.2					
	Low	T2=-65°C			240h	500h	1000h	
	Temperature		ЛS C 7022:В-4	11	0	0	0	20%
_	Storage	1000h						
	High Temp.	Ta=60°C,90%RH		1	240h	500h	1000h	
- 1	High Humi.		ЛS C 7022:B-5	22	0	0	0	10%
	Storage	1000h						
	High Temp.	Ta=85°C,85%RH	JIS C 7022:B-5		240h	500h	1000h	
	High Humi	Vcc/Vpp=5.5V		76	0	0	0	3%
	Bias	1000h						
	Temperature	Ta=-65 C(30min)~150 C(30min)	JIS C 7022:A-4		100cyc.	200cyc.	300cyc.	
1	Cycling	500cyc.	MIL-STD-883C	76	0	0	0	3%
			1010.7					_
7	PCT	Ta=121°C, 100%RH, No Bias	EIAJ IC-121:18		100h	200h	300h	
		2×10 Pa{2atm}		22	0	0	0	10%
		100h	•					
	[Series Test]							
1	Baking	Ta=120°C 3h						
	* * * :						·	
	Aoisture		EIAJ ED-4701:	22	0			10%
_ A	bsorption	Ta=30°C,70% 96h	A-133 ·				ł	
	+							
	R	Highest Temp.=240°C				.		
S	oldering	230°C-240°C, 15s, 2times		1				

<u>CRITERIA</u>

1

No.1 \sim 7: To maintain electrical characteristics within the limits established in the specifications of each device.

No.8: To maintain electrical characteristics within the limits established in the specifications of each device. There is no evidence of damage to the body material(i.e. Package cracking).

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FLCS

FLC3

2. Erase/Write Cycling Test

No.	Test	Conditions	Number of Cycles	Number of Samples	Number Of Failures	Failure Rate	Note
	Erase/ Write Cycling	Ta=0,70°C	10k	750	0	76 DPM/Block	Confidence Level=60%

<u>CRITERIA</u>

No.1: To maintain electrical characteristics within the limits established in the specifications of each device.

3. MISCELLANEOUS

No.	Test	Conditions	Reference Standards	Number Of Samples	Conditions	ESD/Latch-up Strength			ան
						≧0.4kV	≩0.6kV	≧0.8kV	≧1.0kV
1	Electrostatic	C=100pF	MIL-STD	3(each)	GND+				0
	discharges	R=1.5k0 883C	883C		GND-				0
			Method		VCC+			L	0
			3015		VCC-				0
						≧40mA	≩60mA	<u>≥80mA</u>	≥100mA
		Current application	EIAJ	3(each)	+				
2	Latch-up	test	ED-4701-1		-				
		tp=10ms, toff=500ms	C-113				•		
		VccMAX							
	" \bigcirc " Pass, " \times " NG, "-" No measuremen							rement	

<u>CRITERIA</u>

No.1: To maintain electrical characteristics within the limits established in the specifications of each device. No.2: No latch-up occurs.

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Reliability, 8 Mbit Flash Memory, Boot Block, Non -Volatile, ETOX ,LH28F800BGB