

# LH28F160BV-TL/ BVH-TL

**16 M-bit (2 MB x 8/1 MB x 16)  
Smart 3 Flash Memories**

## DESCRIPTION

The LH28F160BV-TL/BVH-TL flash memories with Smart 3 technology are high-density, low-cost, nonvolatile, read/write storage solution for a wide range of applications. The LH28F160BV-TL/BVH-TL can operate at  $V_{CC}$  and  $V_{PP} = 2.7$  V. Their low voltage operation capability realizes longer battery life and suits for cellular phone application. Their boot, parameter and main-blocked architecture, flexible voltage and enhanced cycling capability provide for highly flexible component suitable for portable terminals and personal computers. Their enhanced suspend capabilities provide for an ideal solution for code + data storage applications. For secure code storage applications, such as networking, where code is either directly executed out of flash or downloaded to DRAM, the LH28F160BV-TL/BVH-TL offer two levels of protection : absolute protection with  $V_{PP}$  at GND, selective hardware boot block locking. These alternatives give designers ultimate control of their code security needs.

## FEATURES

- Smart 3 technology
  - 2.7 to 3.6 V  $V_{CC}$
  - 2.7 to 3.6 V or 12 V  $V_{PP}$
- High performance read access time
  - LH28F160BV-TL90/BVH-TL90
    - 90 ns (2.7 to 3.6 V)
  - LH28F160BV-TL12/BVH-TL12
    - 120 ns (2.7 to 3.6 V)
- Enhanced automated suspend options
  - Write suspend to read
  - Block erase suspend to word write
  - Block erase suspend to read
- SRAM-compatible write interface
- User-configurable x8 or x16 operation
- Optimized array blocking architecture
  - Two 8 k-byte boot blocks
  - Six 8 k-byte parameter blocks
  - Thirty-one 64 k-byte main blocks
  - Top or bottom boot location
- Enhanced cycling capability
  - 100 000 block erase cycles
- Low power management
  - Deep power-down mode
  - Automatic power saving mode decreases  $I_{CC}$  in static mode
- Automated word/byte program and block erase
  - Command user interface
  - Status register
- ETOX<sup>TM</sup>\* V nonvolatile flash technology
- Packages
  - 48-pin TSOP Type I (TSOP048-P-1220) Normal bend
  - 60-ball CSP (FBGA060/048-P-0811)

\* ETOX is a trademark of Intel Corporation.

COMPARISON TABLE

VERSIONS	BIT CONFIGURATION	OPERATING TEMPERATURE
LH28F160BV-TL	2 MB x 8/1 MB x 16	0 to +70°C
LH28F160BVH-TL	2 MB x 8/1 MB x 16	-40 to +85°C
LH28F160BG-TL*	1 MB x 16	0 to +70°C
LH28F160BGH-TL*	1 MB x 16	-25 to +85°C

\* Refer to the datasheet of LH28F160BG-TL/BGH-TL.

PIN CONNECTIONS

48-PIN TSOP (Type I)

TOP VIEW

A15 1

A14 2

A13 3

A12 4

A11 5

A10 6

A9 7

A8 8

NC 9

RY/BY# 10

WE# 11

RP# 12

VPP 13

WP# 14

A19 15

A18 16

A17 17

A7 18

A6 19

A5 20

A4 21

A3 22

A2 23

A1 24

48 A16

47 BYTE#

46 GND

45 DQ15

44 DQ7

43 DQ14

42 DQ6

41 DQ13

40 DQ5

39 DQ12

38 DQ4

37 Vcc

36 DQ11

35 DQ3

34 DQ10

33 DQ2

32 DQ9

31 DQ1

30 DQ8

29 DQ0

28 OE#

27 GND

26 CE#

25 A0

(TSOP048-P-1220)

60-BALL CSP

1 2 3 4 5 6 7 8 9 10 11 12

A NC NC NC A14 A13 A15 A16 GND BYTE# NC NC NC

B A11 A10 A12 DQ15 DQ14 DQ7

C A8 NC A9 DQ6 DQ5 DQ13

D WE# RP# RY/BY# DQ12 Vcc DQ4

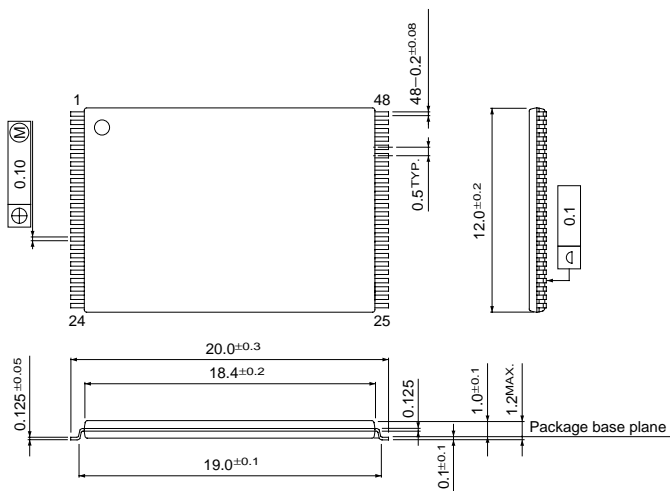
E WP# VPP A19 DQ10 DQ11 DQ3

F A17 A18 A7 DQ1 DQ2 DQ9

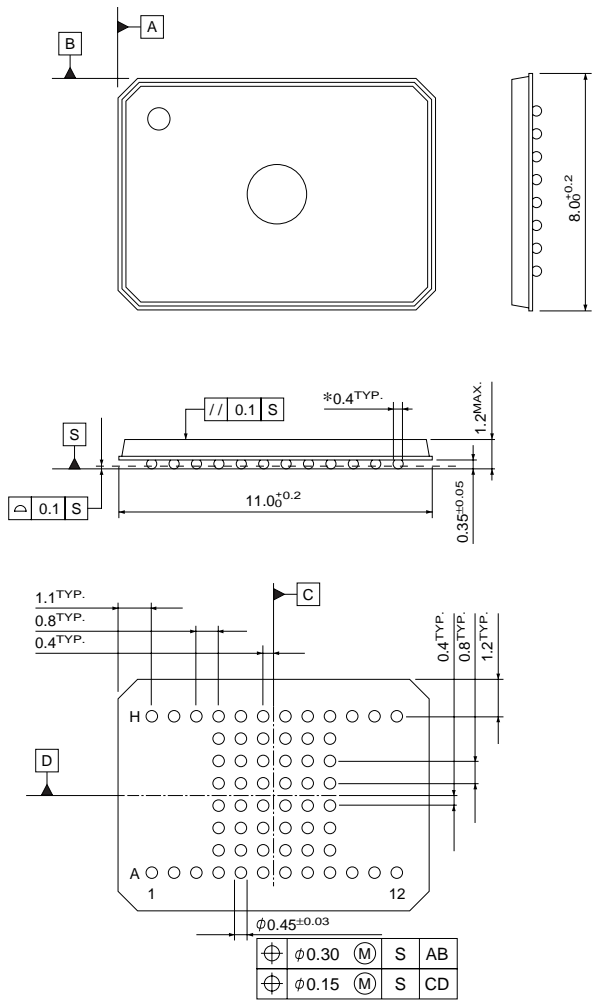
G A5 A6 A4 OE# DQ8 DQ0

H NC NC NC A2 A3 A1 A0 GND CE# NC NC NC

(FBGA060/048-P-0811)

**48 TSOP (TSOP048-P-1220)**

60 CSP (FBGA060/048-P-0811)



\*Land hole diameter  
for ball mounting