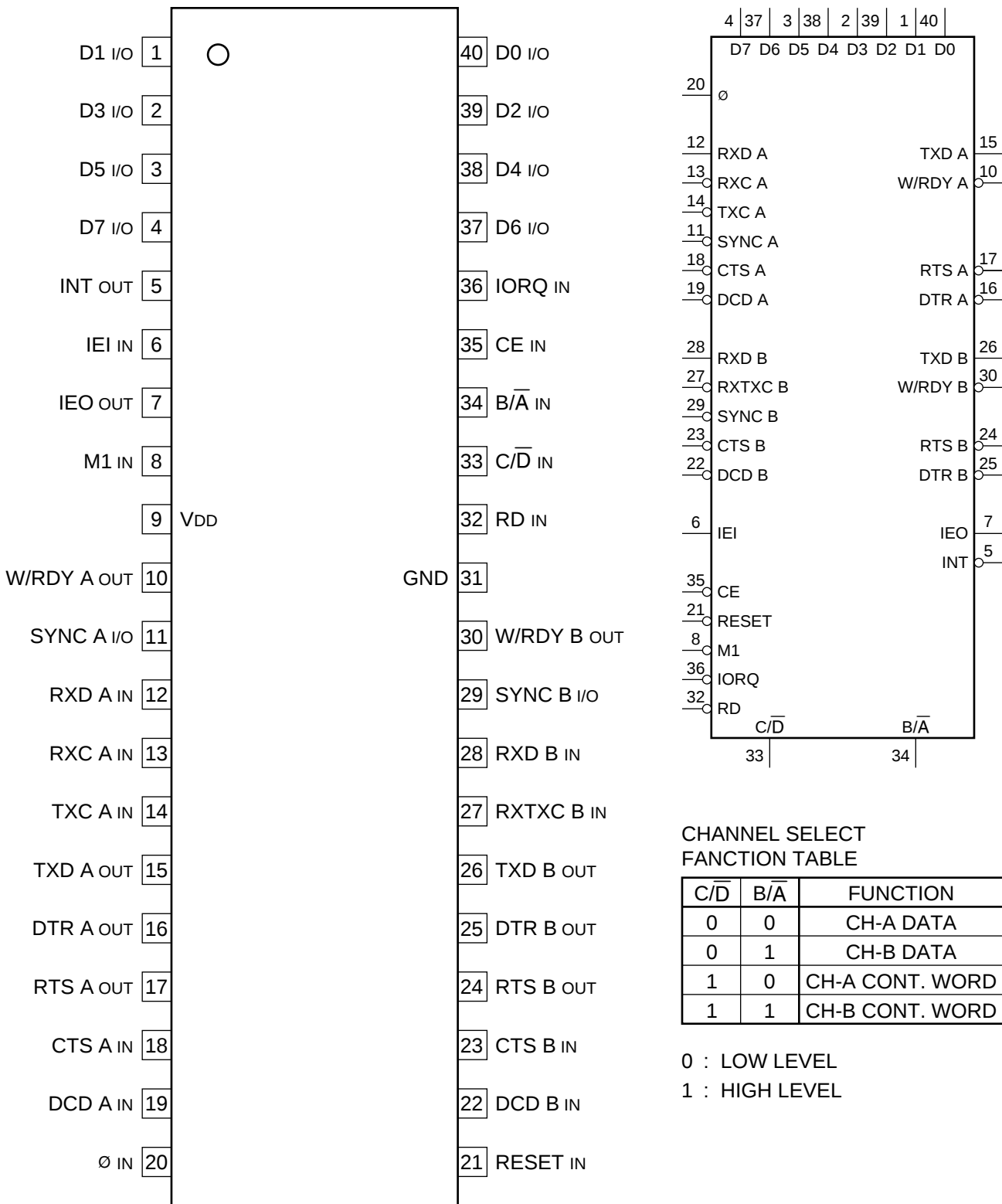


N-MOS 2-CHANNEL SERIAL INPUT/OUTPUT FOR MICROCOMPUTER SYSTEM

—TOP VIEW—



INPUT

$\overline{B/A}$: CHANNEL B/A SELECT
 $\overline{C/D}$: CONTROL/DATA SELECT
CE : CHIP ENABLE
CTS : CLEAR TO SEND
DCD : DATA CARRIER DETECT
IEI : INTERRUPT ENABLE
IORQ : I/O REQUEST
M1 : MACHINE CYCLE 1
RD : READ
RXC : RECEIVE CLOCK
RXD : RECEIVE DATA
TXC : TRANSMIT CLOCK
 $\emptyset$: SYSTEM CLOCK

OUTPUT

DTR : DATA TERMINAL READY
IEO : INTERRUPT ENABLE
INT : INTERRUPT
RTS : REQUEST TO SEND
TXD : TRANSMIT DATA
W/RDY : WAIT/READY

INPUT/OUTPUT

D7 - D0 : DATA BUS (3-STATE)
SYNC : EXTERNAL CHARACTER SYNCHRONIZATION

