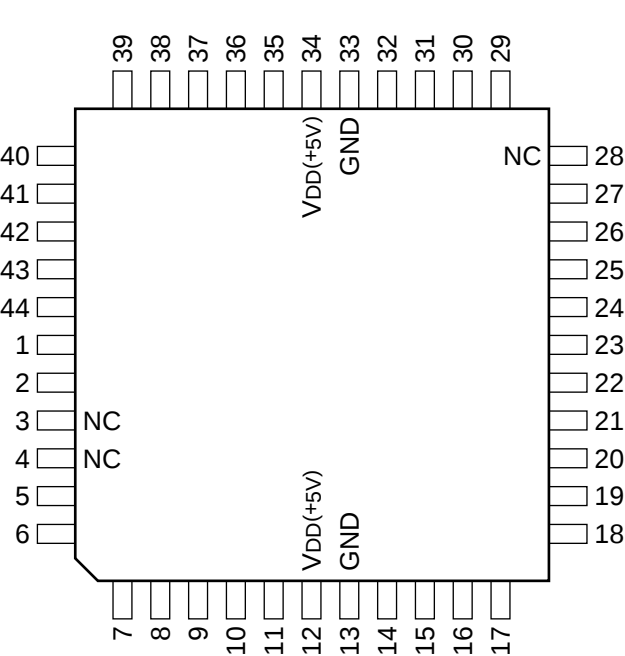
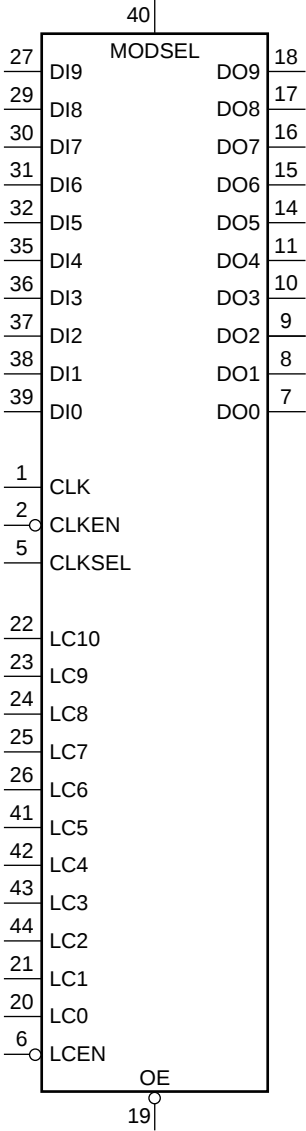

C-MOS 2K PROGRAMMABLE LINE BUFFER
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	CLK	23	I	LC9
2	I	CLKEN	24	I	LC8
3	—	NC	25	I	LC7
4	—	NC	26	I	LC6
5	I	CLKSEL	27	I	DI9
6	I	LCEN	28	—	NC
7	O	DO0	29	I	DI8
8	O	DO1	30	I	DI7
9	O	DO2	31	I	DI6
10	O	DO3	32	I	DI5
11	O	DO4	33	—	GND
12	—	VDD	34	—	VDD
13	—	GND	35	I	DI4
14	O	DO5	36	I	DI3
15	O	DO6	37	I	DI2
16	O	DO7	38	I	DI1
17	O	DO8	39	I	DI0
18	O	DO9	40	I	MODSEL
19	I	OE	41	I	LC5
20	I	LC0	42	I	LC4
21	I	LC1	43	I	LC3
22	I	LC10	44	I	LC2



INPUT
CLK ; MASTER CLOCK
CLKEN ; CLOCK ENABLE
CLKSEL ; CLOCK SELECT
DI9-DI0 ; DATA INPUT
LC10-LC0 ; LENGTH CONTROL
LCEN ; LENGTH CONTROL ENABLE
MODSEL ; MODE SELECT
OE ; OUTPUT ENABLE

OUTPUT
DO9-DO0 ; DATA OUTPUT

