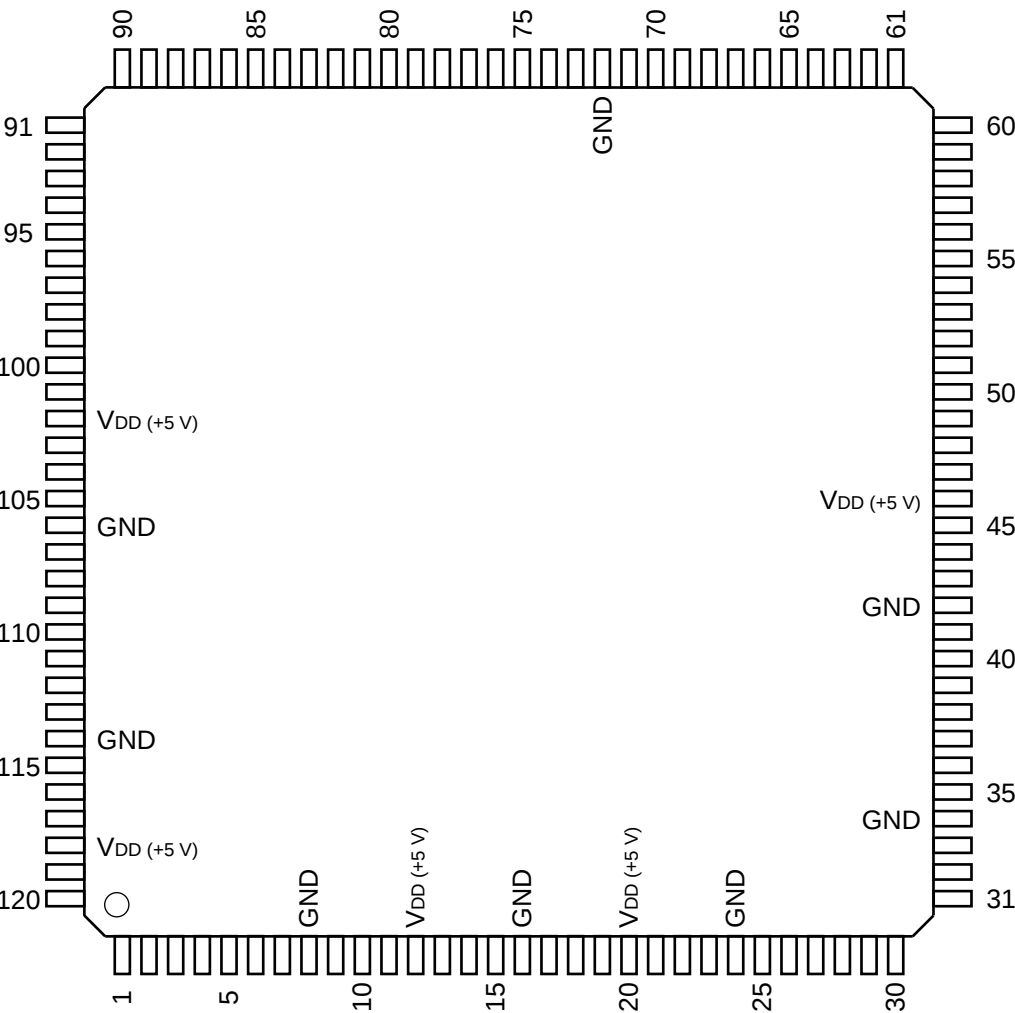


LF2250QC-33 (LOGICDEV) FLAT PACKAGE

C-MOS 12×10-BIT MATRIX MULTIPLIER
– TOP VIEW –

IL11
LF2250QC-33 (1/3)



MODE1	MODE0	DATA PORT FORMATTING					OPERATING MODE
		XC11-XC0	YC11-YC8	YC7-YC4	YC3-YC0	ZC11-ZC0	
0	0	X11-X0	Y11-Y8	Y7-Y4	Y3-Y0	Z11-Z0	3x3 MATRIX MULTIPLIER
0	1	CASIN15 -CASIN4	CASIN3 -CASIN0	NC	CASOUT3 -CASOUT0	CASOUT15 -CASOUT4	9-TAP FIR FILTER
1	0	CASIN15 -CASIN4	CASIN3 -CASIN0	NC	CASOUT3 -CASOUT0	CASOUT15 -CASOUT4	3x3 CONVOLVER
1	1	CASIN15 -CASIN4	CASIN3 -CASIN0	NC	CASOUT3 -CASOUT0	CASOUT15 -CASOUT4	4x2 CONVOLVER

(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	XC6	25	O	ZC0	49	I	KC9	73	I	A0	97	I	B11
2	O	XC5	26	O	ZC1	50	I	KB0	74	I	A1	98	I	C0
3	O	XC4	27	O	ZC2	51	I	KB1	75	I	A2	99	I	C1
4	O	XC3	28	O	ZC3	52	I	KB2	76	I	A3	100	I	C2
5	O	XC2	29	O	ZC4	53	I	KB3	77	I	A4	101	I	C3
6	O	XC1	30	O	ZC5	54	I	KB4	78	I	A5	102	—	V _{DD}
7	O	XC0	31	O	ZC6	55	I	KB5	79	I	A6	103	I	C4
8	—	GND	32	O	ZC7	56	I	KB6	80	I	A7	104	I	C5
9	O	YC11	33	O	ZC8	57	I	KB7	81	I	A8	105	I	C6
10	O	YC10	34	—	GND	58	I	KB8	82	I	A9	106	—	GND
11	O	YC9	35	O	ZC9	59	I	KB9	83	I	A10	107	I	C7
12	—	V _{DD}	36	O	ZC10	60	I	KA0	84	I	A11	108	I	C8
13	O	YC8	37	O	ZC11	61	I	KA1	85	I	B0	109	I	C9
14	O	YC7	38	I	KC0	62	I	KA2	86	I	B1	110	I	C10
15	O	YC6	39	I	KC1	63	I	KA3	87	I	B2	111	I	C11
16	—	GND	40	I	KC2	64	I	KA4	88	I	CLK	112	I	MODE1
17	O	YC5	41	I	KC3	65	I	KA5	89	I	B3	113	I	MODE0
18	O	YC4	42	—	GND	66	I	KA6	90	I	B4	114	—	GND
19	O	YC0	43	I	KC4	67	I	KA7	91	I	B5	115	O	XC11
20	—	V _{DD}	44	I	KC5	68	I	KA8	92	I	B6	116	O	XC10
21	O	YC1	45	I	KC6	69	I	KA9	93	I	B7	117	O	XC9
22	O	YC2	46	—	V _{DD}	70	I	CWE1	94	I	B8	118	—	V _{DD}
23	O	YC3	47	I	KC7	71	I	CWE0	95	I	B9	119	O	XC8
24	—	GND	48	I	KC8	72	—	GND	96	I	B10	120	O	XC7

INPUT

A0-A11 ; DATA INPUTS
 B0-B11 ; DATA INPUTS
 C0-C11 ; DATA INPUTS
 CLK ; MASTER CLOCK
 CWE0, CWE1 ; COEFFICIENT WRITE ENABLE
 KA0-KA9 ; COEFFICIENT INPUTS
 KB0-KB9 ; COEFFICIENT INPUTS
 KC0-KC9 ; COEFFICIENT INPUTS
 MODE0, MODE1 ; MODE SELECT

OUTPUT

XC0-XC11 ; DATA OUTPUTS / CASCADE INPUTS
 YC0-YC11 ; DATA OUTPUTS / CASCADE INPUTS/OUTPUTS
 ZC0-ZC11 ; DATA OUTPUTS / CASCADE OUTPUTS

[illegible]