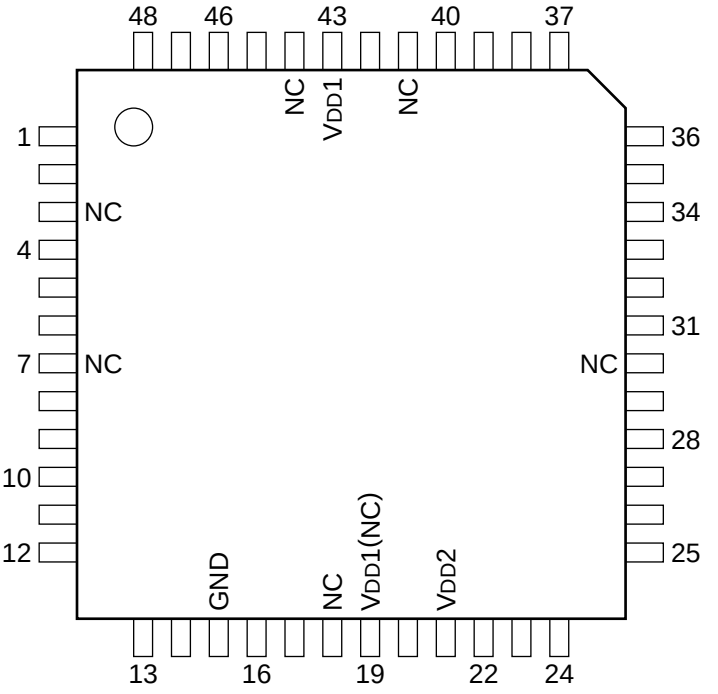
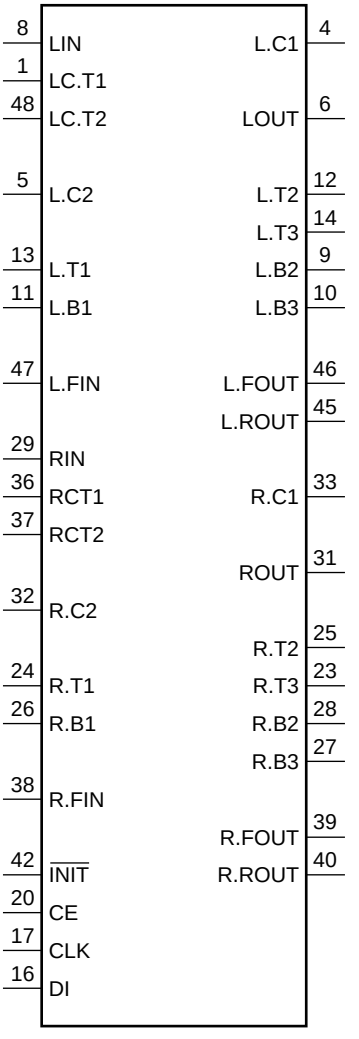
C-MOS ELECTRONIC VOLUME CONTROL

-TOP VIEW-





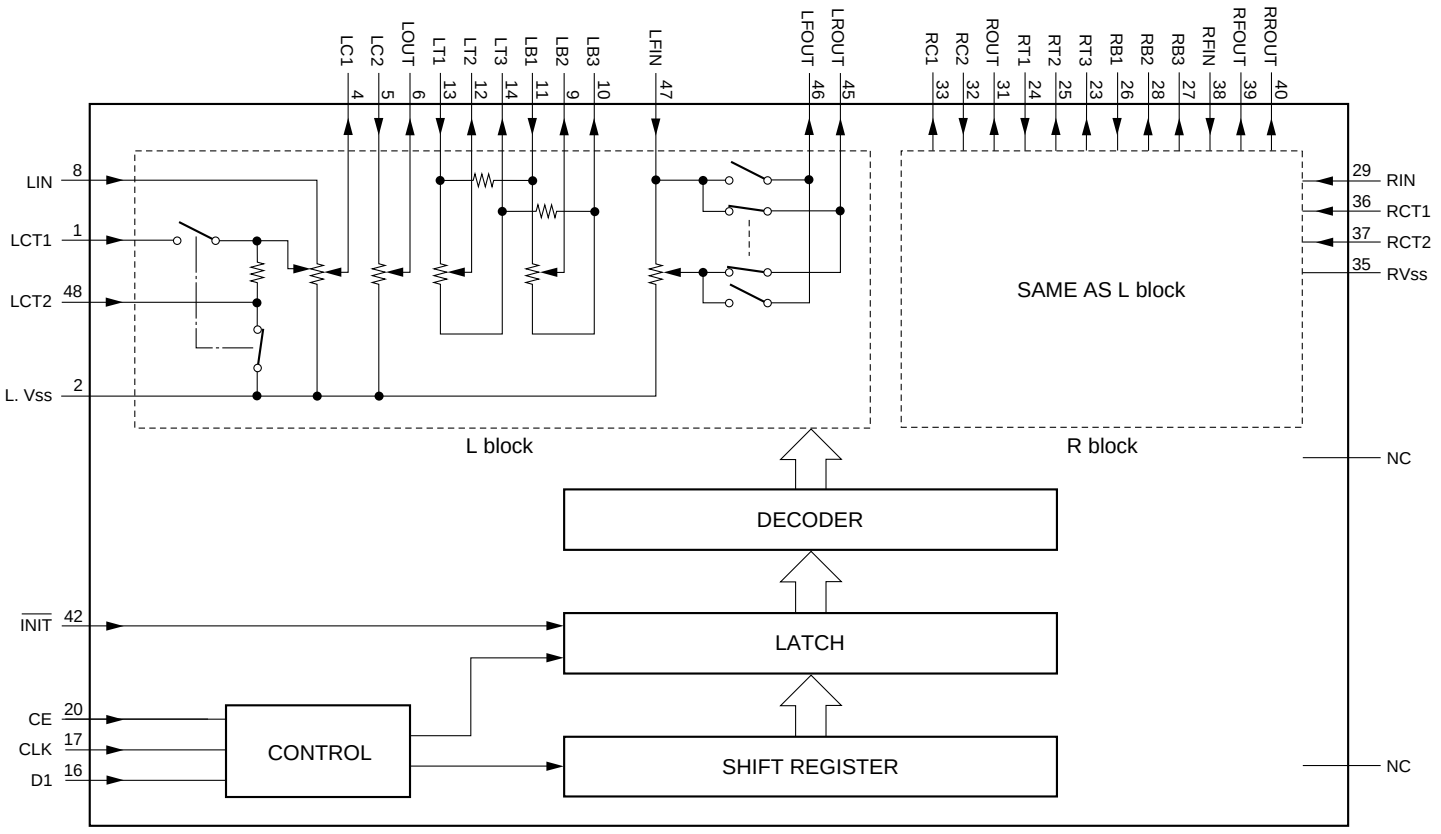
PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	LCT1	25	O	RT2
2	–	LVSS	26	I	RB1
3	–	NC	27	O	RB3
4	O	LC1	28	O	RB2
5	I	LC2	29	I	RIN
6	I	LOUT	30	–	NC
7	–	NC	31	O	ROUT
8	I	LIN	32	I	RC2
9	O	LB2	33	O	RC1
10	O	LB3	34	–	NC
11	I	LB1	35	–	RVSS
12	O	LT2	36	I	RCT1
13	I	LT1	37	I	RCT2
14	I	LT3	38	I	RFIN
15	–	GND1	39	O	RFOUT
16	I	D1	40	O	RROUT
17	I	CLK	41	–	NC
18	–	NC	42	I	$\overline{\text{INIT}}$
19	–	VDD1(NC)	43	–	VDD1
20	I	CE	44	–	NC
21	–	VDD2	45	O	LROUT
22	–	GND2	46	O	LFOUT
23	O	RT3	47	I	LFIN
24	I	RT1	48	I	LCT2

INPUT

L,R.B1 ; TONE BASS
L,R.C2 ; MAIN 1dB STEP ATTENUATER
L,R.FIN ; FADER
L,R.IN ; MAIN 5dB STEP ATTENUATER
L,R.T1 ; TONE TREBLE

OUTPUT

L,R.B2,B3 ; TONE BASS
L,R.C1 ; MAIN 5dB STEP ATTENUATER
L,R.FOUT,ROUT ; FADER
L,R.OUT ; MAIN 1dB STEP ATTENUATER
L,R.T2,T3 ; TONE TREBLE


LC1 4
LC2 5
LOUT 6
LT1 13
LT2 12
LT3 14
LB1 11
LB2 9
LB3 10
LFIN 47
LFOUT 46
LROUT 45
RC1 33
RC2 32
ROUT 31
RT1 24
RT2 25
RT3 23
RB1 26
RB2 28
RB3 27
RFIN 38
RFOUT 39
RROUT 40
LIN 8
LCT1 1
LCT2 48
L. Vss 2
29 RIN
36 RCT1
37 RCT2
35 RVss
SAME AS L block
L block
R block
NC
DECODER
INIT 42
LATCH
CE 20
CLK 17
D1 16
CONTROL
SHIFT REGISTER
NC