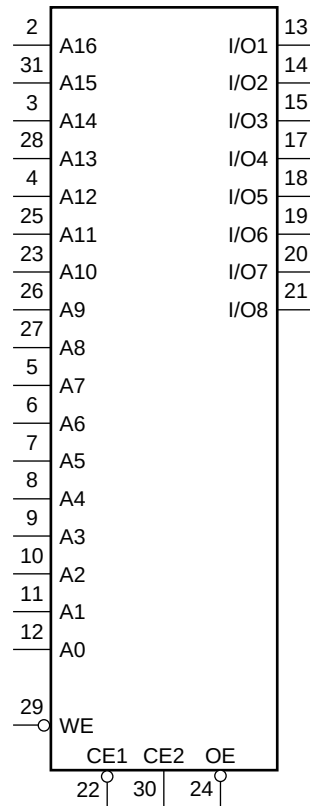
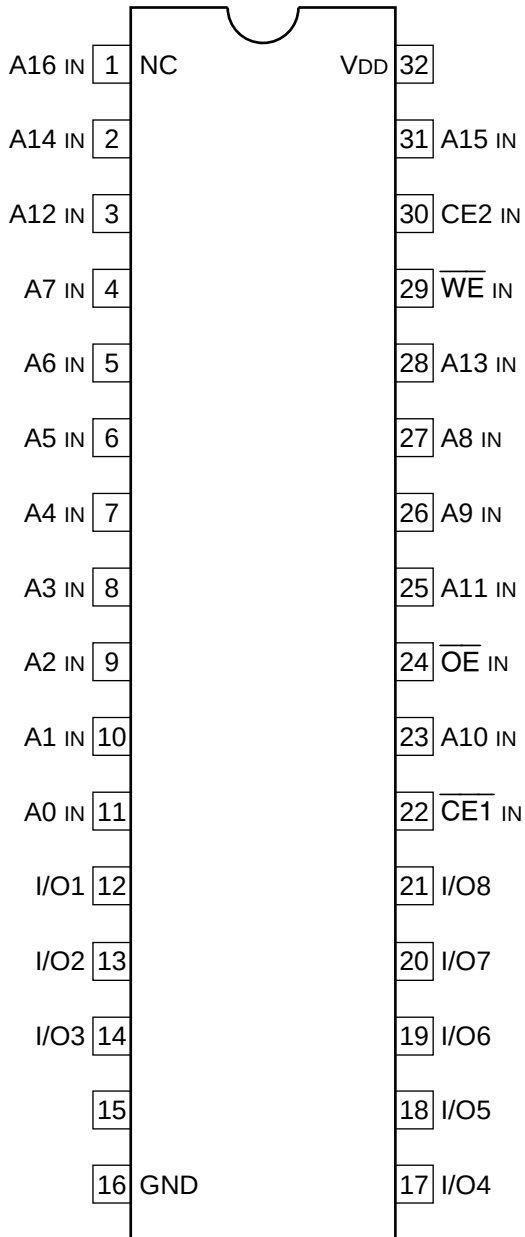


C-MOS 1 M (131,072 × 8)-BIT SRAM

—TOP VIEW—

**INPUT**

A0 - A16 : ADDRESS

CE1, CE2 : CHIP ENABLE

OE : OUTPUT ENABLE

WE : READ/WRITE ENABLE

INPUT/OUTPUT

I/O1 - I/O8 : DATA

