



# **High-Performance Dual Comparator**

## Overview

The LA6393M is a high-performance dual comparator that is capable of operating from a single power supply over a wide range of 2V to 36V. Because of its excellent input characteristics and low power, it can be very conveniently applied to multisignal parallel comparator circuits that require high-density assembly.

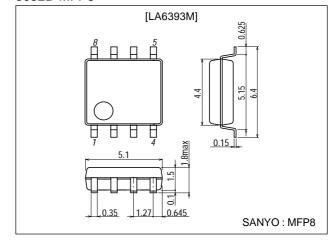
## **Features**

- Wide supply voltage range (Single supply : 2.0 to 36.0V, dual supplies : ±1.0 to 18.0V).
- Wide common-mode input voltage range (0 to  $V_{CC}$ -1.5V).
- Open collector output enabling wired OR.
- Small current drain (0.6mA) and low power.
- Mini flat package enabling compactness of sets.

## **Package Dimensions**

unit:mm

3032B-MFP8



# **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		36	V
Differential input voltage	V <sub>ID</sub>		36	V
Common-mode input voltage range	VICM		-0.3 to +36	V
Allowable power dissipation	Pd max		300	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +125	°C

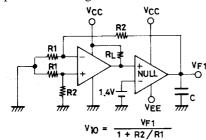
#### Operating Characteristics at Ta = 25°C, $V_{CC}=5V$

Parameter	Symbol	Conditions	Test Circuit	Ratings			Unit
				min	typ	max	Unit
Input offset voltage	V <sub>IO</sub>		1		±1	±5	mV
Input offset current	IIO		2		±5	±50	nA
Input bias current	IB		3		25	250	nA
Common-mode input voltage range	VICM			0		V <sub>CC</sub> -1.5	V
Current drain	Icc	R <sub>L</sub> =∞	4		0.6	1	mA
Voltage gain	VG	$R_L=15k\Omega$	5		200		V/mV
Response time		$V_{RL}$ =5V, $R_L$ =5.1k $\Omega$	6		1.3		μs
Output sink current	ISINK	V <sub>IN</sub> <sup>-</sup> =1V, V <sub>IN</sub> <sup>+</sup> =0V, V <sub>O</sub> ≤1.5V	7	6	16		mA
Output saturation voltage	VOL	V <sub>IN</sub> <sup>-</sup> =1V, V <sub>IN</sub> <sup>+</sup> =0V, I <sub>SINK</sub> ≤3mA	8		0.2	0.4	V
Output leakage current	ILEAK	V <sub>IN</sub> <sup>-</sup> =0V, V <sub>IN</sub> <sup>+</sup> =1V, V <sub>O</sub> =5V	9		0.1		nA

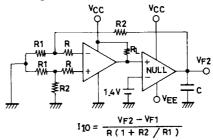
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## **Test Circuits**

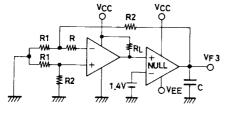
1. Input offset voltage

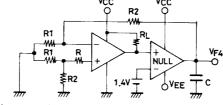


2. Input offset current



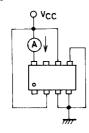
3. Input bias current



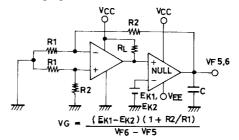


$$i_B = \frac{|V_{F3} - V_{F4}|}{2R (1 + R2/R1)}$$

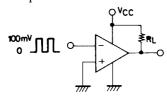
4. Current drain



5. Voltage gain



6. Response time



Vout

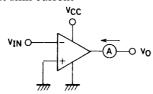
VIN

O

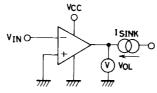
AV (over drive)

AV : Overdrive

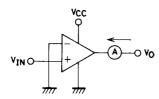
7. Output sink current



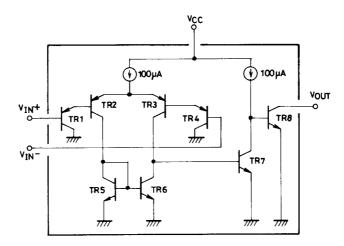
8. Output saturation voltage



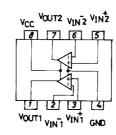
9. Output leakage current



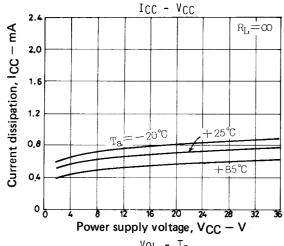
## **Equivalent Circuit**

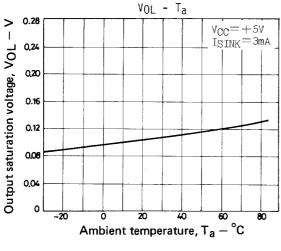


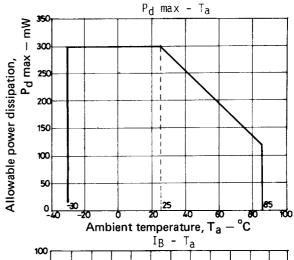
## **Pin Assignment**

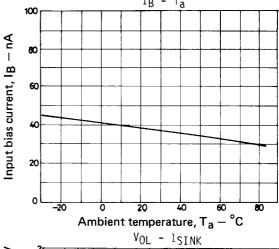


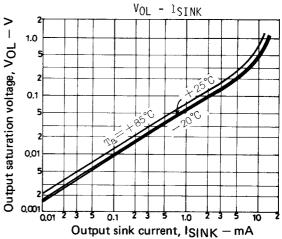
# **Main Characteristics**

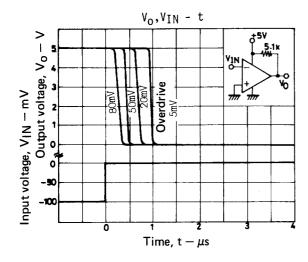


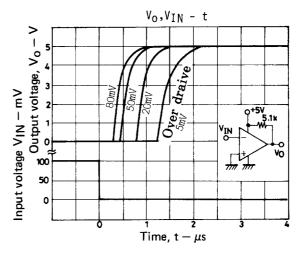




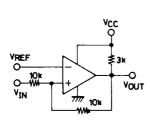




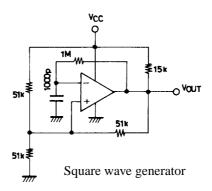




## **Sample Application Circuits**



Voltage comparator (with hysteresis)



Unit (resistance:  $\Omega$ , capacitance: F)

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