

LA5685N

Multifunction, Multi-Power Supply IC for Car Radios

Overview

The LA5685N is a multifunction, multipower supply IC developed for car radios. It has 8.5V AM output, 8.5V FM output, 8.5V common output, 5.2V microcomputer output, and 5.1V bias output, making it the ideal power supply for LA1833 and LA1887 ICs for FM/AM tuner systems.

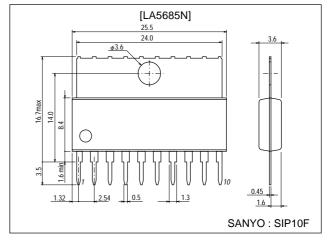
Features

- A total of five built-in outputs : V_1 =8.5V (AM), V_2 =8.5V (FM), V_3 =8.5V (common), V_4 =5.2V (microcomputer), and V_5 =5.1V.
- R ON/OFF, FM/AM switching functions.
- Minimal static current for backup (120µA typ).
- Built-in overvoltage protection circuit (V₁, V₂, and V₃ go off at 28V (typ.), V₄ and V₅ go off at 56V (typ)).
- Built-in thermal shutdouwn circuit (output goes off at Tj=170°C (typ.)).
- Built-in short protection circuit.

Package Dimensions

unit:mm

3046B-SIP10F



Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input votage	V _{CC} max1	4% duty pulse width 200ms pulse input (input pulse when output on)	75	V
	V _{CC} max2		25	V
	I1 max		80	mA
	I2 max		100	mA
Output current	I3 max		200	mA
	I4 max		50	mA
	I5 max		5	mA
Allowable power dissipation	Pd max		2.45	W
Operating temperature	Topr		-35 to +85	°C
Storage temperature	Tstg		-40 to +125	°C

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	VCC op	9V to 10.5V not regulated	9 to 16	V
B/U voltage	B/U	6V to 8.5V not regulated	6 to 16	V

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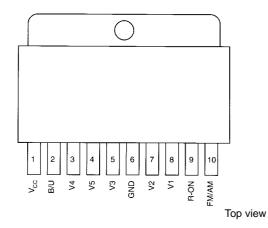
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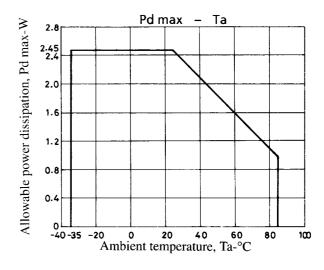
Operating Characteristics at Ta = 25°C, unless otherwise noted, V_{CC} =12V, R-ON=FM/AM=5V External 33 μ F OS capacitor connected to V1, V2, V3, and V4

Parameter	Symbol	Conditions		Ratings		
Faranietei	Symbol	Conditions	min	typ	max	Unit
Static current	I _{CC} 1	R-ON=FM/AM=0V		4.5	7.0	mA
Static current	I _{CC} 2	R-ON=FM/AM=5V		4.5	7.0	mA
	V1	FM/AM=0V, I1=20mA	7.8	8.5	9.2	V
	V2	FM/AM=5V, I2=50mA	7.8	8.5	9.2	V
Output voltage	V3	I3=100mA	7.8	8.5	9.0	V
	V4	I4=20mA	4.9	5.2	5.5	V
	V5	I5=1mA	V4-0.5		V4	V
	ΔV1 line	FM/AM=0V, 11V <v<sub>CC<15V, I1=20mA</v<sub>			50	mV
	ΔV2 line	FM/AM=5V, 11V <v<sub>CC<15V, I1=50mA</v<sub>			50	mV
Line regulation	ΔV3 line	I3=100mA, 11V <v<sub>CC<15V</v<sub>			50	mV
	ΔV4 line	I4=20mA, 11V <v<sub>CC<15V</v<sub>			50	mV
	ΔV5 line	I5=1mA, 11V <v<sub>CC<15V</v<sub>			50	mV
B/U static current	IB/U	B/U=16V, V _{CC} =0V			0.3	mA
V5 on-off TH voltage	V5 _{TH}	B/U=12V	6	7	8	V
R-ON on voltage	R-ON ON		2.5		Vcc	V
R-ON off voltage	R-ON-OFF		-0.3		+1.0	V
FM/AM on voltage	FM/AM ON		2.5		Vcc	V
FM/AM off voltage	FM/AM OFF		-0.3		+1.0	V
Input current R-ON	IR-ON	R-ON=5V			0.2	mA
Input current FM/AM	IFM/AM	FM/AM=5V			0.2	mA
	ΔV1 Load	FM/AM=0V, 1mA <i1<65ma< td=""><td></td><td></td><td>50</td><td>mV</td></i1<65ma<>			50	mV
	ΔV2 Load	FM/AM=5V, 1mA <i2<90ma< td=""><td></td><td></td><td>50</td><td>mV</td></i2<90ma<>			50	mV
Load regulation	ΔV3 Load	1mA <l3<160ma< td=""><td></td><td></td><td>100</td><td>mV</td></l3<160ma<>			100	mV
	ΔV4 Load	1mA <i4<40ma< td=""><td></td><td></td><td>50</td><td>mV</td></i4<40ma<>			50	mV
	ΔV5 Load	0.1mA <l5<2ma< td=""><td></td><td></td><td>200</td><td>mV</td></l5<2ma<>			200	mV
	Rr1	FM/AM=0V, f=120Hz, I1=20mA	40*			dB
	Rr2	FM/AM=5V, f=120Hz, I2=50mA	40*			dB
Ripple regulation	Rr3	f=120Hz, I3=100mA	40*			dB
	Rr4	f=120Hz, I4=20mA	40*			dB
	Rr5	f=120Hz, I5=1mA	40*			dB

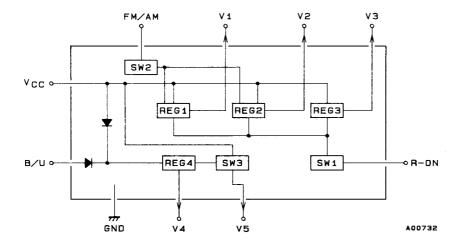
Note: * indicates design guaranteed value.

Pin Assignment



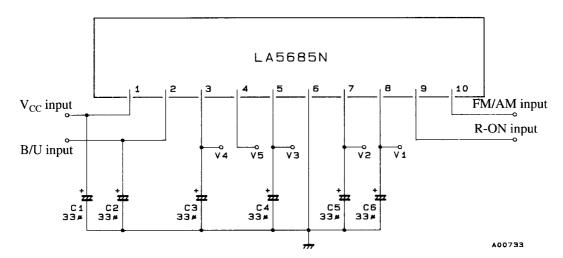


Block Diagram



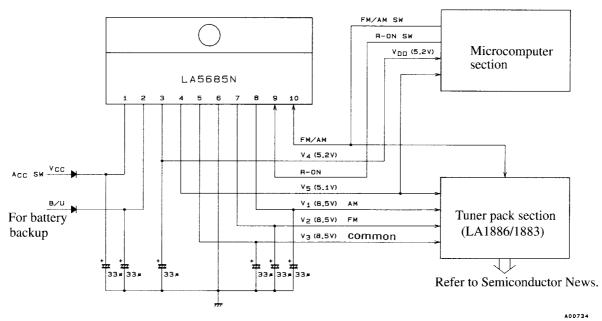
Pin	Name	Pin	Name
1	VCC	6	GND
2	B/U	7	V2
3	V4	8	V1
4	V5	9	R-ON
5	V3	10	FM/AM

Test Circuit



Unit (capacitance: F)

Sample Application Circuit



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Input/Output Table

Inputs			Outputs					
ACC	B/U	R-ON	FM/AM	V1	V2	V3	V4	V5
L	L	*	*	L	L	L	L	L
L	Н	*	*	L	L	L	Н	L
Н	*	Н	L	Н	L	Н	Н	Н
Н	*	Н	Н	L	Н	Н	Н	Н
Н	*	L	*	L	L	L	Н	Н

- Negative voltages are not to be applied to these pins.
- Always use input/output capacitors (instead of for V5). (We recommended OS capacitors with good characteristics at low temperature.)
- Built-in overvoltage protection circuit (V1, V2, and V3 go off at 28V (typ.), V4 and V5 go off at 56V (typ.))
- Built-in thermal shutdown circuit (output goes off at Tj=170°C (typ.))
- Built-in short protection circuit.

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