

**SANYO**

No.2277A

**LA3607**

7-Band Graphic Equalizer

**Features**

- 7-band graphic equalizer for one channel can be formed easily by externally connecting capacitors and variable resistors which fix fo (resonance frequency).
- Series connection of the LA3607 makes multiband available.
- Boost, cut amount can be varied by external resistors.
- Highly stable to capacitive load

**Maximum Ratings at Ta=25°C**

			unit
Maximum Supply Voltage	V <sub>CC</sub> <sup>max</sup>	20	V
Allowable Power Dissipation	Pdmax	300	mW
Operating Temperature	T <sub>opr</sub>	-20 to +75	°C
Storage Temperature	T <sub>stg</sub>	-40 to +125	°C

**Operating Conditions at Ta=25°C**

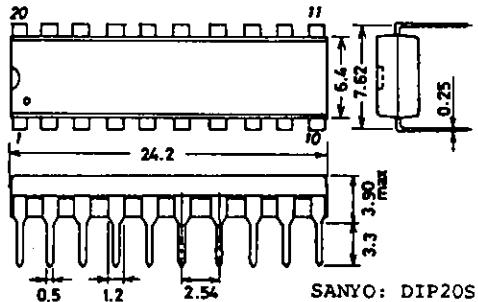
		unit
Recommended Supply Voltage	V <sub>CC</sub>	8 V
Operating Voltage Range	V <sub>CCop</sub>	5 to 15 V

**Operating Characteristics at Ta=25°C, V<sub>CC</sub>=8V, R<sub>L</sub>=10kohms, R<sub>g</sub>=600ohms,**

		See specified Test Circuit.	min	typ	max	unit
Quiescent Current	I <sub>cc0</sub>	Quiescent		7	9	mA
Voltage Gain	VG	f=1kHz, V <sub>IN</sub> =-10dB at all flat mode	-3.8	-0.8	2.2	dB
Boost Amount	BOOST	f=60Hz f=150Hz f=400Hz f=1kHz f=2.5kHz f=6kHz f=15kHz	10 10 10 10 10 10 10	12 12 12 12 12 12 12	14 14 14 14 14 14 14	dB dB dB dB dB dB dB

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**Package Dimensions**  
(unit: mm)  
3021B

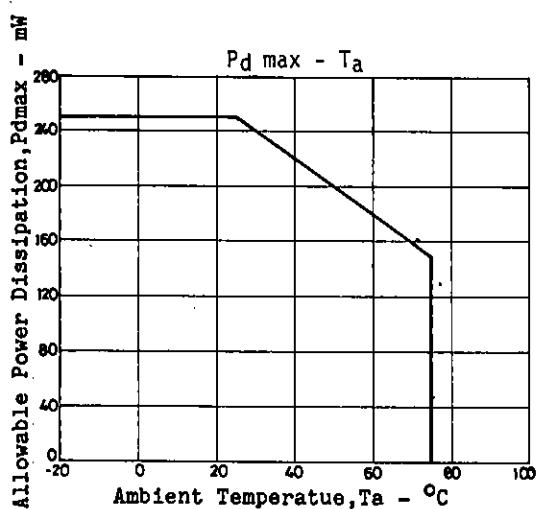


SANYO: DIP20S

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Cut Amount	CUT	f=60Hz f=150Hz f=400Hz f=1kHz f=2.5kHz f=6kHz f=15kHz	$V_o = -10\text{dB}$ is taken as 0dB at all flat mode at f=1kHz.	min -14 -14 -14 -14 -14 -14 -14	typ -12 -12 -12 -12 -12 -12 -12	max -10 -10 -10 -10 -10 -10 -10	unit dB dB dB dB dB dB dB
Total Harmonic Distortion	THD	$f=1\text{kHz}, V_o = 1.0\text{V}$ at all flat mode input				0.02	0.1 %
Output Noise Voltage	$V_{NO}$	All flat, input short, B.P.F., 10Hz to 30kHz				7	40 $\mu\text{V}$

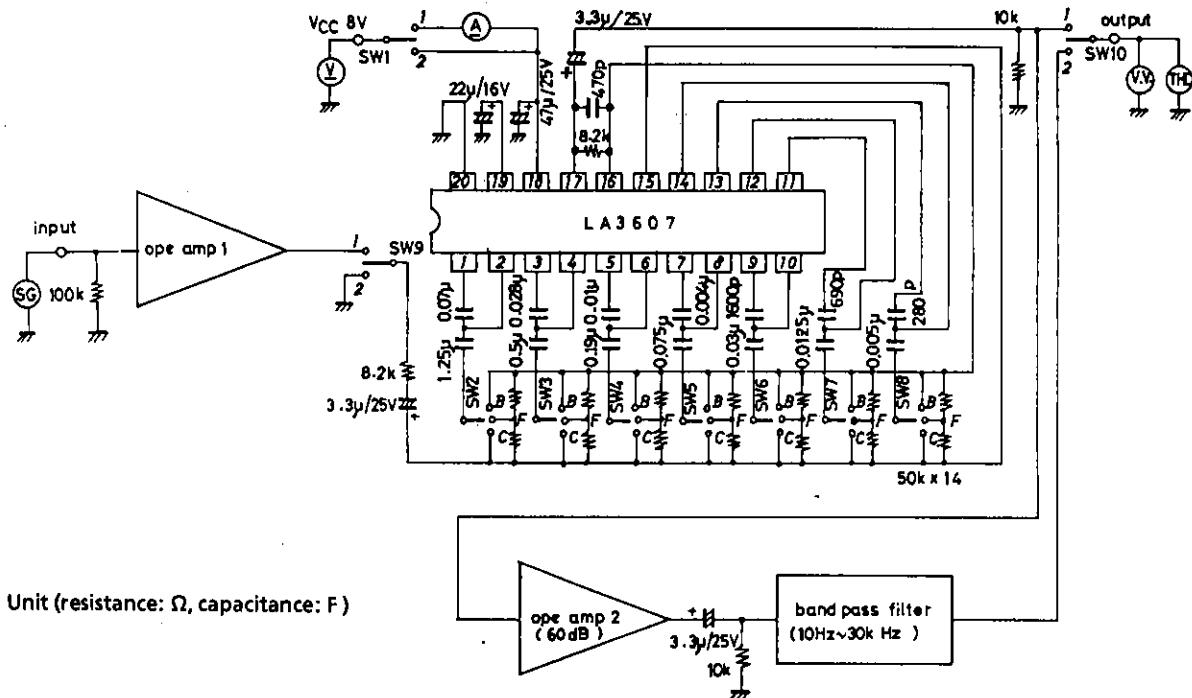


Test Method:  $V_{CC} = 8\text{V}$ ,  $R_L = 10\text{kohms}$ ,  $R_g = 600\text{ohms}$

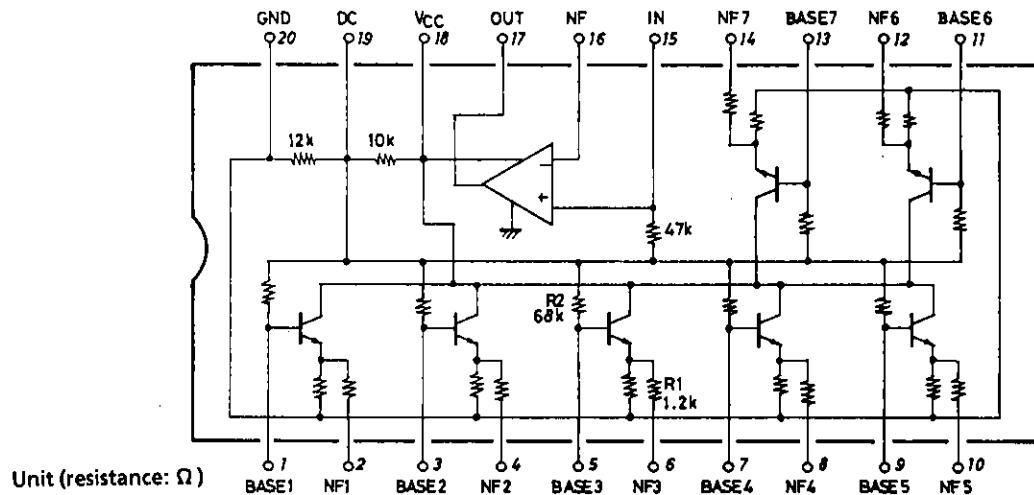
Item	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10	Conditions
Icc0	1	F	F	F	F	F	F	2	1		
VG	2	F	F	F	F	F	F	1	1	$f = 1\text{kHz}$ $V_{IN} = -10\text{dB}$	
BOOST1	2	B	F	F	F	F	F	1	1	$f = 60\text{Hz}$	
BOOST2	2	F	B	F	F	F	F	1	1	$f = 150\text{Hz}$	
BOOST3	2	F	F	B	F	F	F	1	1	$f = 400\text{Hz}$	
BOOST4	2	F	F	F	B	F	F	1	1	$f = 1\text{kHz}$	
BOOST5	2	F	F	F	F	B	F	1	1	$f = 2.5\text{kHz}$	
BOOST6	2	F	F	F	F	F	B	1	1	$f = 6\text{kHz}$	
BOOST7	2	F	F	F	F	F	F	B	1	$f = 15\text{kHz}$	
CUT1	2	C	F	F	F	F	F	1	1	$f = 60\text{Hz}$	
CUT2	2	F	C	F	F	F	F	1	1	$f = 150\text{Hz}$	
CUT3	2	F	F	C	F	F	F	1	1	$f = 400\text{Hz}$	
CUT4	2	F	F	F	C	F	F	1	1	$f = 1\text{kHz}$	
CUT5	2	F	F	F	F	C	F	1	1	$f = 2.5\text{kHz}$	
CUT6	2	F	F	F	F	F	C	1	1	$f = 6\text{kHz}$	
CUT7	2	F	F	F	F	F	F	C	1	$f = 15\text{kHz}$	
THD	2	F	F	F	F	F	F	1	1	$f = 1\text{kHz}, V_o = 1.0\text{V}$	
VNO	2	F	F	F	F	F	F	2	2		

# LA3607

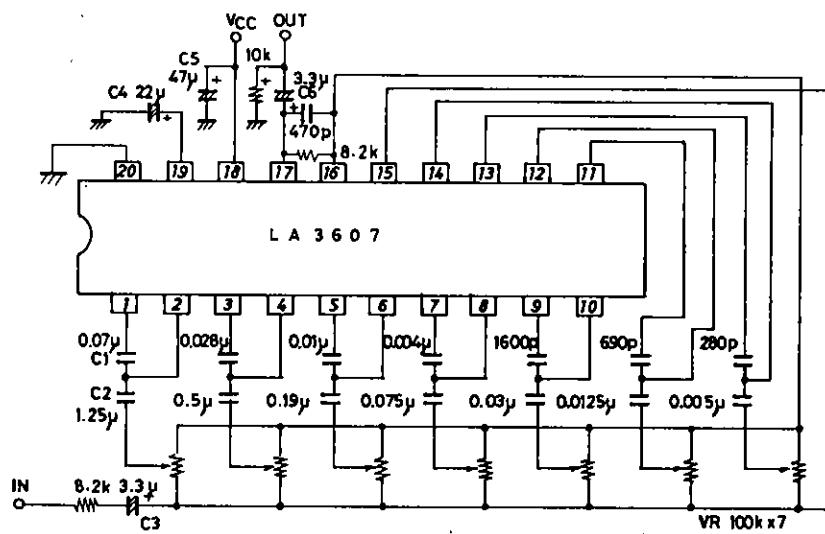
## Test Circuit



## Equivalent Circuit Block Diagram

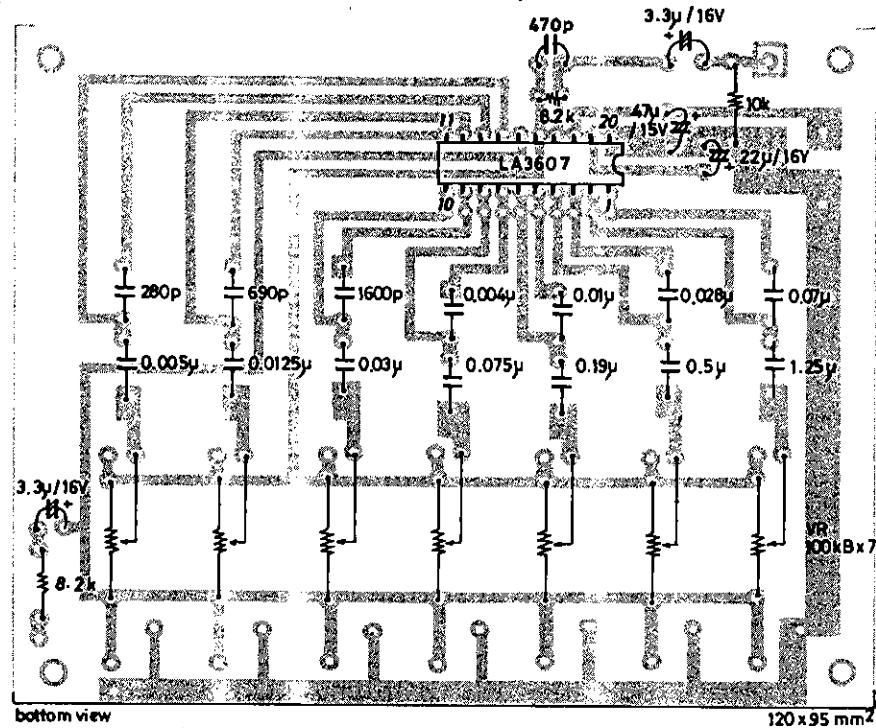


## Sample Application Circuit



Unit (resistance: Ω, capacitance: F)

## Sample Printed Circuit Pattern (Cu-foiled side)

Unit (resistance:  $\Omega$ , capacitance: F) $f_o$ (resonance frequency)In the sample application circuit,  $f_o$  for each of 7 bands is set as follows: $f_o = 60\text{Hz}, 150\text{Hz}, 400\text{Hz}, 1\text{kHz}, 2.5\text{kHz}, 6\text{kHz}, 15\text{kHz}$  $f_o$  is calculated using the following formula.

$$f_o = \frac{1}{2\pi / C_1 \cdot C_2 \cdot R_1 \cdot R_2}$$

Q (quality factor)

Q is calculated using the following formula.

$$Q = \sqrt{\frac{C_1 \cdot R_2}{C_2 \cdot R_1}}$$

When Q is increased, the frequency band affected by the resonance circuit is narrowed and a clear distinction between this band and adjacent band is provided, but the frequency response swells greatly at all boost mode and the peak of the composite frequency is lowered. The above must be considered to fix  $C_1$ ,  $C_2$ .

## Description of external parts

C1, C2 : Capacitors used to fix  $f_o$  (resonance frequency)

C3 : Input capacitor. Decreasing the capacitor value lowers the frequency response at low frequencies.

C4 : Decoupling capacitor. Decreasing the capacitor value makes the effect of power supply stronger, whereby ripple is liable to occur.

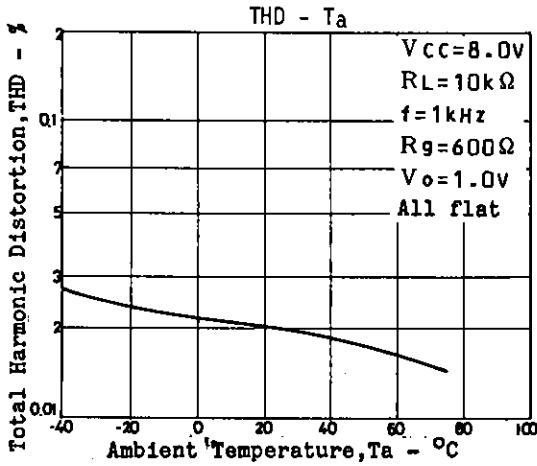
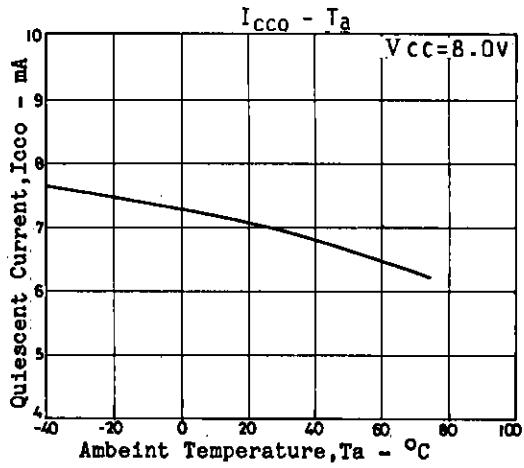
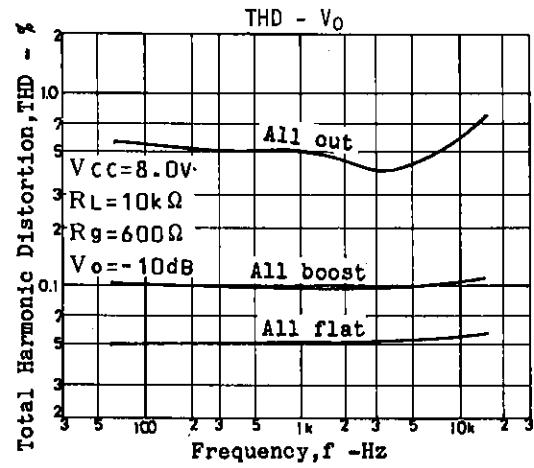
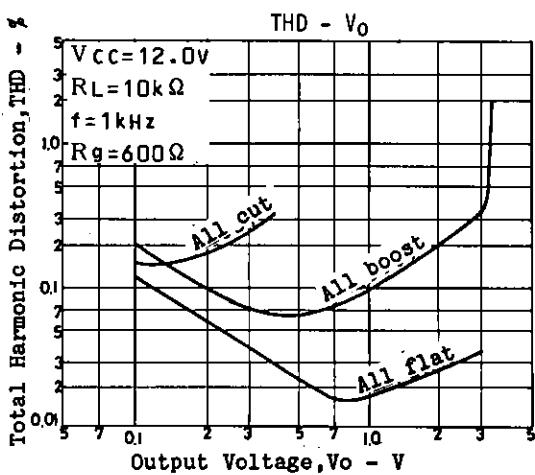
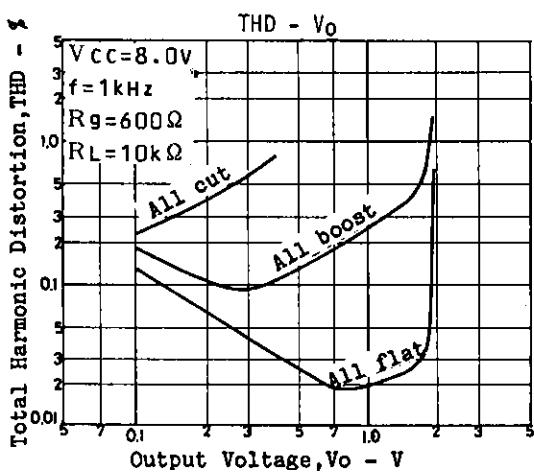
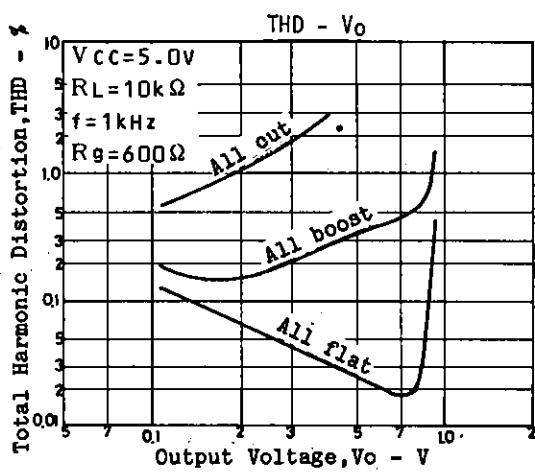
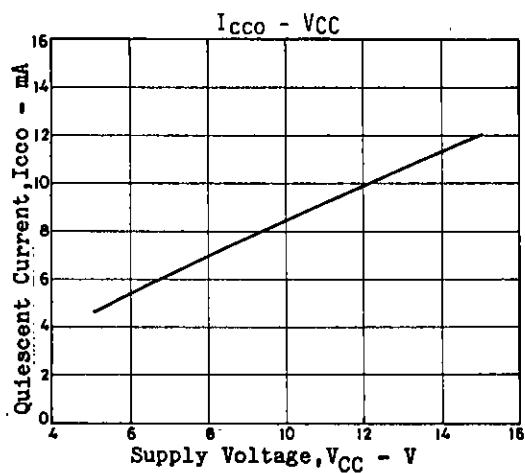
C5 : Power capacitor

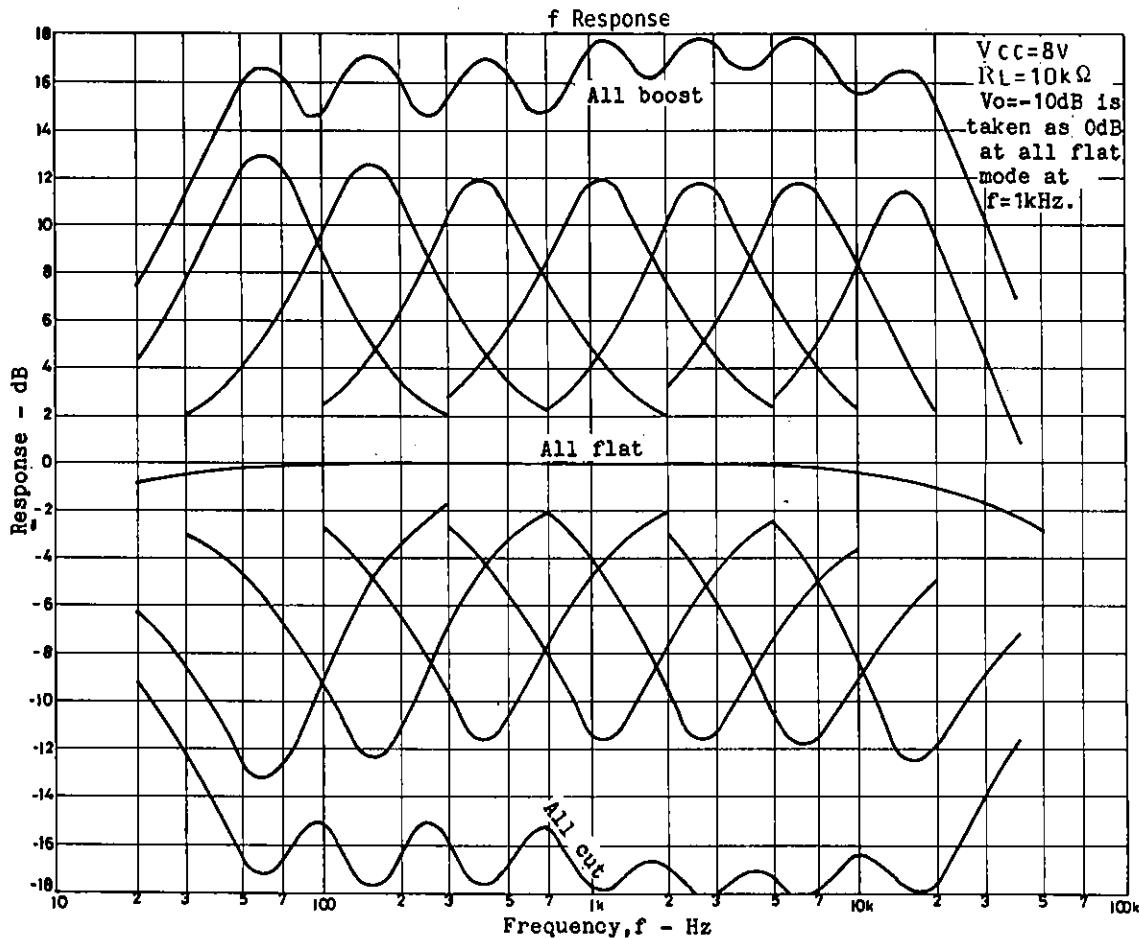
C6 : Output capacitor. Decreasing the capacitor value lowers the frequency response at low frequencies.

## Proper cares in using IC

- Maximum supply voltage  $V_{CC\max}$  20V must not be exceeded. The operating voltage is in the range of 5 to 15V.

- Application of power with the pin-to-pin spaces shorted causes breakdown or deterioration of the IC to occur. When mounting the IC on the board or applying power, make sure that the pin-to-pin spaces are not shorted with solder, etc.





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