



No. C1141C



LA1232

Monolithic Linear Integrated Circuit

FM IF SYSTEM

Functions

1. IF amp, limiter
2. Quadrature detector
3. AF preamp
4. Muting at weak input
5. Muting during detuning
6. Signal meter drive output
7. AFC, tuning meter drive output
8. Delay AGC output
9. Muting drive voltage inverter
10. IF amp stop circuit

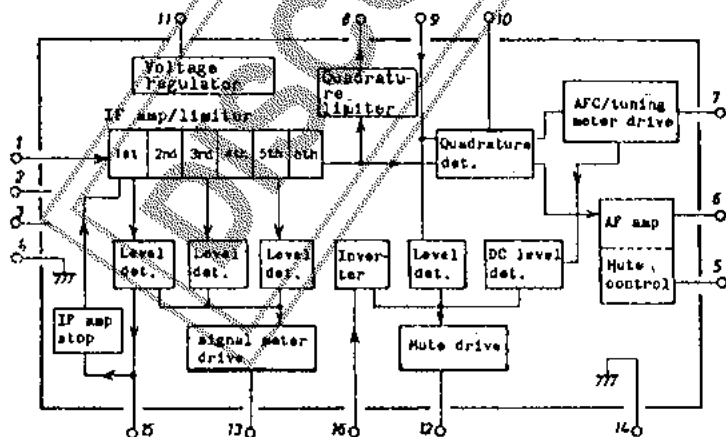
Features

1. High limiting sensitivity: 18uV typ
2. Low distortion: 0.05% typ. (Depends on the linearity of the phase characteristic of the phase shifter.)
3. High demodulation output: 330mVrms typ.
4. High S/N: 84.0dB typ.
5. Pop noise reduced muting during detuning
6. Signal meter drive output in proportion to input signal level (dB)
7. Detuning muting band having good symmetry
8. Tuning meter drive output having wide deflection
9. Delay AGC drive output for front end
10. On-chip voltage regulator: operating voltage=9 to 14V
11. Excellent interstation muting characteristic
12. Pin compatible with LA1231N

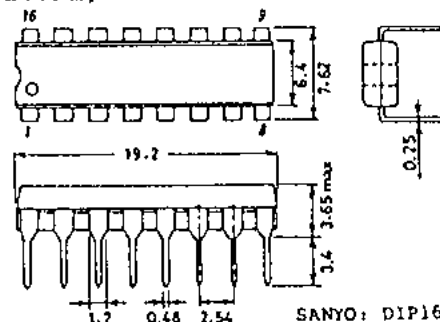
General Description

The LA1232 is a high integration IC that is intended for use in FM IF systems and contains almost all the functions required for the IF stage of an FM tuner. The equivalent circuit block diagram is shown below. A sample application circuit is also shown below together with its peripheral circuit. The IF amplifier/limiter stage consists of 6 stages of double-ended differential amplifier having good AMR characteristic, and a signal meter drive stage coupled in parallel with the amplifier/limiter stage is composed of 3 stages of level detector and a drive circuit for extending the linearity range.

Equivalent Circuit Block Diagram



Case Outline 3006B-D16IC
(unit:mm)



These specifications are subject to change without notice.

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The FM detector stage consists of a double-balanced quadrature detector to which a low frequency preamplifier and a muting controller are attached.

The muting drive stage consists of a level detector to detect the S/N ratio of the carrier when the input signal is weak and a circuit to detect the DC output of the S curve supplied from the FM detector during detuning and a drive circuit, and operates to reduce interstation noise and pop noise caused by muting during detuning.

Further, a voltage inverter is contained so that muting can be turned ON at any input signal level. The output of the voltage inverter is connected to the muting drive output pin. Muting can be turned ON/OFF by externally applying the control voltage to the input pin of the voltage inverter. For the control voltage, the signal meter drive output is suited.

The AFC output and tuning meter drive stage are of current type. The AFC sensitivity and muting bandwidth during detuning can be adjusted by an external resistor.

The IF amplifier stop circuit, being a circuit to stop the FM IF amplifier at the AM reception mode, makes it possible to reduce pop noise caused by FM-AM reception mode switchover.

Maximum Ratings at Ta=25°C

			unit
Maximum Supply Voltage	V _{CC} max	Pin 11	16 V
Maximum Input Voltage	V _I	Pin 1-2	±1 Vp-p
Maximum Current Dissipation	I _{CC}	Pin 11	40 mA
Maximum Flow-in Current	I ₁₅	Pin 15	1 mA
	I ₁₆	Pin 16	1 mA
Maximum Flow-out Current	I ₁₀	Pin 10	2 mA
	I ₁₂	Pin 12	2 mA
	I ₁₃	Pin 13	2 mA
	I ₁₅	Pin 15	2 mA
Allowable Power Dissipation	Pdmax		650 mW
Operating Temperature	T _{opg}		-20 to +70 °C
Storage Temperature	T _{stg}		-20 to +125 °C

Operating Conditions at Ta=25°C

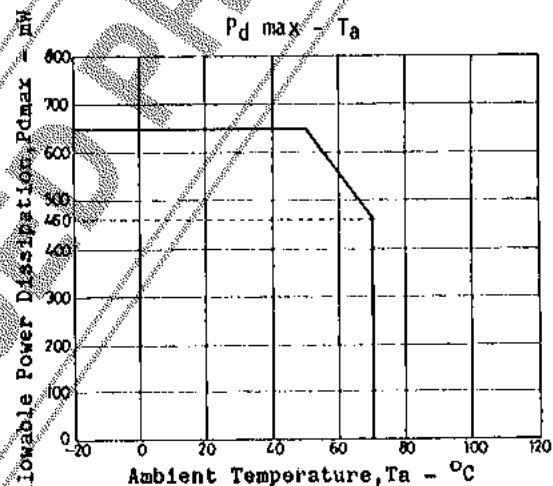
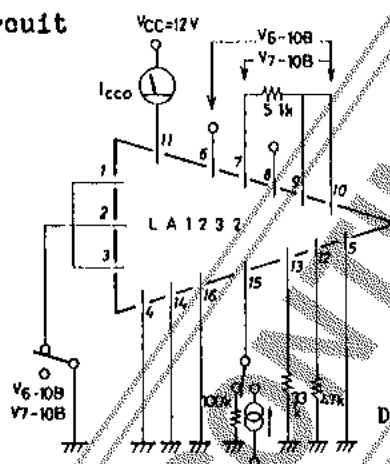
		unit
Recommended Operating Voltage	V _{CC}	12 V

Operating Characteristics at Ta=25°C, V_{CC}=12V, f=10.7MHz

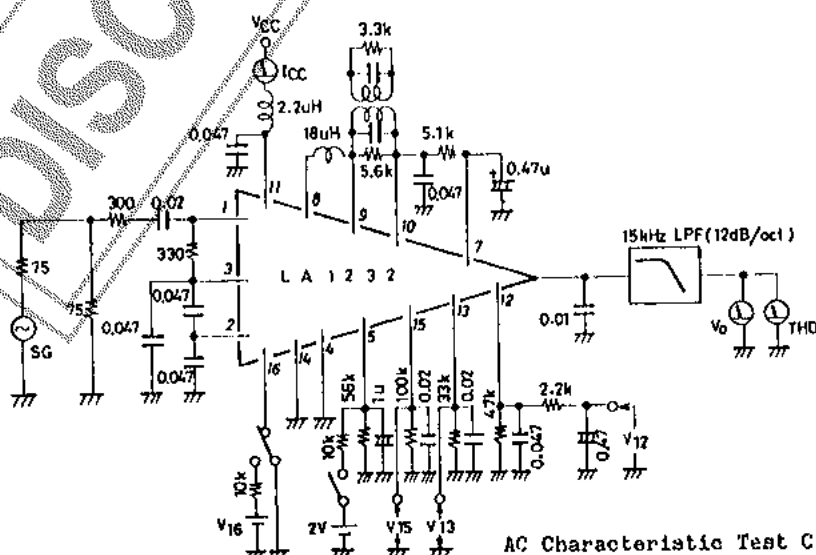
			min	typ	max	unit
Quiescent Current	I _{ceo}	Quiescent		22	30	mA
Current Dissipation	I _{CC}	Vin=100dBu		26.5	33	mA
Demodulation Output	V _o	Vin=100dBu, 400Hz 100%mod	240	330	460	mVrms
S/N		Vin=100dBu, 400Hz 100%mod	78	84		dB
Input Limiting Voltage	Vin(Lim)	V _o 3dB down, 400Hz 100%mod		25	31	dB
Total Harmonic Distortion	THD	Vin=100dBu, 400Hz 100%mod		0.05	0.3	%
Muting Sensitivity	Vin(mute)	V12=1.4V	23	29	35	dB
Muting Attenuation	Mute(att)	V5=2V, Vin=100dBu, 400Hz 100%mod	60	65		dB
Muting Bandwidth	BW(mute)	Vin=100dBu, V12=1.4V	140	220	370	kHz
AM Rejection	AMR	Vin=100dBu, FM 400Hz 100%mod	45	60		dB
		AM 1kHz 30%mod				
Muting Drive Output	V ₁₂	Quiescent	4.0	4.9	6.0	V
		Vin=100dBu	0	0	0.3	V
Signal Meter Drive Output	V ₁₃	Quiescent	0	0	0.1	V
		Vin=70dBu	1.9	3.0	4.2	V
		Vin=100dBu	4.5	5.5		V

			min	typ	max	unit
AGC Output	V ₁₅	Quiescent	4.2	5.0	5.5	V
		Vin=100dBu	0	0	0.5	V
IF OFF Current	I ₁₅ (off)	Quiescent, V ₈₋₁₀ ≤ 20mV	10	35	60	uA
Muting Operation Level	V ₁₆ (mute)	Vin=100dBu, V ₁₂ =1.4V	0.7	0.84	1.0	V
Offset Voltage	V ₆₋₁₀ B	Quiescent, Pin6-10	-0.5	0	+0.5	V
	V ₇₋₁₀ B	Quiescent, Pin7-10, R ₇₋₁₀ =5.1kohm	-0.25	0	+0.25	V
Pin Voltage	V ₁	Quiescent		2.6		V
	V ₂	Quiescent		2.6		V
	V ₃	Quiescent		2.6		V
	V ₆	Quiescent		5.6		V
	V ₇	Quiescent		5.6		V
	V ₈	Quiescent		5.4		V
	V ₁₀	Quiescent		5.6		V
	V ₁₂	Quiescent		4.9		V
	V ₁₃	Quiescent		0		V
	V ₁₅	Quiescent		5.0		V

Test Circuit

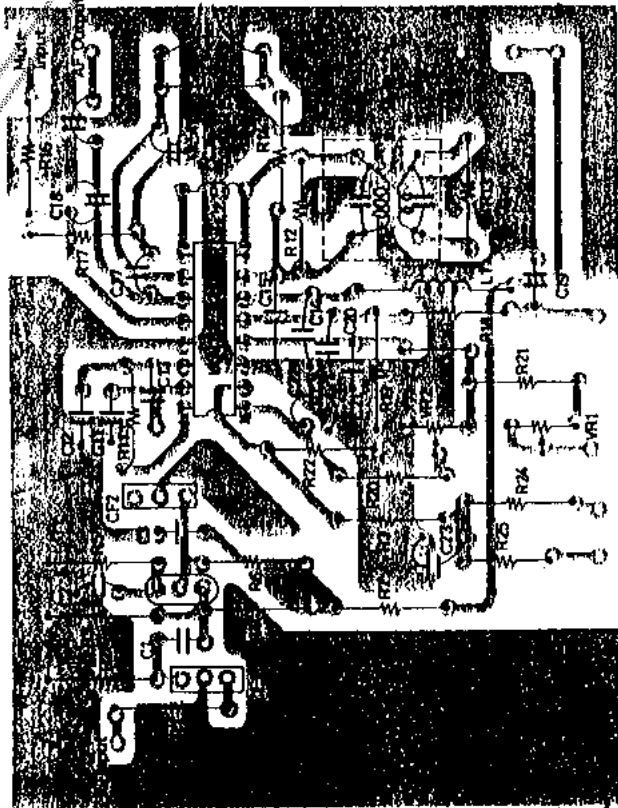
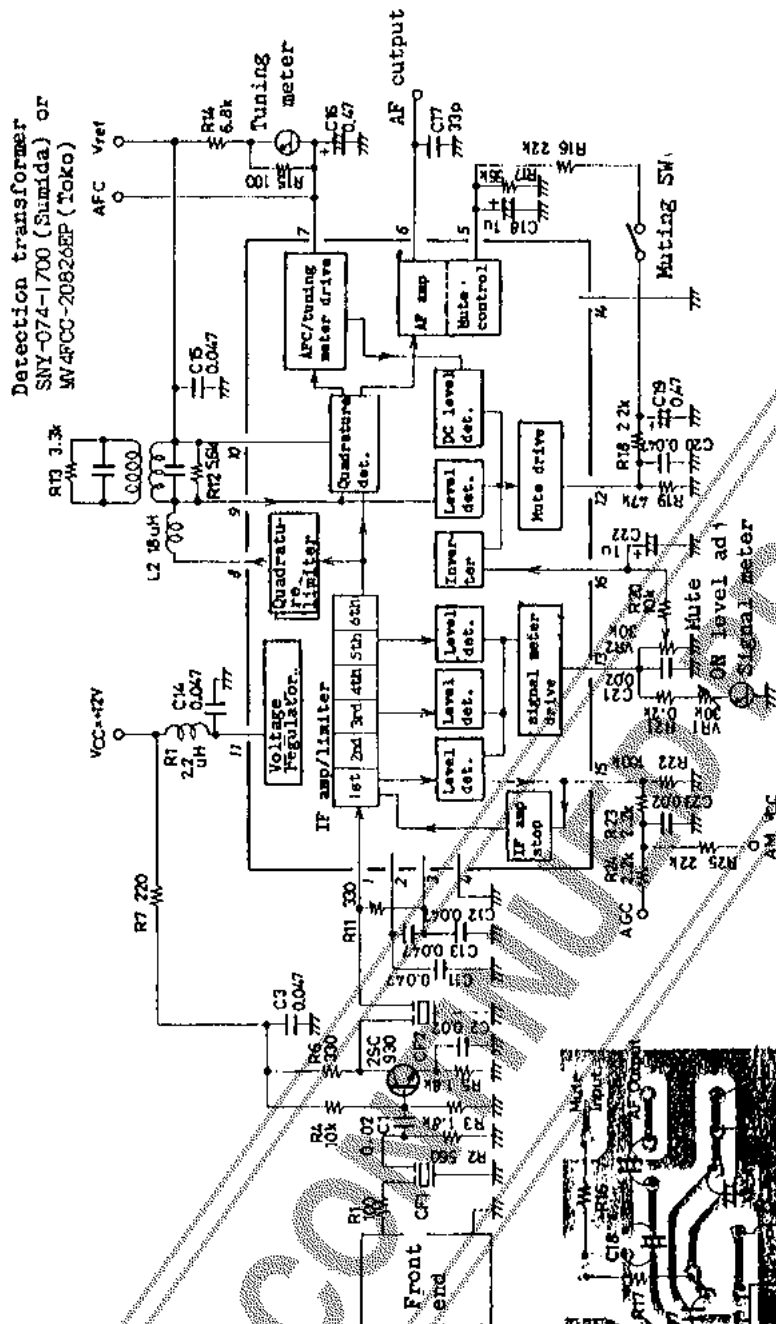


DC Characteristic Test Circuit



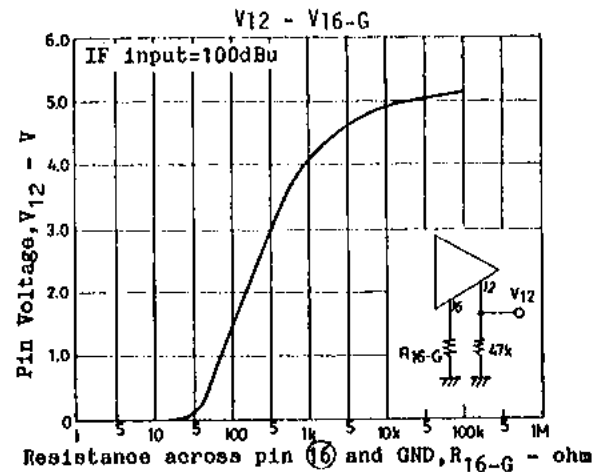
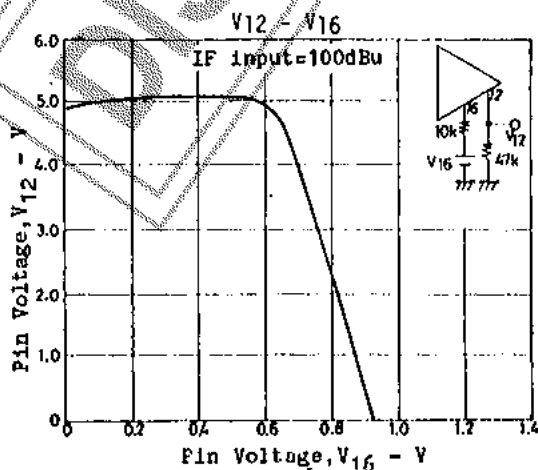
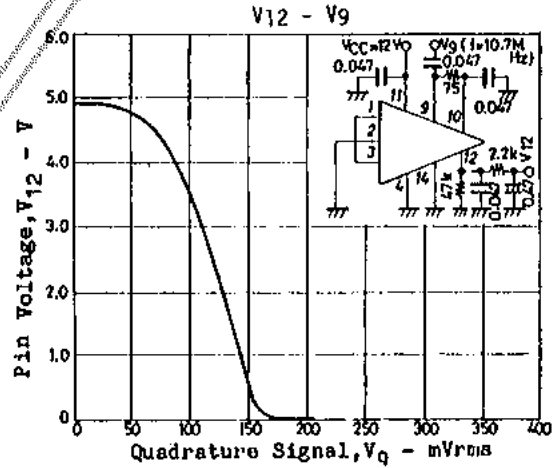
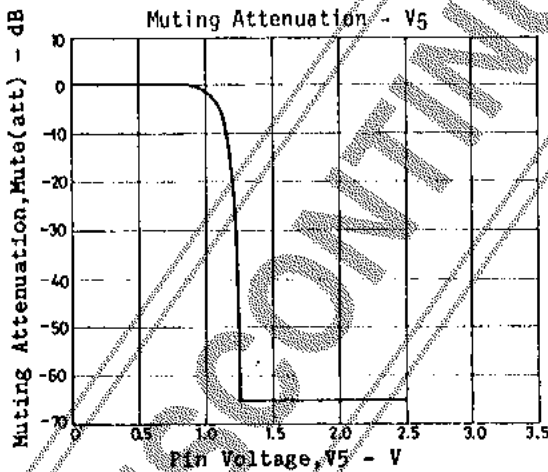
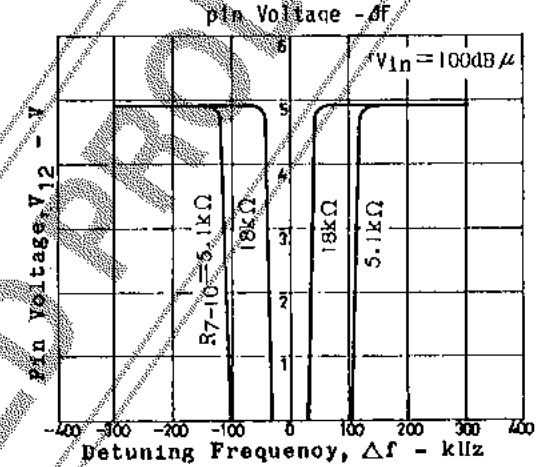
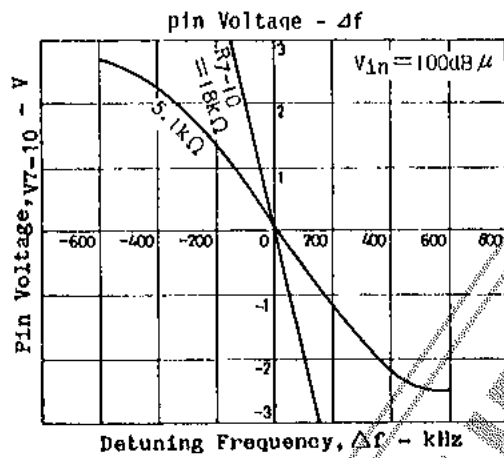
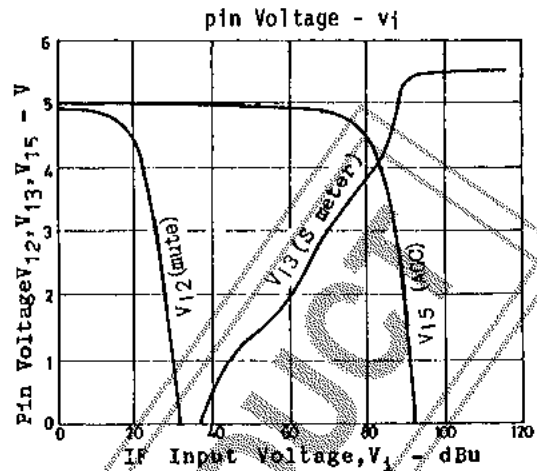
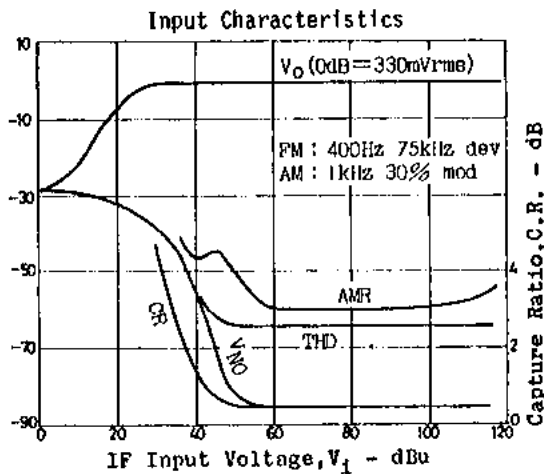
AC Characteristic Test Circuit

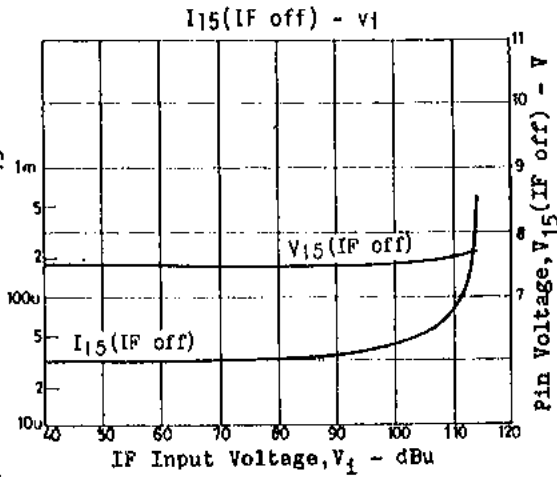
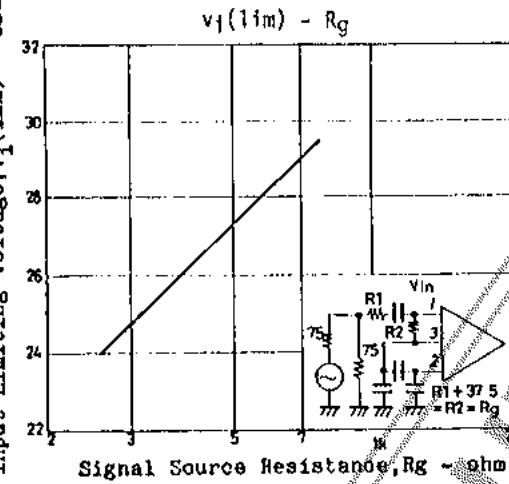
Sample Application Circuit



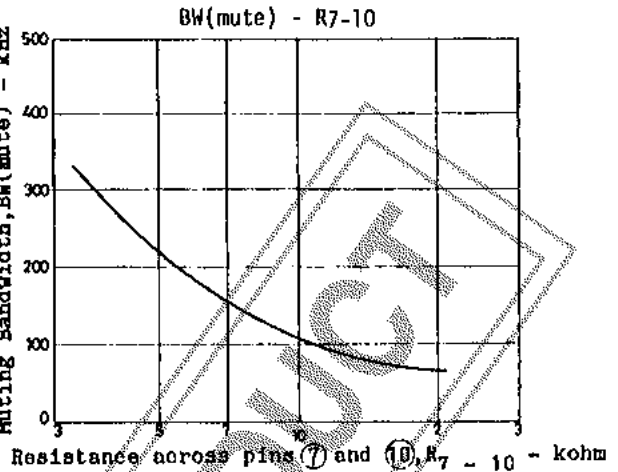
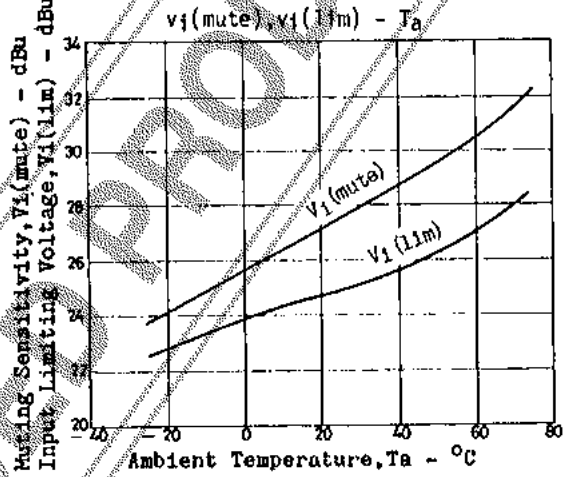
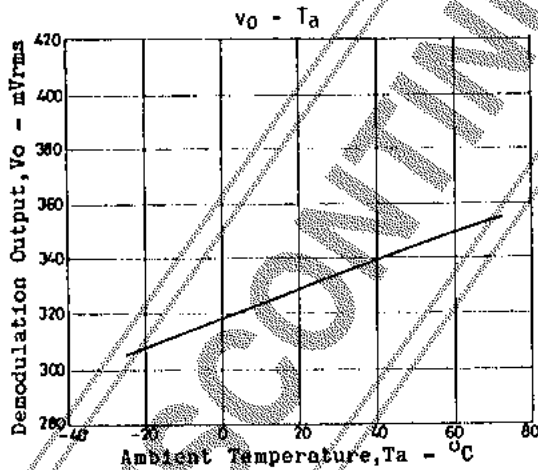
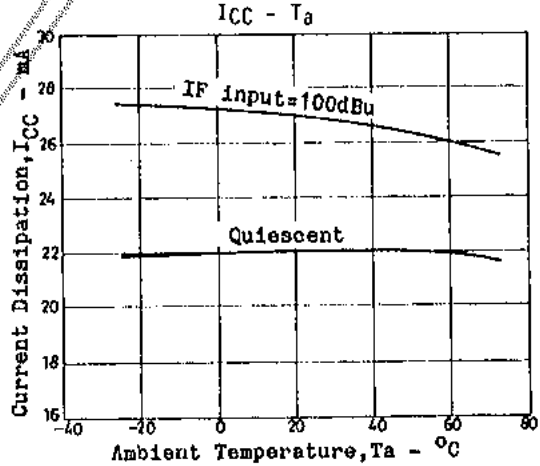
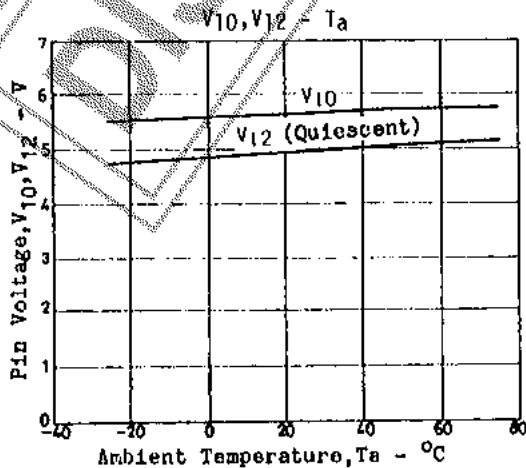
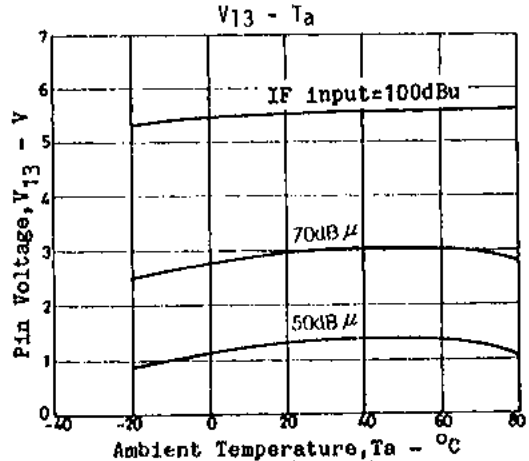
Sample Printed Circuit Pattern (70x95mm², Cu-folled area)

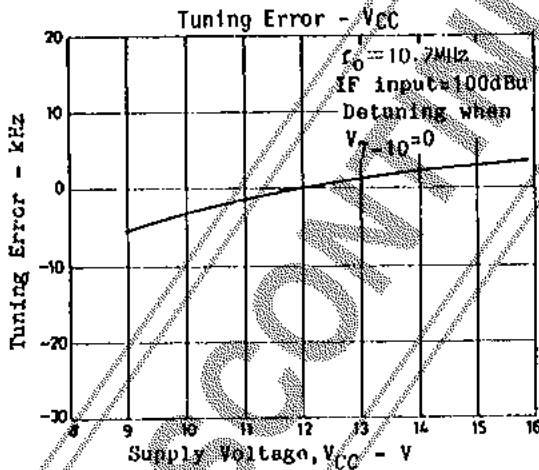
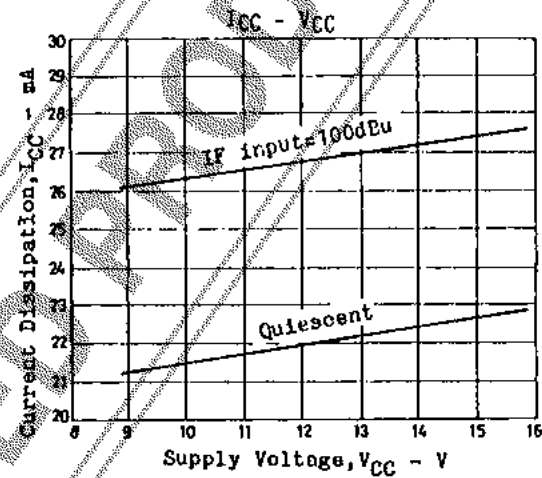
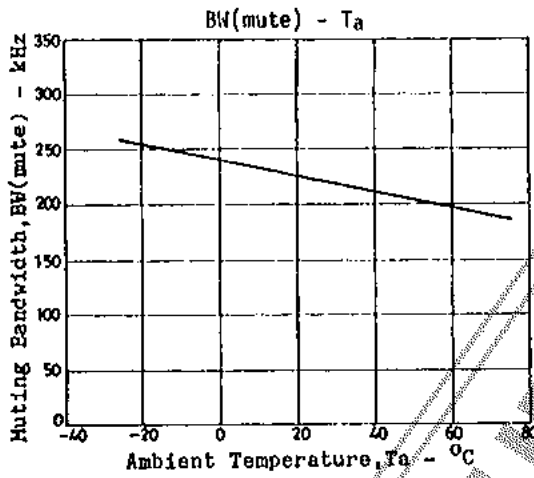
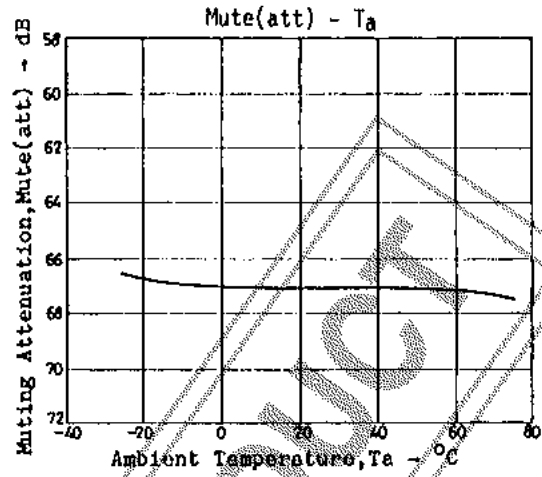
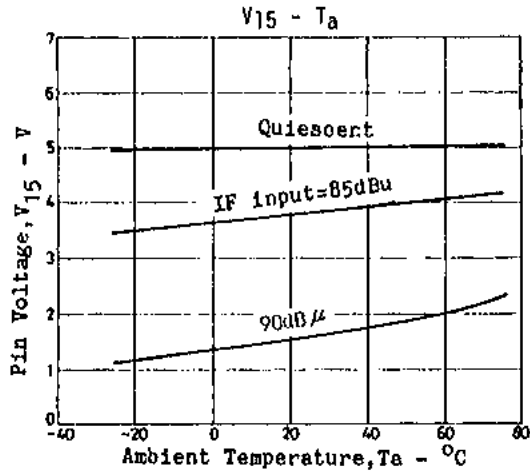
Demodulation Output V_0 , Distortion THD,
AM Rejection AMR, Output Noise Voltage V_N



IF Amp Stop Current, I_{15} (IF off) - AInput Limiting Voltage, V_i (lim) - dBu

Muting Bandwidth, BW(mute) - kHz

Muting Sensitivity, V_i (mute) - dBuDemodulation Output, V_o - mVrmsCurrent Dissipation, I_{CC} - mAPin Voltage, V_{10} , V_{12} - VPin Voltage, V_{13} - V



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