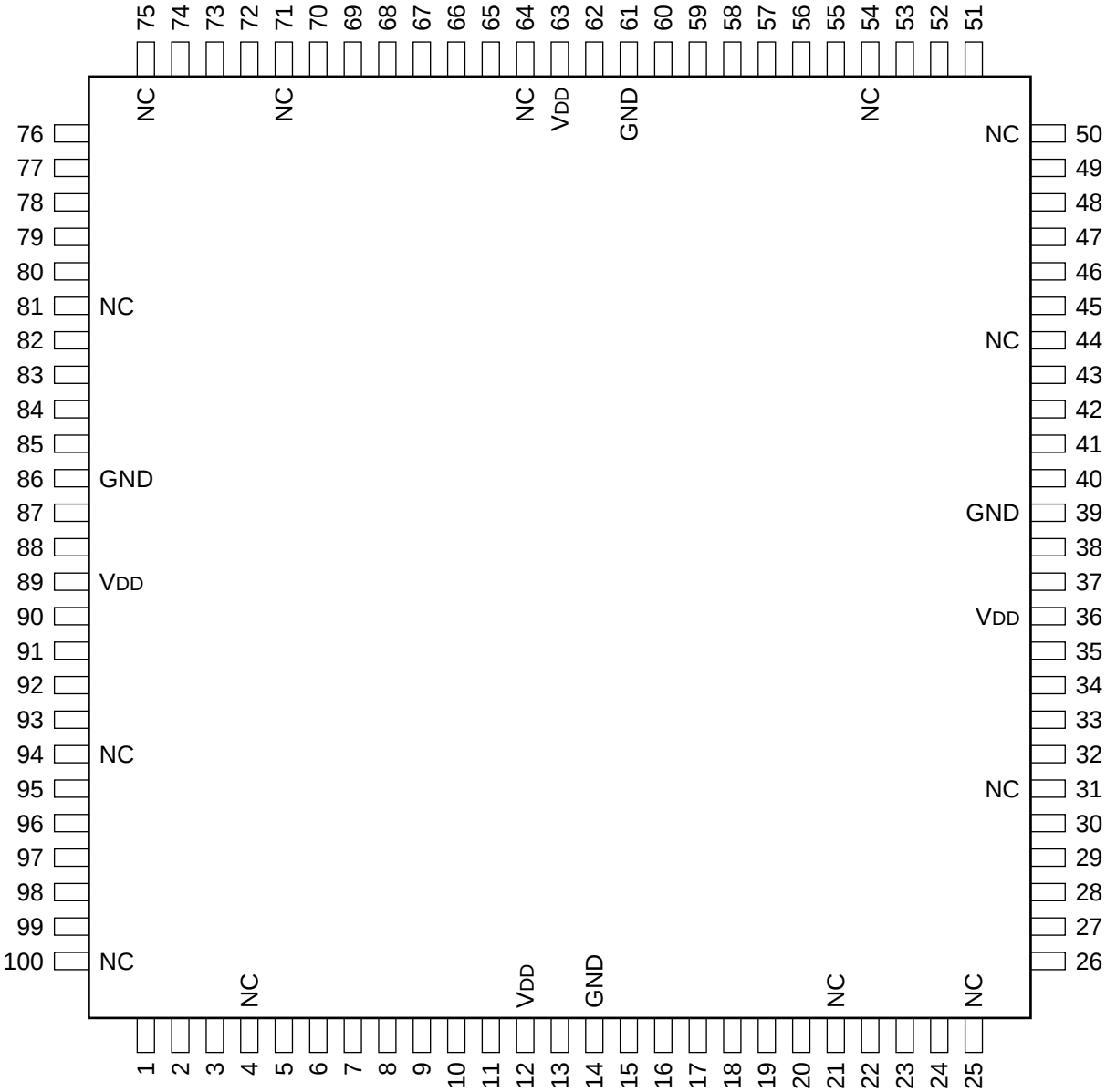


C-MOS HIGH DENSITY PROGRAMMABLE LOGIC

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	I/O57	26	I/O	I/O7	51	I/O	I/O25	76	I/O	I/O39
2	I/O	I/O58	27	I/O	I/O8	52	I/O	I/O26	77	I/O	I/O40
3	I/O	I/O59	28	I/O	I/O9	53	I/O	I/O27	78	I/O	I/O41
4	—	NC	29	I/O	I/O10	54	—	NC	79	I/O	I/O42
5	I/O	I/O60	30	I/O	I/O11	55	I/O	I/O28	80	I/O	I/O43
6	I/O	I/O61	31	—	NC	56	I/O	I/O29	81	—	NC
7	I/O	I/O62	32	I/O	I/O12	57	I/O	I/O30	82	I/O	I/O44
8	I/O	I/O63	33	I/O	I/O13	58	I/O	I/O31	83	I/O	I/O45
9	I	IN7	34	I/O	I/O14	59	I	TCK/IN3	84	I/O	I/O46
10	I	Y0	35	I/O	I/O15	60	I	Y2	85	I/O	I/O47
11	I	RESET	36	—	V _{DD}	61	—	GND	86	—	GND
12	—	V _{DD}	37	I	TMS/IN1	62	I	GOE0	87	I/O	TDO/IN2
13	I	GOE1	38	I	IN 6	63	—	V _{DD}	88	I	IN5
14	—	GND	39	—	GND	64	—	NC	89	—	V _{DD}
15	I	ISPEN	40	I/O	I/O16	65	I	Y1	90	I/O	I/O48
16	I	TDI/IN 0	41	I/O	I/O17	66	I	IN4	91	I/O	I/O49
17	I/O	I/O0	42	I/O	I/O18	67	I/O	I/O32	92	I/O	I/O50
18	I/O	I/O1	43	I/O	I/O19	68	I/O	I/O33	93	I/O	I/O51
19	I/O	I/O2	44	—	NC	69	I/O	I/O34	94	—	NC
20	I/O	I/O3	45	I/O	I/O20	70	I/O	I/O35	95	I/O	I/O52
21	—	NC	46	I/O	I/O21	71	—	NC	96	I/O	I/O53
22	I/O	I/O4	47	I/O	I/O22	72	I/O	I/O36	97	I/O	I/O54
23	I/O	I/O5	48	I/O	I/O23	73	I/O	I/O37	98	I/O	I/O55
24	I/O	I/O6	49	I/O	I/O24	74	I/O	I/O38	99	I/O	I/O56
25	—	NC	50	—	NC	75	—	NC	100	—	NC

INPUT

GOE0, GOE1 : GLOBAL OUTPUT ENABLE

IN0 - IN7 : DATA

ISPEN : IN-SYSTEM PROGRAMMING ENABLE

RESET : RESET

TCK : CLOCK

TDI : SERIAL DATA

TMS : MODE CONTROL

Y0 - Y2 : CLOCK

OUTPUT

TDO : SERIAL DATA

INPUT/OUTPUT

I/O0 - I/O63 : DATA

