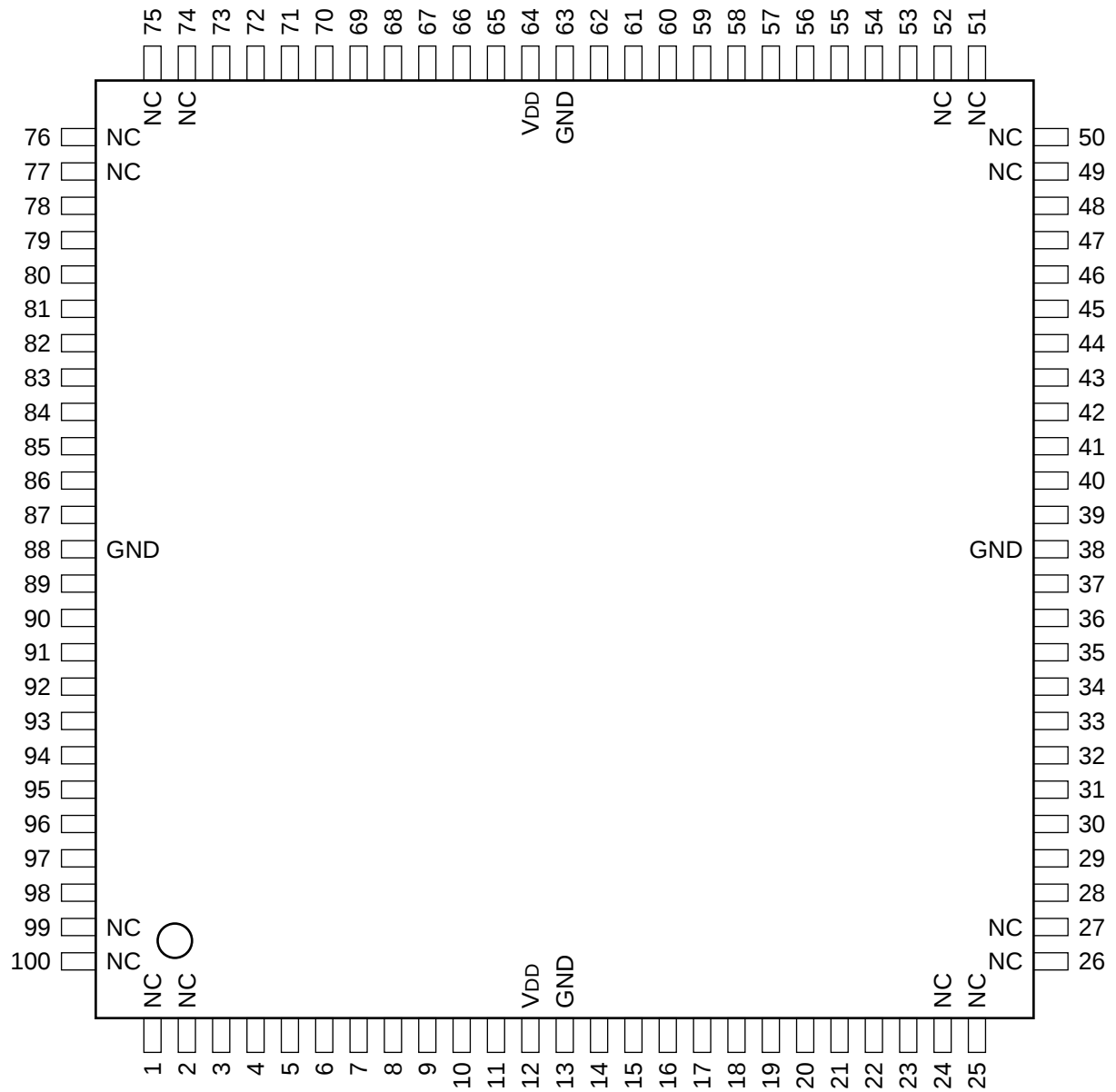


C-MOS PROGRAMMABLE LOGIC DEVICE

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	26	—	NC	51	—	NC	76	—	NC
2	—	NC	27	—	NC	52	—	NC	77	—	NC
3	I/O	I/O57	28	I/O	I/O7	53	I/O	I/O25	78	I/O	I/O39
4	I/O	I/O58	29	I/O	I/O8	54	I/O	I/O26	79	I/O	I/O40
5	I/O	I/O59	30	I/O	I/O9	55	I/O	I/O27	80	I/O	I/O41
6	I/O	I/O60	31	I/O	I/O10	56	I/O	I/O28	81	I/O	I/O42
7	I/O	I/O61	32	I/O	I/O11	57	I/O	I/O29	82	I/O	I/O43
8	I/O	I/O62	33	I/O	I/O12	58	I/O	I/O30	83	I/O	I/O44
9	I/O	I/O63	34	I/O	I/O13	59	I/O	I/O31	84	I/O	I/O45
10	I	IN7	35	I/O	I/O14	60	I	SCLK/IN3	85	I/O	I/O46
11	I	Y0	36	I/O	I/O15	61	I	Y3	86	I/O	I/O47
12	—	V _{DD}	37	I	MODE/IN1	62	I	Y2	87	I	GOE1/IN5
13	—	GND	38	—	GND	63	—	GND	88	—	GND
14	I	<u>ISPEN</u>	39	I/O	SDO/IN2	64	—	V _{DD}	89	I	IN6
15	I	<u>RESET</u>	40	I/O	I/O16	65	I	Y1	90	I/O	I/O48
16	I	SDI/IN0	41	I/O	I/O17	66	I	GOE0/IN4	91	I/O	I/O49
17	I/O	I/O0	42	I/O	I/O18	67	I/O	I/O32	92	I/O	I/O50
18	I/O	I/O1	43	I/O	I/O19	68	I/O	I/O33	93	I/O	I/O51
19	I/O	I/O2	44	I/O	I/O20	69	I/O	I/O34	94	I/O	I/O52
20	I/O	I/O3	45	I/O	I/O21	70	I/O	I/O35	95	I/O	I/O53
21	I/O	I/O4	46	I/O	I/O22	71	I/O	I/O36	96	I/O	I/O54
22	I/O	I/O5	47	I/O	I/O23	72	I/O	I/O37	97	I/O	I/O55
23	I/O	I/O6	48	I/O	I/O24	73	I/O	I/O38	98	I/O	I/O56
24	—	NC	49	—	NC	74	—	NC	99	—	NC
25	—	NC	50	—	NC	75	—	NC	100	—	NC

INPUT

GOE0/IN4, GOE1/IN5 : GLOBAL OUTPUT ENABLE/INPUT
IN6, IN7 : INPUT
 $\overline{\text{ISPEN}}$: IN-SYSTEM PROGRAMMING ENABLE
MODE/IN1 : ISP STATE CONTROL/INPUT
 $\overline{\text{RESET}}$: RESET
SCLK/IN3 : SERIAL CLOCK/INPUT
SDI/IN0 : PROGRAMMING DATA LOAD/INPUT
Y0 - Y3 : CLOCK

INPUT/OUTPUT

I/O0 - I/O63 : DATA
SDO/IN2 : SERIAL SHIFT REGISTER DATA/INPUT