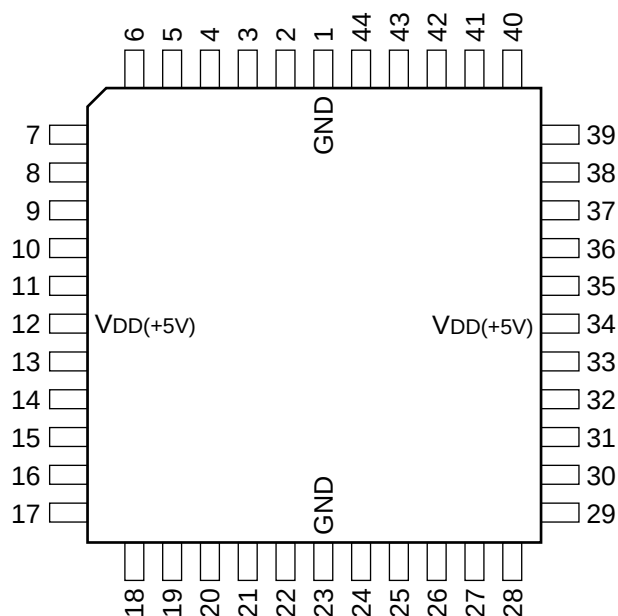

C-MOS PROGRAMMABLE LOGIC DEVICE

-TOP VIEW-

(V_{DD}=+5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	23	—	GND
2	I	IN3	24	I/O	SDO/IN1
3	I/O	I/O24	25	I/O	I/O8
4	I/O	I/O25	26	I/O	I/O9
5	I/O	I/O26	27	I/O	I/O10
6	I/O	I/O27	28	I/O	I/O11
7	I/O	I/O28	29	I/O	I/O12
8	I/O	I/O29	30	I/O	I/O13
9	I/O	I/O30	31	I/O	I/O14
10	I/O	I/O31	32	I/O	I/O15
11	I	Y0	33	I	Y2/SCLK
12	—	V _{DD}	34	—	V _{DD}
13	I	ISPEN	35	I	Y1/RESET
14	I	SDI/IN0	36	I	MODE/IN2
15	I/O	I/O0	37	I/O	I/O16
16	I/O	I/O1	38	I/O	I/O17
17	I/O	I/O2	39	I/O	I/O18
18	I/O	I/O3	40	I/O	I/O19
19	I/O	I/O4	41	I/O	I/O20
20	I/O	I/O5	42	I/O	I/O21
21	I/O	I/O6	43	I/O	I/O22
22	I/O	I/O7	44	I/O	I/O23

14	SDI/IN0	I/O0	15
24	SDO/IN1	I/O1	16
36	MODE/IN2	I/O2	17
2	IN3	I/O3	18
		I/O4	19
11	Y0	I/O5	20
35	Y1/RESET	I/O6	21
33	Y2/SCLK	I/O7	22
		I/O8	25
13	ISPEN	I/O9	26
		I/O10	27
		I/O11	28
		I/O12	29
		I/O13	30
		I/O14	31
		I/O15	32
		I/O16	37
		I/O17	38
		I/O18	39
		I/O19	40
		I/O20	41
		I/O21	42
		I/O22	43
		I/O23	44
		I/O24	3
		I/O25	4
		I/O26	5
		I/O27	6
		I/O28	7
		I/O29	8
		I/O30	9
		I/O31	10

INPUT

IN0 - IN3 ; INPUT
 ISPEN ; IN-SYSTEM PROGRAMMING ENABLE
 MODE ; MODE
 RESET ; ACTIVE LOW (0) RESET
 SCLK ; SERIAL CLOCK
 SDI ; SERIAL DATA IN
 Y0 - Y3 ; INPUT CLOCK

OUTPUT

SDO ; SERIAL DATA OUT

INPUT/OUTPUT

I/O0 - I/O31 ; INPUT/OUTPUT

