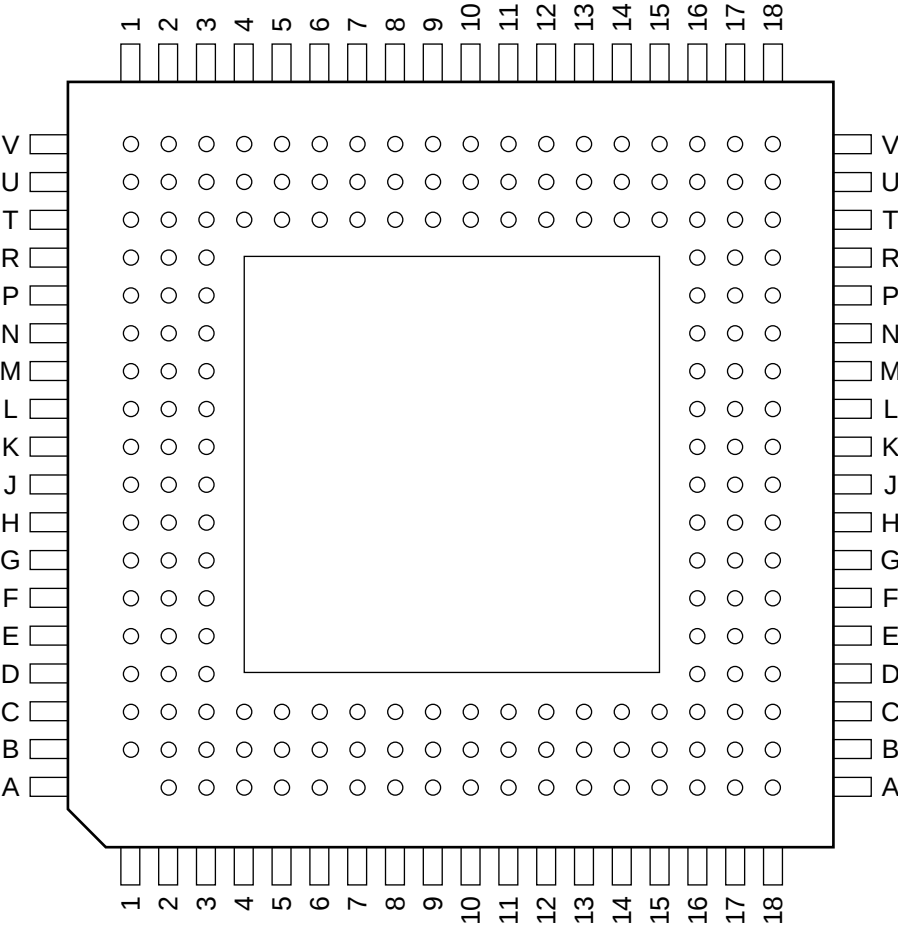


C-MOS 64-BIT RISC MICROPROCESSOR
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
A2	—	V _{DD}	C11	I/O	SYSAD41	K2	I/O	SYSAD16	T11	I/O	SYSAD57
A3	—	GND	C12	I/O	SYSAD10	K3	I	INT2	T12	I/O	SYSAD26
A4	—	V _{DD}	C13	I/O	SYSCMD8	K16	—	PGND	T13	I	IOIN
A5	I/O	SYSCMD3	C14	I/O	SYSCMDP	K17	I	PV _{DD}	T14	I	COLDRESET
A6	—	GND	C15	I/O	SYSAD44	K18	—	GND	T15	I/O	SYSAD60
A7	—	NC	C16	I/O	SYSAD13	L1	—	GND	T16	I/O	SYSAD29
A8	—	GND	C17	O	TCLOCK0	L2	I/O	SYSAD48	T17	O	RCLOCK0
A9	—	V _{DD}	C18	—	GND	L3	I	INT1	T18	—	V _{DD}
A10	—	GND	D1	—	GND	L16	I/O	SYSADC3	U1	—	V _{DD}
A11	—	V _{DD}	D2	I/O	SYSAD35	L17	I/O	SYSADC7	U2	I	EXTRQST
A12	—	GND	D3	I/O	SYSCMD1	L18	—	V _{DD}	U3	I/O	SYSAD53
A13	—	V _{DD}	D16	O	TCLOCK1	M1	—	V _{DD}	U4	I	MODEIN
A14	—	GND	D17	I/O	SYSAD14	M2	I/O	SYSAD17	U5	I/O	SYSAD22
A15	I/O	SYSAD43	D18	—	V _{DD}	M3	I/O	SYSAD49	U6	I/O	SYSAD23
A16	—	V _{DD}	E1	I/O	SYSAD2	M16	I/O	SYSAD31	U7	I	NMI
A17	—	GND	E2	I/O	SYSCMD0	M17	I	V _{DD} OK	U8	I/O	SYSADC6
A18	—	GND	E3	I/O	SYSAD3	M18	—	GND	U9	I/O	SYSAD24
B1	—	GND	E16	I	JTMS	N1	—	GND	U10	—	NC
B2	I/O	SYSCMD2	E17	I/O	SYSAD46	N2	I	INT0	U11	I/O	SYSAD25
B3	I/O	SYSAD37	E18	I/O	SYSAD15	N3	I/O	SYSAD50	U12	O	IOOUT
B4	O	MODECLOCK	F1	—	V _{DD}	N16	I/O	SYSAD62	U13	I/O	SYSAD58
B5	I/O	SYSAD6	F2	I	INT5	N17	I/O	SYSAD63	U14	I/O	SYSAD27
B6	I/O	SYSAD7	F3	I/O	SYSAD34	N18	—	V _{DD}	U15	I/O	SYSAD28
B7	I/O	SYSCMD4	F16	O	JTDO	P1	I/O	SYSAD18	U16	I	RESET
B8	I/O	SYSADC4	F17	I/O	SYSAD47	P2	I	VALIDIN	U17	I/O	SYSAD61
B9	I/O	SYSAD8	F18	—	GND	P3	I/O	SYSAD19	U18	—	GND
B10	I/O	SYSCMD6	G1	—	GND	P16	O	SYNCOUT	V1	—	GND
B11	I/O	SYSAD9	G2	I/O	SYSAD1	P17	O	MASTEROUT	V2	—	GND
B12	I/O	SYSCMD7	G3	I/O	SYSAD33	P18	—	GND	V3	—	V _{DD}
B13	I/O	SYSAD42	G16	I	JTDI	R1	—	V _{DD}	V4	—	GND
B14	I/O	SYSAD11	G17	I/O	SYSADC1	R2	I/O	SYSAD51	V5	O	RELEASE
B15	I/O	SYSAD12	G18	—	V _{DD}	R3	O	VALIDOUT	V6	—	V _{DD}
B16	O	FAULT	H1	—	V _{DD}	R16	O	RCLOCK1	V7	—	GND
B17	I/O	SYSAD45	H2	I/O	SYSAD32	R17	I/O	SYSAD30	V8	—	V _{DD}
B18	—	V _{DD}	H3	I	INT4	R18	—	GND	V9	—	GND
C1	—	V _{DD}	H16	I/O	SYSADC5	T1	—	GND	V10	—	V _{DD}
C2	I/O	SYSAD4	H17	I	JTCK	T2	I/O	SYSAD20	V11	—	GND
C3	I/O	SYSAD36	H18	—	GND	T3	I/O	SYSAD52	V12	—	V _{DD}
C4	I/O	SYSAD5	J1	—	GND	T4	I/O	SYSAD21	V13	—	GND
C5	I	WRRDY	J2	I/O	SYSAD0	T5	I	RDRDY	V14	—	V _{DD}
C6	I/O	SYSAD38	J3	I	INT3	T6	I/O	SYSAD54	V15	I/O	SYSAD59
C7	I/O	SYSAD39	J16	I	SYNCIN	T7	I/O	SYSAD55	V16	—	GND
C8	I/O	SYSADC0	J17	I	MASTERCLOCK	T8	I/O	SYSADC2	V17	—	V _{DD}
C9	I/O	SYSCMD5	J18	—	V _{DD}	T9	—	V _{DD}	V18	—	GND
C10	I/O	SYSAD40	K1	—	V _{DD}	T10	I/O	SYSAD56			

INPUT

<u>COLDRESET</u>	: COLD RESET
<u>EXTRQST</u>	: EXTERNAL REQUEST
INT0 - INT5	: INTERRUPT
IOIN	: (RESERVED FOR FUTURE INPUT)
JTCK	: JTAG CLOCK
JTDI	: JTAG DATA
JTMS	: JTAG COMMAND
MASTERCLOCK	: MASTER CLOCK
MODEIN	: BOOT MODE DATA
NMI	: NON-MASKABLE INTERRUPT
<u>RESET</u>	: RESET
<u>RDRDY</u>	: READ READY
SYNCIN	: SYNCHRONIZATION CLOCK
VALIDIN	: VALID
VDDOK	: VDD IS OK
WRRDY	: WRITE READY

OUTPUT

<u>FAULT</u>	: FAULT
IOOUT	: (RESERVED FOR FUTURE OUTPUT)
JTDO	: JTAG DATA
MASTEROUT	: MASTER CLOCK
MODECLOCK	: BOOT MODE CLOCK
RCLOCK0, RCLOCK1	: RECEIVE CLOCKS
RELEASE	: RELEASE INTERFACE
SYNCOUT	: SYNCHRONIZATION CLOCK
TCLOCK0, TCLOCK1	: TRANSMIT CLOCKS
VALIDOUT	: VALID

INPUT/OUTPUT

SYSAD0 - SYSAD63	: SYSTEM ADDRESS/DATA BUS
SYSADC0 - SYSADC7	: SYSTEM ADDRESS/DATA CHECK BUS
SYSCMD0 - SYSCMD8	: SYSTEM COMMAND/DATA IDENTIFIER BUS
SYSCMDP	: RESERVED SYSTEM COMMAND/DATA IDENTIFIER BUS PARITY