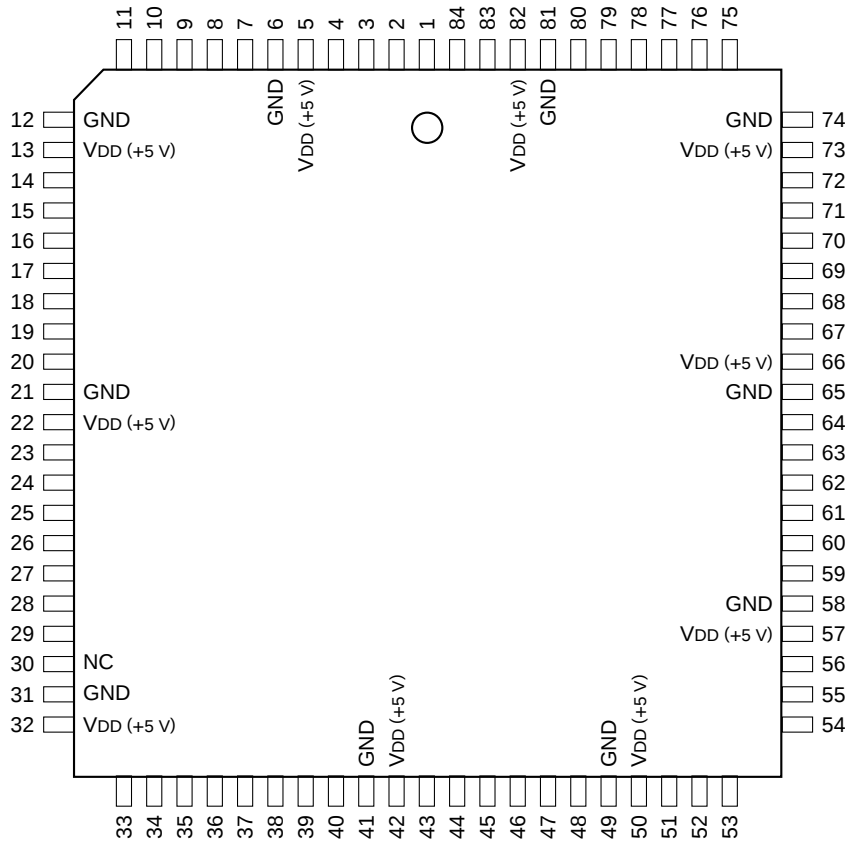


C-MOS RISC CONTROLLER WITH FLOATING POINT ACCELERATOR

—TOP VIEW—



(VDD = +5 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	A/D23	22	—	VDD	43	O	DATAEN	64	I/O	A/D8
2	I/O	A/D24	23	I	INT4	44	I/O	WR	65	—	GND
3	I/O	A/D25	24	I	INT3	45	O	RD	66	—	VDD
4	I/O	A/D26	25	I	SINT2	46	I/O	ALE	67	I/O	A/D9
5	—	VDD	26	I	SINT1	47	I/O	DIAG0/IVDREQ	68	I/O	A/D10
6	—	GND	27	I	SINT0	48	O	DIAG1	69	I/O	A/D11
7	I/O	A/D27	28	I	SBRCOND3	49	—	GND	70	I/O	A/D12
8	I/O	A/D28	29	I	SBRCOND2	50	—	VDD	71	I/O	A/D13
9	I/O	A/D29	30	—	NC	51	O	ADDR2	72	I/O	A/D14
10	I/O	A/D30	31	—	GND	52	O	ADDR3	73	—	VDD
11	I/O	A/D31	32	—	VDD	53	O	BURST/WRNEAR	74	—	GND
12	—	GND	33	I	BRCOND0	54	I/O	A/D0	75	I/O	A/D15
13	—	VDD	34	I	BUSREQ	55	I/O	A/D1	76	I/O	A/D16
14	I	CLKIN	35	I	RDCEN	56	I/O	A/D2	77	I/O	A/D17
15	I/O	RSVD4	36	I	ACK	57	—	VDD	78	I/O	A/D18
16	I/O	RSVD3	37	I	BUSERROR	58	—	GND	79	I/O	A/D19
17	I/O	RSVD2	38	I	RESET	59	I/O	A/D3	80	I/O	A/D20
18	I/O	RSVD1	39	O	BUSGNT	60	I/O	A/D4	81	—	GND
19	I	COHREQ	40	O	SYSCLK	61	I/O	A/D5	82	—	VDD
20	I	INT5	41	—	GND	62	I/O	A/D6	83	I/O	A/D21
21	—	GND	42	—	VDD	63	I/O	A/D7	84	I/O	A/D22

INPUT

<u>ACK</u>	: ACKNOWLEDGE
<u>BRCOND0</u>	: BRANCH CONDITION PORT
<u>BUSERROR</u>	: BUS ERROR
<u>BUSREQ</u>	: DMA ARBITER BUS REQUEST
<u>CLKIN</u>	: MASTER CLOCK
<u>COHREQ</u>	: COHERENT DMA REQUEST
<u>INT3 - INT5</u>	: PROCESSOR INTERRUPT
<u>IVDREQ</u>	: INVALIDDATA REQUEST
<u>RDCEN</u>	: READ BUFFER CLOCK ENABLE
<u>RESET</u>	: MASTER PROCESSOR RESET
<u>SBRCOND2, SBRCOND3</u>	: BRANCH CONDITION PORT
<u>SINT0 - SINT2</u>	: PROCESSOR INTERRUPT

OUTPUT

<u>ADDR2, ADDR3</u>	: LOW ADDRESS
<u>BURST/WRNEAR</u>	: BURST TRANSFER/WRITE NEAR
<u>BUSGNT</u>	: DMA ARBITER BUS GRANT
<u>DATAEN</u>	: EXTERNAL DATA ENABLE
<u>DIAG0, DIAG1</u>	: DIAGNOSTIC
<u>RD</u>	: READ INDICATE
<u>SYSCLK</u>	: SYSTEM REFERENCE CLOCK

INPUT/OUTPUT

<u>A/D0 - A/D31</u>	: ADDRESS/DATA
<u>ALE</u>	: ADDRESS LATCH ENABLE
<u>RSVD1 - RSVD4</u>	: RESERVED
<u>WR</u>	: WRITE INDICATE

