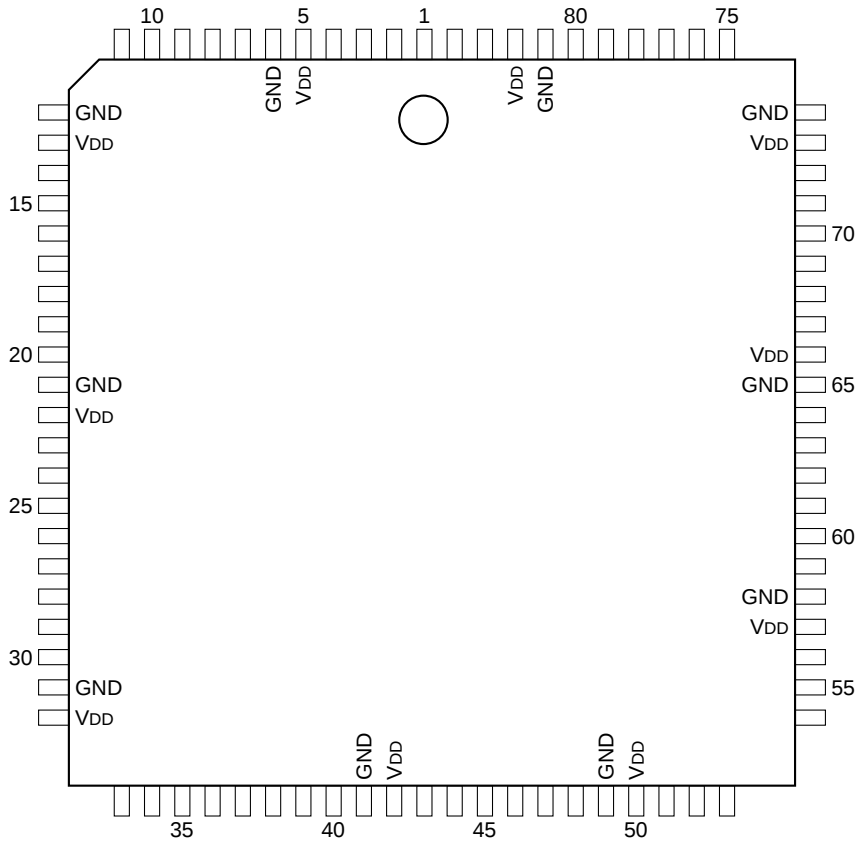


C-MOS RISC CPU

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	A/D (23)	22	—	V _{DD}	43	O	DATA EN	64	I/O	A/D (8)
2	I/O	A/D (24)	23	I	INT (4)	44	O	WR	65	—	GND
3	I/O	A/D (25)	24	I	INT (3)	45	O	RD	66	—	V _{DD}
4	I/O	A/D (26)	25	I	SINT (2)	46	O	ALE	67	I/O	A/D (9)
5	—	V _{DD}	26	I	SINT (1)	47	O	LAST	68	I/O	A/D (10)
6	—	GND	27	I	SINT (0)	48	O	DIAG	69	I/O	A/D (11)
7	I/O	A/D (27)	28	I/O	*1/I _{OSTROBE}	49	—	GND	70	I/O	A/D (12)
8	I/O	A/D (28)	29	I/O	*2/EXTDATAEN	50	—	V _{DD}	71	I/O	A/D (13)
9	I/O	A/D (29)	30	O	T _C	51	O	ADDR (2)	72	I/O	A/D (14)
10	I/O	A/D (30)	31	—	GND	52	O	ADDR (3)	73	—	V _{DD}
11	I/O	A/D (31)	32	—	V _{DD}	53	O	BURST/WRNEAR	74	—	GND
12	—	GND	33	O	MEMSTROBE	54	I/O	A/D (0)	75	I/O	A/D (15)
13	—	V _{DD}	34	I	BUSREQ	55	I/O	A/D (1)	76	I/O	A/D (16)
14	I	CLK IN	35	I	RDCEN	56	I/O	A/D (2)	77	I/O	A/D (17)
15	I	TRISTATE	36	I	ACK	57	—	V _{DD}	78	I/O	A/D (18)
16	O	BE16 (1)	37	I	BUSERROR	58	—	GND	79	I/O	A/D (19)
17	O	BE16 (0)	38	I	RESET	59	I/O	A/D (3)	80	I/O	A/D (20)
18	O	ADDR (1)	39	O	BUSGNT	60	I/O	A/D (4)	81	—	GND
19	O	ADDR (0)	40	O	SYSCLK	61	I/O	A/D (5)	82	—	V _{DD}
20	I	INT (5)	41	—	GND	62	I/O	A/D (6)	83	I/O	A/D (21)
21	—	GND	42	—	V _{DD}	63	I/O	A/D (7)	84	I/O	A/D (22)

*1 ; SBRCOND (3)

*2 ; SBRCOND (2)

INPUT

<u>ACK</u>	; ACKNOWLEDGE
<u>BUSERROR</u>	; BUS ERROR
<u>BUSREQ</u>	; DMA ARBITER BUS REQUEST
<u>CLK</u>	; MASTER CLOCK
<u>INT(3) - (5)</u>	; PROCESSOR INTERRUPT 3 - 5
<u>RDCEN</u>	; READ BUFFER CLOCK ENABLE
<u>RESET</u>	; MASTER PROCESSOR RESET
<u>SBRCOND/IO STROBE</u>	; BRANCH CONDITION PORT /IO STROBE
<u>SBRCOND/EXT DATA EN</u>	; BRANCH CONDITION PORT /EXT DATA EN
<u>SINT(0) - (2)</u>	; PROCESSOR INTERRUPT 0 - 2
<u>TRISTATE</u>	; TRL-STATE

OUTPUT

<u>ADDR0 - 3</u>	; LOW ADDRESS 0 - 3
<u>BE16(0), (1)</u>	; BYTE ENABLE STROBES FOR 16-BIT MEMORY PORT 0, 1
<u>BURST/WRNEAR</u>	; BURST TRANSFER WRITE NEAR
<u>BUSGNT</u>	; DMA ARBITER BUS GRANT
<u>DATA EN</u>	; DATA ENABLE
<u>DIAG</u>	; DIAGNOSTIC
<u>LAST</u>	; LAST DATUM IN MINI BURST
<u>MEMSTROBE</u>	; MEMORY STROBE
<u>RD</u>	; READ
<u>SYSCLK</u>	; SYSTEM REFERENCE CLOCK
<u>TC</u>	; TERMINAL COUNT

INPUT/OUTPUT

<u>A/D0 - 31</u>	; ADDRESS DATA 0 - 31
<u>ALE</u>	; ADDRSS LATCH ENABLE
<u>WR</u>	; WRITE

