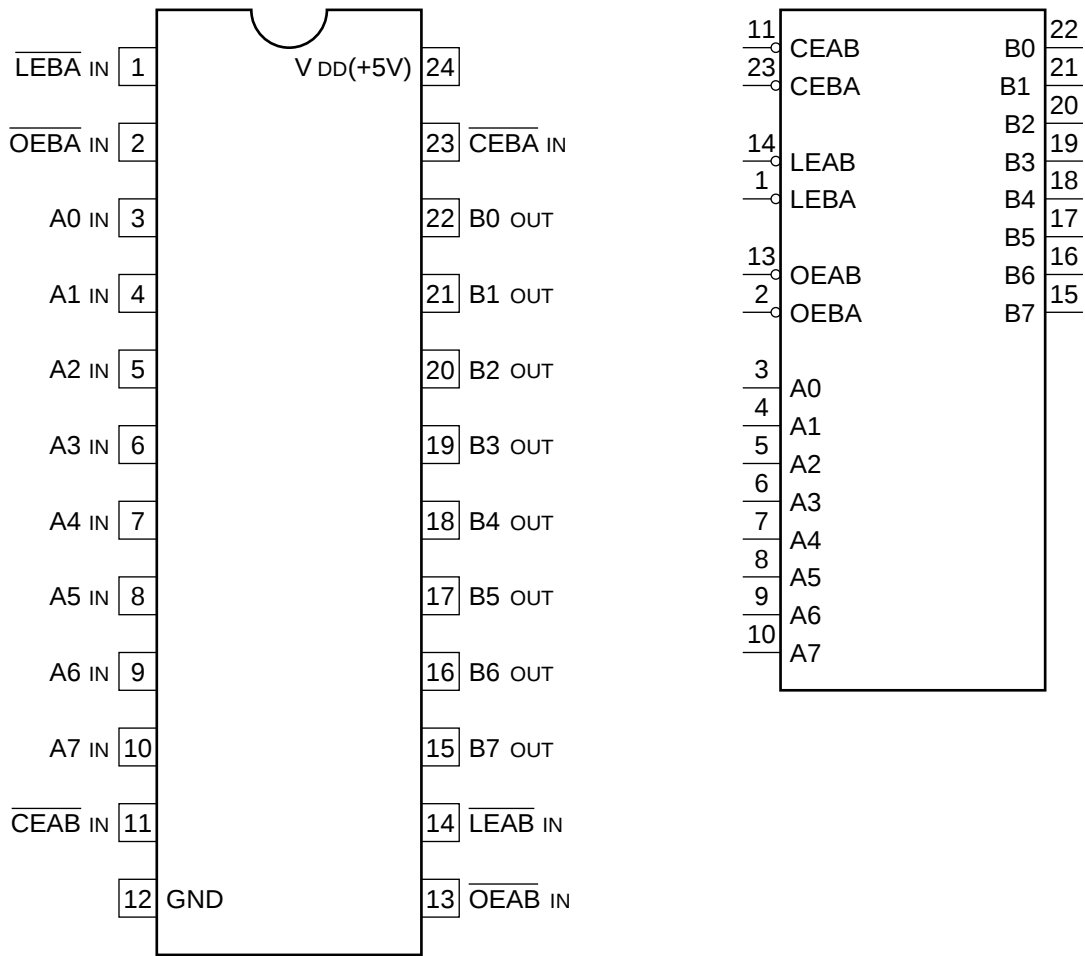

C-MOS MSI OCTAL LATCHED TRANSCEIVER
- TOP VIEW -



A0-A7 ; A TO B DATA INPUTS OR B TO A 8 STATE OUTPUTS
B0-B7 ; B TO A DATA INPUTS OR B TO A 8 STATE OUTPUTS
CEAB ; A TO B ENABLE INPUT
CEBA ; B TO A ENABLE INPUT
LEAB ; A TO B LATCH INPUT
LEBA ; B TO A LATCH INPUT
OEAB ; A TO B OUTPUT ENABLE INPUT
OEBA ; B TO A OUTPUT ENABLE INPUT

INPUTS			LATCH STATE	OUTPUTS BUFFERS
CEAB	LEAB	OEAB	A to B	B0, B7
1	—	—	STARING	HIGHT Z
—	1	—	STARING	—
—	—	1	—	HIGHT Z
0	0	0	TRANSPARENT	CURRENT A INPUT
0	1	0	STARING	PREVIOUS A INPUT

1 : LOW VOLTAGE LEVEL
0 : HIGHT VOLTAGE LEVEL
— : DONT CARE or IRRELEVANT

