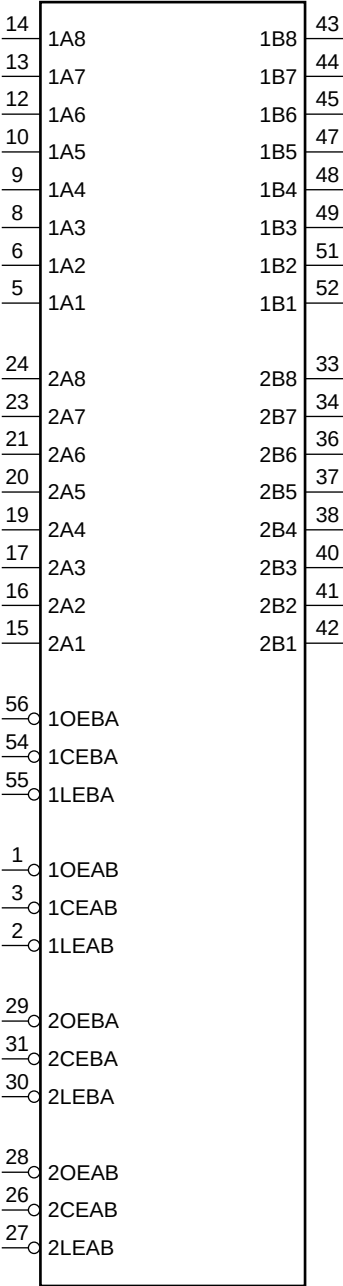
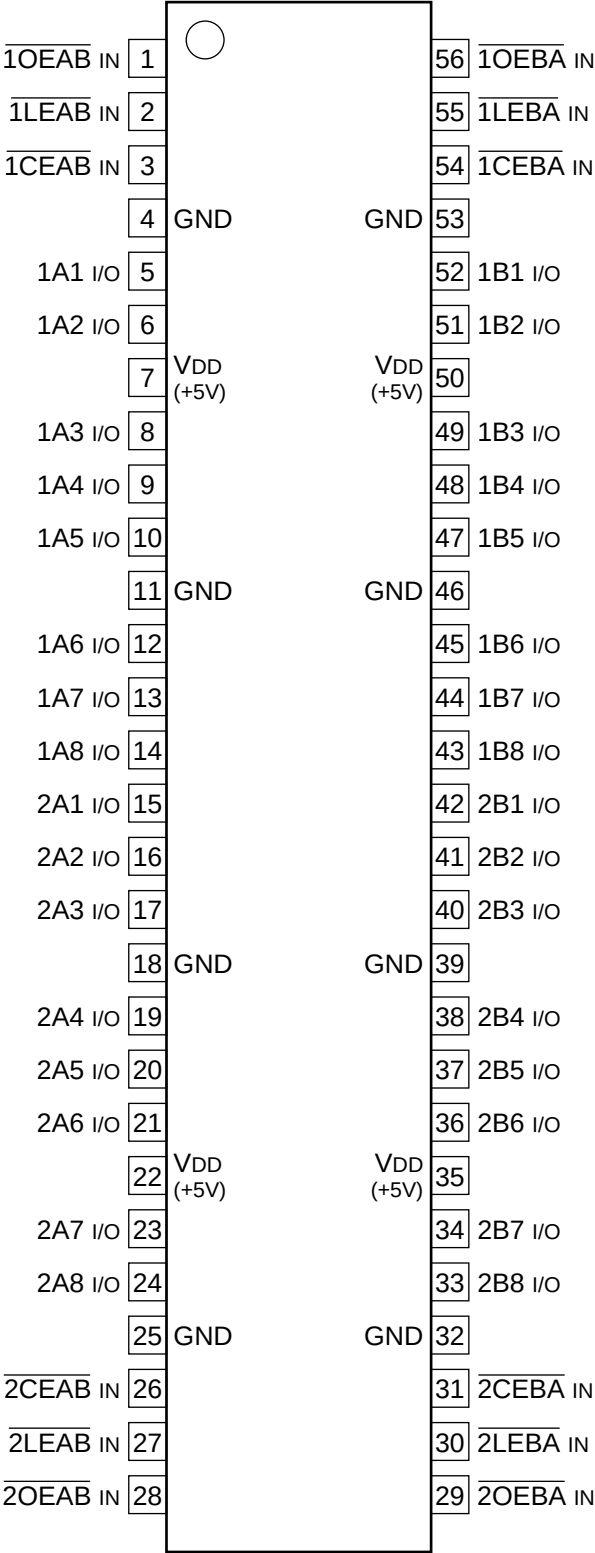

C-MOS 16-BIT LATCHED TRANSCEIVER
-TOP VIEW-



A to B				
INPUTS			LATCH STATUS	OUTPUT BUFEEERS
XCEAB	XLEAB	XOEAB	XAX to XBX	XBX
1	X	X	STORING	HI-Z
X	1	X	STORING	X
X	X	1	—	HI-Z
0	0	0	TRANSPARENT	CURRENT A INPUTS
0	1	0	STORING	PREVIOUS A INPUTS
B to A				
INPUTS			LATCH STATUS	OUTPUT BUFEEERS
XCEBA	XLEBA	XOEBA	XBX to XAX	XAX
1	X	X	STORING	HI-Z
X	1	X	STORING	X
X	X	1	—	HI-Z
0	0	0	TRANSPARENT	CURRENT A INPUTS
0	1	0	STORING	PREVIOUS A INPUTS

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

HI-Z ; HIGH IMPEDANCE

