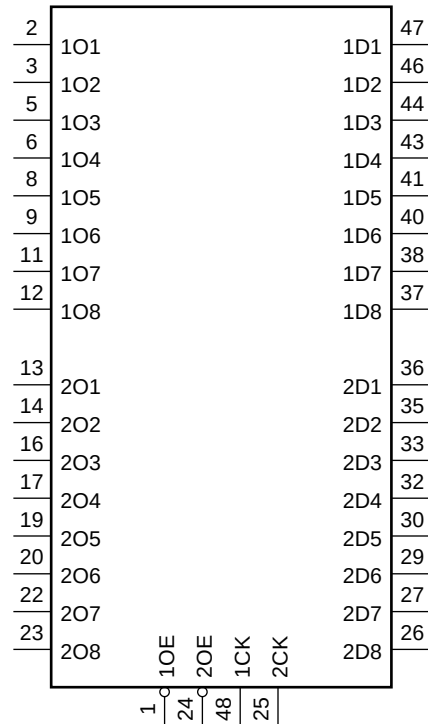
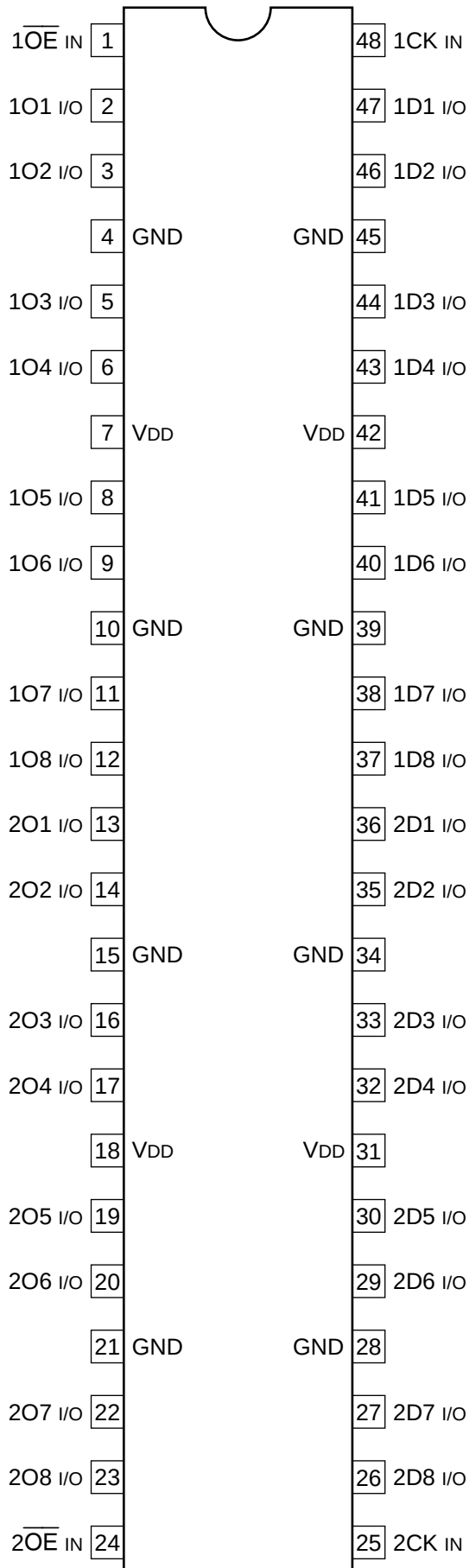


C-MOS 16-BIT REGISTER

—TOP VIEW—



PIN DESCRIPTION

PIN NAMES	DESCRIPTION
x Dx	DATA INPUTS
x CK	CLOCK INPUTS
$\overline{xOE}$	3-STATE OUTPUT ENABLE INPUTS (ACTIVE LOW)
x Ox	3-STATE OUTPUTS

FUNCTION TABLE (*1)

INPUTS			OUTPUTS
x Dx	x CK	$\overline{xOE}$	x Ox
1		0	1
0		0	0
x	x	1	HI-Z

NOTE :

- (*1) 0 : LOW LEVEL
 1 : HIGH LEVEL
 x : DON'T CARE
 HI-Z : HIGH IMPEDANCE

