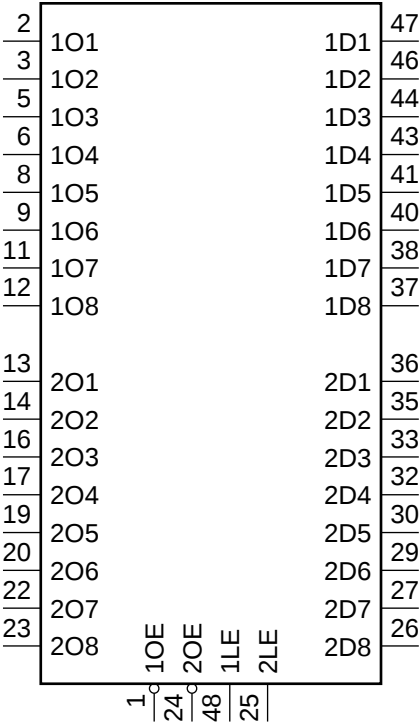
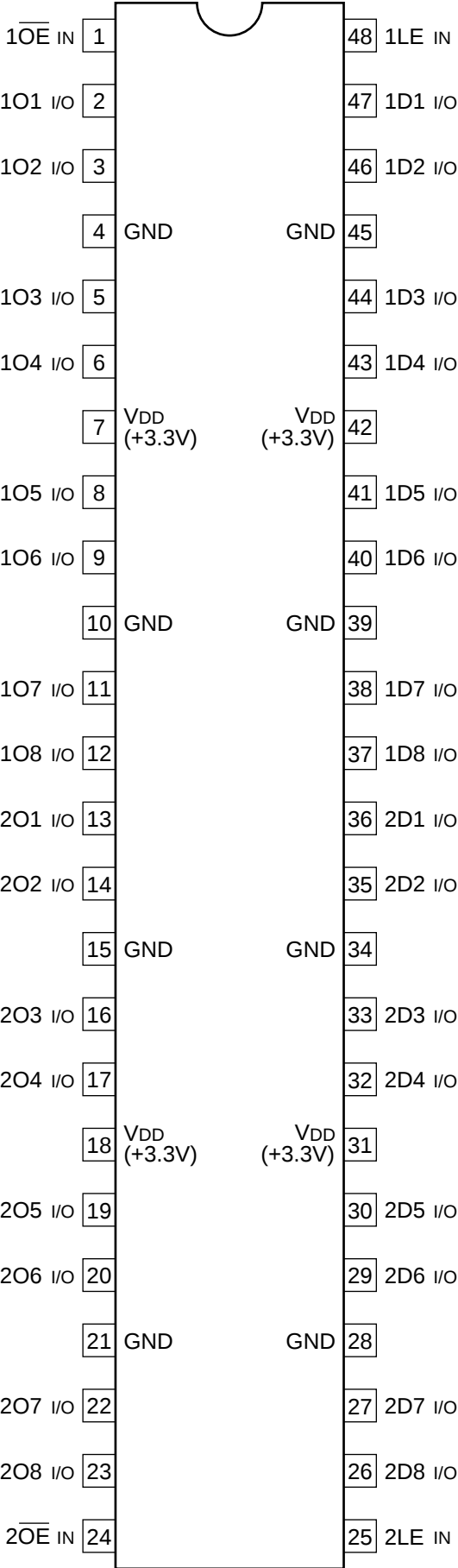

C-MOS 16-BIT TRANSPARENT LATCHE

- TOP VIEW -



PIN DESCRIPTION

PIN NAMES	DESCRIPTION
x D x	DATA INPUTS
x LE	LATCH ENABLE INPUTS (ACTIVE HIGH)
x OE	OUTPUT ENABLE INPUTS (ACTIVE LOW)
x O x	3-STATE OUTPUTS

FUNCTION TABLE (*1)

INPUTS			OUTPUTS
x D x	x LE	x OE	x O x
H	H	L	H
L	H	L	L
X	X	X	Z

NOTE :

- (*1) H = HIGH VOLTAGE LEVEL
L = LOW VOLTAGE LEVEL
X = DON'T CARE
Z = HIGH IMPEDANCE

