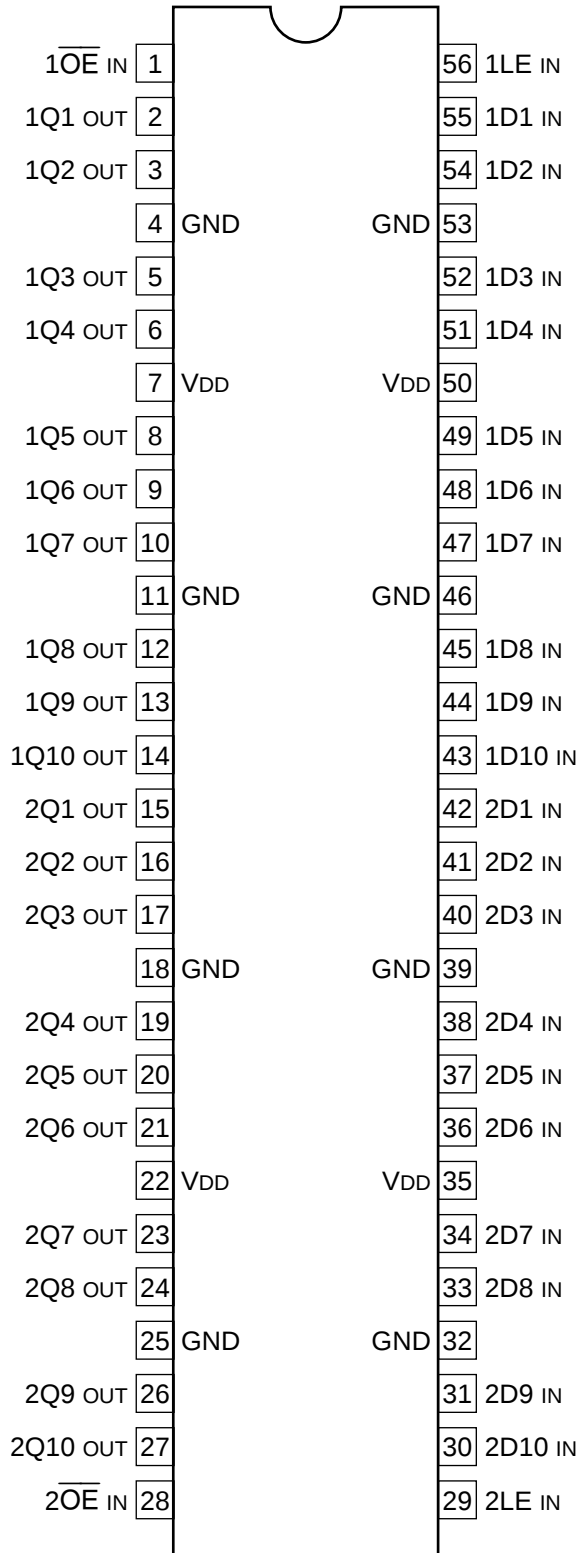


C-MOS 20-BIT TRANSPARENT LATCH

—TOP VIEW—



INPUTS			OUTPUTS
xDx	xLE	$\overline{xOE}$	xQx
1	1	0	1
0	1	0	0
x	0	0	Q*1
x	x	1	HI-Z

*1 : OUTPUT LEVEL BEFORE xLE HIGH-to-LOW TRANSITION.

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

INPUT

1D1 - 1D10, : DATA

2D1 - 2D10

1LE, 2LE : LATCH ENABLE

1OE, 2OE : OUTPUT ENABLE

OUTPUT

1Q1 - 1Q10, : 3-STATE DATA

2Q1 - 2Q10

IDT74FCT162841ETPA (2/2)

