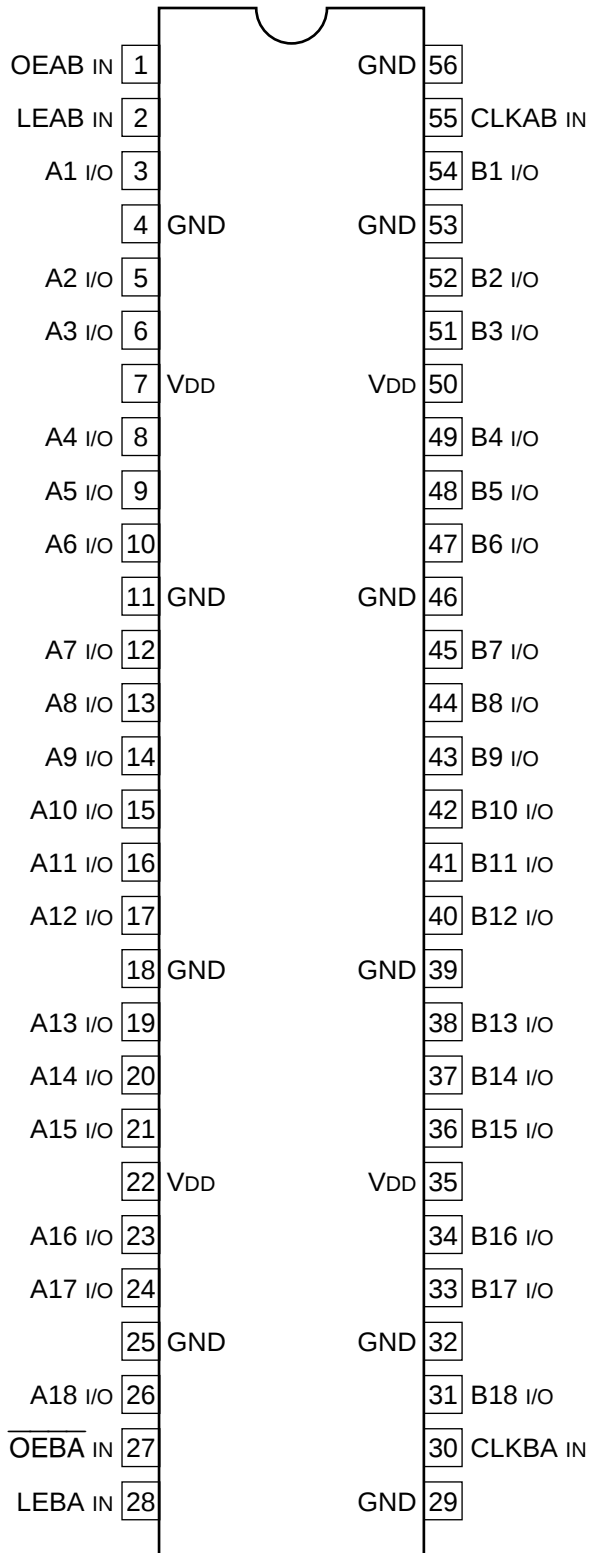


C-MOS 18-BIT REGISTER TRANSCEIVER

—TOP VIEW—



A to B

INPUTS				OUTPUT
OEAB	LEAB	CLKAB	Ax	Bx
0	x	x	x	HI-Z
1	1	x	0	0
1	1	x	1	1
1	0	$\uparrow$	0	0
1	0	$\uparrow$	1	1
1	0	0	x	B*1
1	0	1	x	B*2

B to A

INPUTS				OUTPUT
OEBA	LEBA	CLKBA	Ax	Bx
0	x	x	x	HI-Z
1	1	x	0	0
1	1	x	1	1
1	0	$\uparrow$	0	0
1	0	$\uparrow$	1	1
1	0	0	x	B*1
1	0	1	x	B*2

*1 : OUTPUT LEVEL BEFORE THE INDICATED STEADY-STATE INPUT CONDITIONS WERE ESTABLISHED.

*2 : OUTPUT LEVEL BEFORE THE INDICATED STEADY-STATE INPUT CONDITIONS WERE ESTABLISHED, PROVIDED THAT CLK_{xx} WAS HIGH BEFORE LE_{xx} WENT LOW.

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

INPUT

CLKAB : A-to-B CLOCK

CLKBA : B-to-A CLOCK

LEAB : A-to-B LATCH ENABLE

LEBA : B-to-A LATCH ENABLE

OEAB : A-to-B OUTPUT ENABLE

OEBA : B-to-A OUTPUT ENABLE

INPUT/OUTPUT

A1 - A18 : A-to-B DATA INPUTS OR
B-to-A 3-STATE OUTPUTS

B1 - B18 : B-to-A DATA INPUTS OR
A-to-B 3-STATE OUTPUTS

