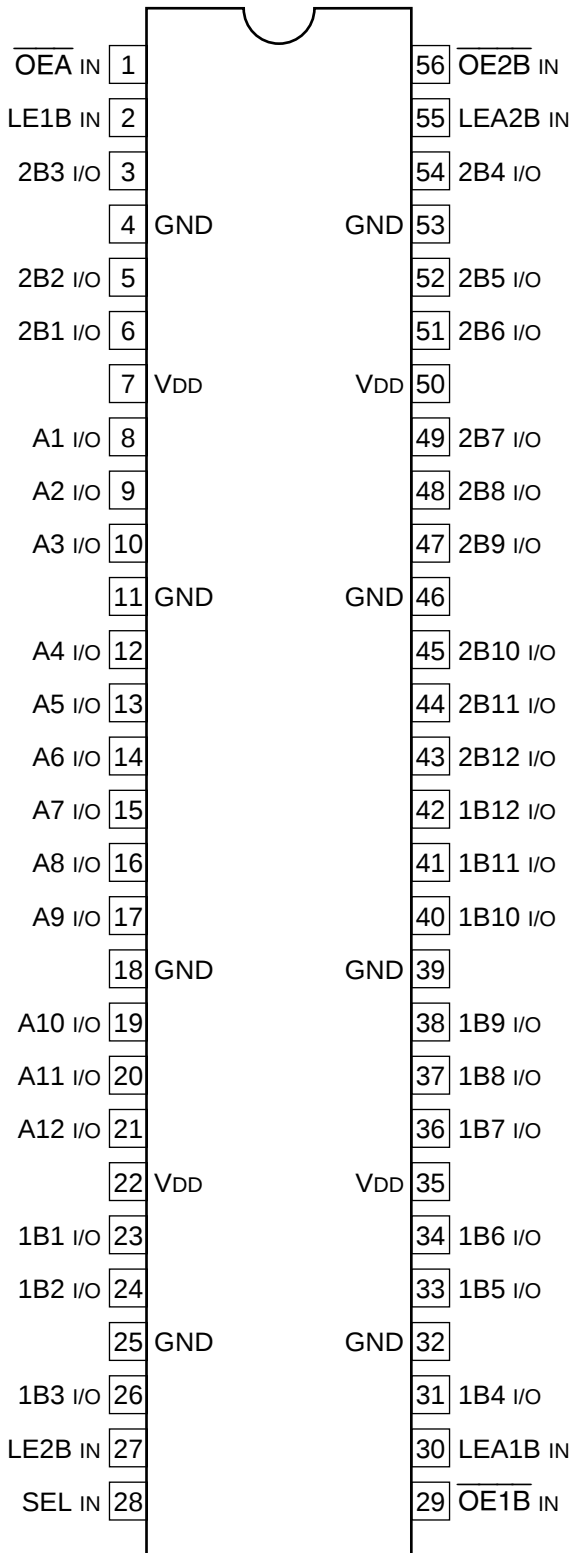


C-MOS 12-BIT TRI-PORT BUS EXCHANGER

—TOP VIEW—



INPUTS						OUTPUTS
1B	2B	SEL	LE1B	LE2B	OEA	A
1	x	1	1	x	0	1
0	x	1	1	x	0	0
x	x	1	0	x	0	A*1
x	1	0	x	1	0	1
x	0	0	x	1	0	0
x	x	0	x	0	0	A*1
x	x	x	x	x	1	HI-Z

INPUTS					OUTPUTS	
A	LEA1B	LEA2B	OE1B	OE2B	1B	2B
1	1	1	0	0	1	1
0	1	1	0	0	0	0
1	1	0	0	0	1	B*1
0	1	0	0	0	0	B*1
1	0	1	0	0	B*1	1
0	0	1	0	0	B*1	0
x	0	0	0	0	B*1	B*1
x	x	x	1	1	HI-Z	HI-Z
x	x	x	0	1	ACTIVE	HI-Z
x	x	x	1	0	HI-Z	ACTIVE
x	x	x	0	0	ACTIVE	ACTIVE

*1 : OUTPUT LEVEL BEFORE THE INDICATED STEADY-STATE INPUT CONDITIONS WERE ESTABLISHED.

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

INPUT

LEA1B : LATCH ENABLE FOR A-1B LATCH

LEA2B : LATCH ENABLE FOR A-2B LATCH

LE1B : LATCH ENABLE FOR 1B-A LATCH

LE2B : LATCH ENABLE FOR 2B-A LATCH

SEL : 1B OR 2B PATH SELECTION

OEA : OUTPUT ENABLE FOR A PORT

OE1B : OUTPUT ENABLE FOR 1B PORT

OE2B : OUTPUT ENABLE FOR 2B PORT

INPUT/OUTPUT

A1 - A12 : BIDIRECTIONAL DATA PORT A

1B1 - 1B12 : BIDIRECTIONAL DATA PORT 1B

2B1 - 2B12 : BIDIRECTIONAL DATA PORT 2B

