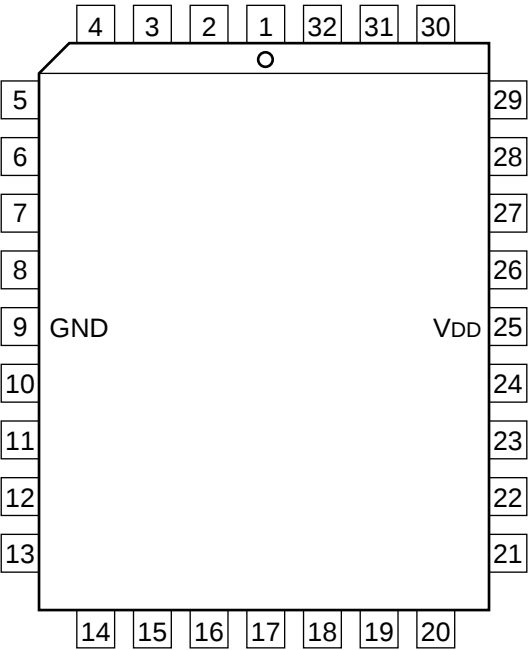
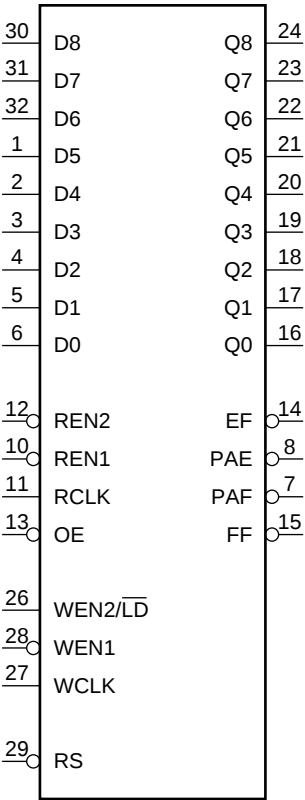


C-MOS (2,048 × 9)-BIT SYNCHRONOUS FIFO MEMORY
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	D5	17	O	Q1
2	I	D4	18	O	Q2
3	I	D3	19	O	Q3
4	I	D2	20	O	Q4
5	I	D1	21	O	Q5
6	I	D0	22	O	Q6
7	O	PAF	23	O	Q7
8	O	PAE	24	O	Q8
9	—	GND	25	—	VDD
10	I	REN1	26	I	WEN2/LD
11	I	RCLK	27	I	WCLK
12	I	REN2	28	I	WEN1
13	I	OE	29	I	RS
14	O	EF	30	I	D8
15	O	FF	31	I	D7
16	O	Q0	32	I	D6



INPUT

D0 - D8 : DATA

OE : OUTPUT ENABLE

RCLK : READ CLOCK

REN1, REN2 : READ ENABLE 1, 2

RS : RESET

WCLK : WRITE CLOCK

WEN1 : WRITE ENABLE 1

WEN2/LD : WRITE ENABLE 2/LOAD

OUTPUT

EF : EMPTY FLAG

FF : FULL FLAG

PAE : PROGRAMMABLE ALMOST-EMPTY FLAG

PAF : PROGRAMMABLE ALMOST-FULL FLAG

Q0 - Q8 : DATA

