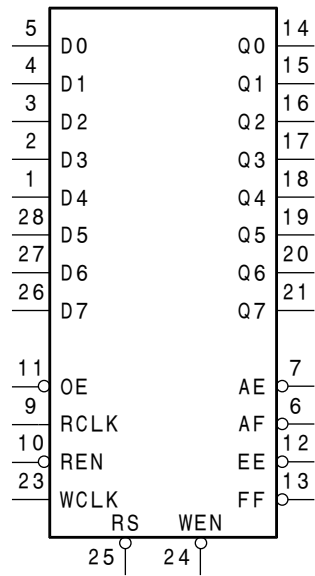
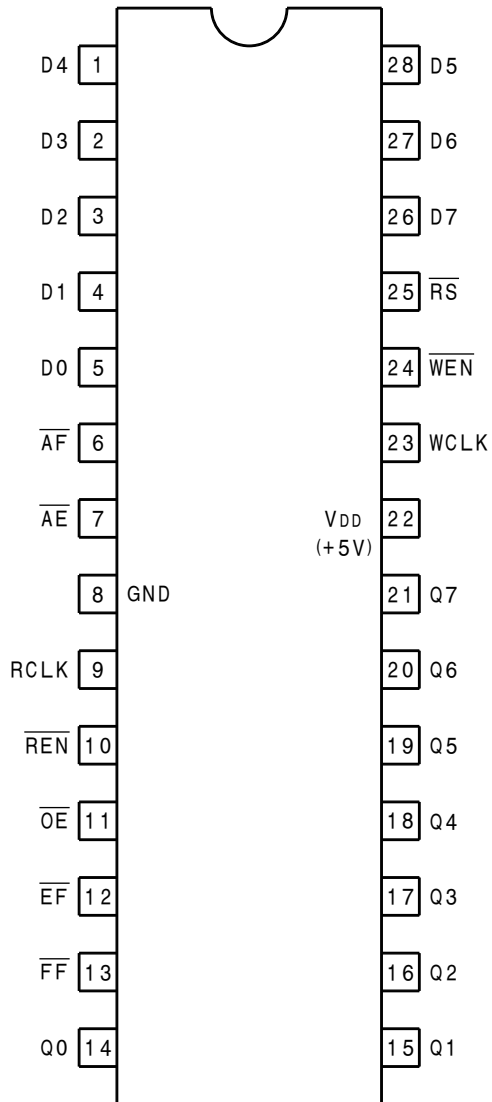

C-MOS FIFO 256x8-BIT DUALPORT MEMORY

-TOP VIEW-



INPUT

D0 - D7 ; DATA INPUTS
 $\overline{OE}$; OUTPUT ENABLE
 RCLK ; READ CLOCK
 $\overline{REN}$; READ ENABLE
 $\overline{RS}$; RESET
 WCLK ; WRITE CLOCK
 $\overline{WEN}$; WRITE ENABLE

OUTPUT

$\overline{AE}$; ALMOST-EMPTY FLAG
 $\overline{AF}$; ALMOST-FULL FLAG
 $\overline{EF}$; EMPTY FLAG
 $\overline{FF}$; FULL FLAG
 Q0 - Q7 ; DATA OUTPUTS

