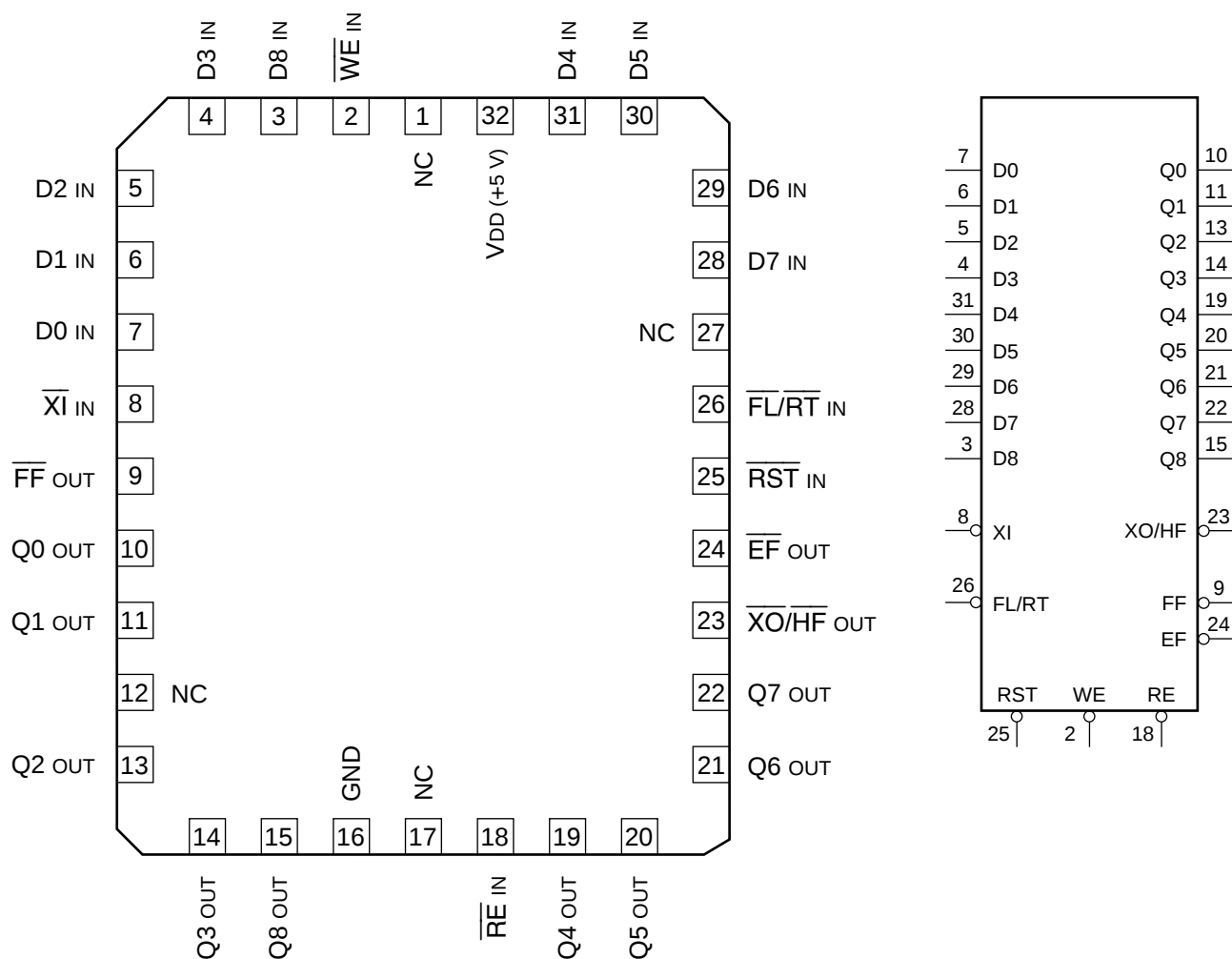


C-MOS ASYNCHRONOUS FIFO 256 × 9 BITS DUAL PORT MEMORY

—TOP VIEW—

**INPUT**

D0 - D8 : DATA INPUTS
 $\overline{FL/RT}$: FIRST LOAD/RETRANSMIT
 $\overline{RE}$: READ ENABLE
 $\overline{RST}$: RESET
 $\overline{WE}$: WRITE ENABLE
 $\overline{XI}$: EXPANSION IN

OUTPUT

$\overline{EF}$: EMPTY FLAG
 $\overline{FF}$: FULL FLAG
Q0 - Q8 : DATA OUTPUTS
 $\overline{XO/HF}$: EXPANSION OUT/HALF-FULL FLAG

