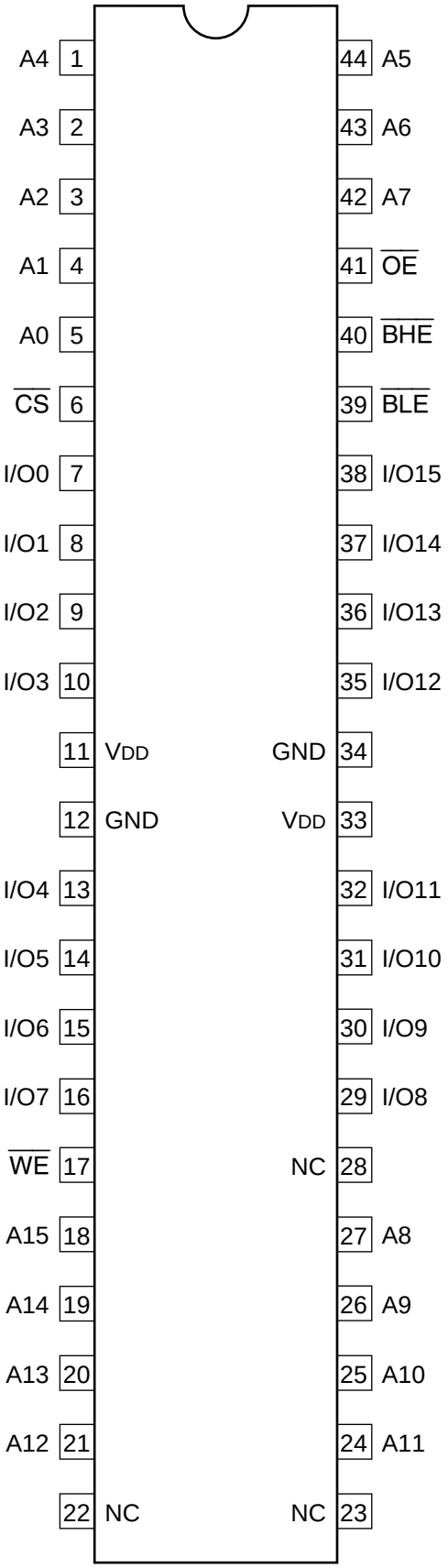
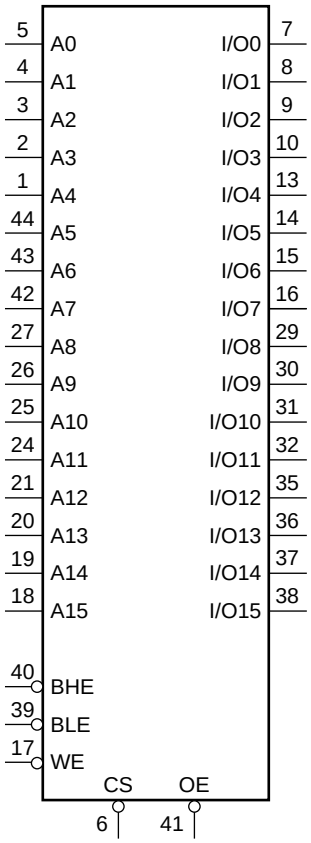
C-MOS 1 M (64 K × 16)-BIT SRAM

—TOP VIEW—





INPUT

A0 - A15	:	ADDRESS
$\overline{BHE}$	:	HIGH BYTE ENABLE
$\overline{BLE}$	:	LOW BYTE ENABLE
$\overline{CS}$	:	CHIP SELECT
$\overline{OE}$	:	OUTPUT ENABLE
$\overline{WE}$	:	WRITE ENABLE

INPUT/OUTPUT

I/O0 - I/O15	:	DATA