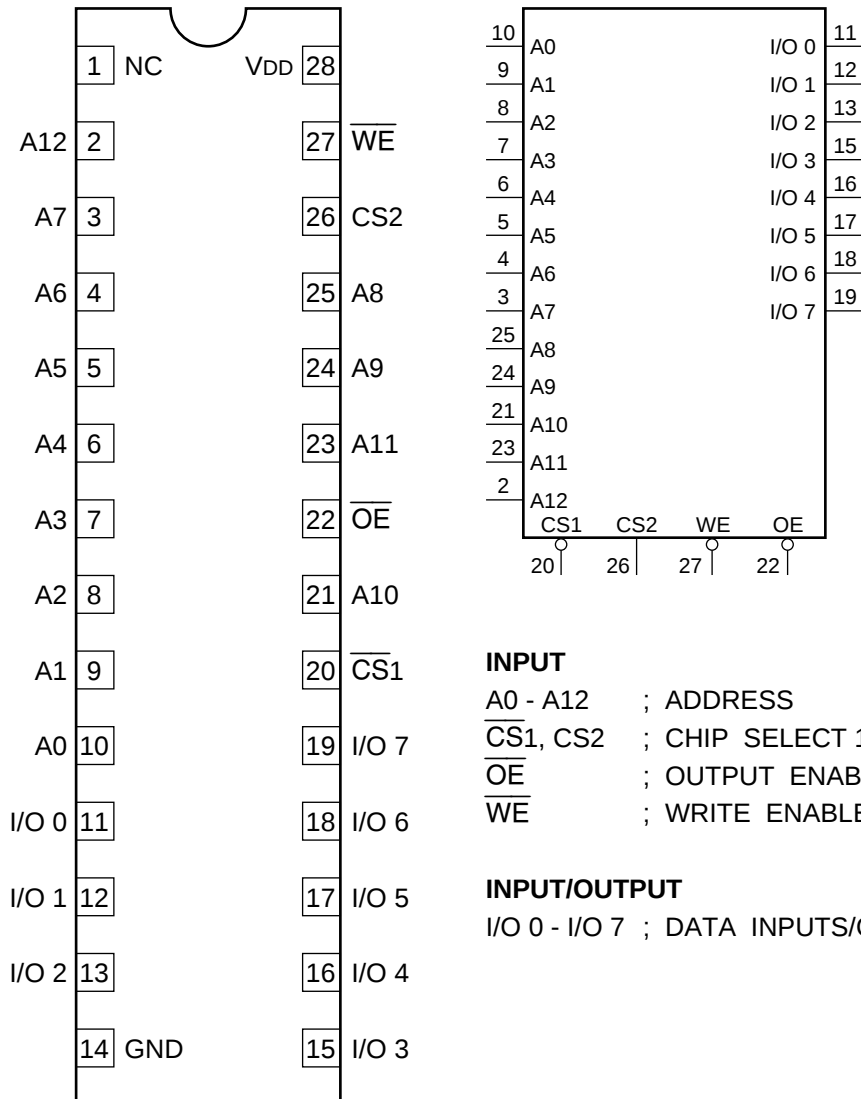


C-MOS 64 K (8 K × 8)-BIT SRAM

—TOP VIEW—



WE	CS1	CS2	OE	I/O	FUNCTION
X	1	X	X	HI-Z	DESELECTED-STANDBY (ISB)
X	X	0	X	HI-Z	DESELECTED-STANDBY (ISB)
X	VHC	VHC OR VLC	X	HI-Z	DESELECTED-STANDBY (ISB1)
X	X	VLC	X	HI-Z	DESELECTED-STANDBY (ISB1)
1	0	1	1	HI-Z	OUTPUT DISABLED
1	0	1	0	DATA OUT	READ DATA
0	0	1	X	DATA IN	WRITE DATA

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

HI-Z ; HIGH IMPEDANCE

VLC ; 0.2 V

VHC ; -0.2 V

