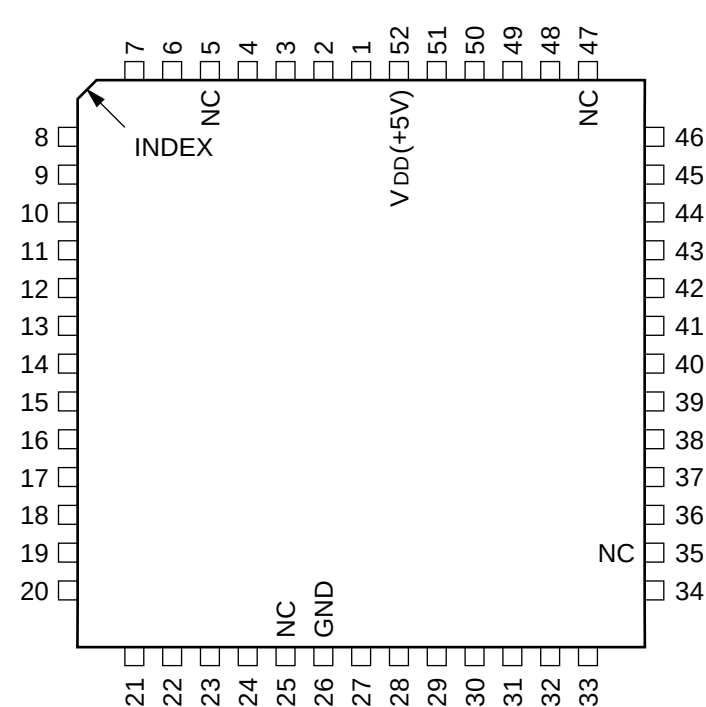

C-MOS 8K (1K x 8) -BIT DUAL PORT RAM

- TOP VIEW -



A9-A0 ; ADDRESS INPUTS
BUSY ; BUSY OUTPUT
CE ; CHIP ENABLE INPUT
INT ; INTERRUPT OUTPUT
I/O7-I/O0 ; DATA INPUTS/OUTPUTS
OE ; OUTPUT ENABLE
R/W ; READ/WRITE CONTROL

