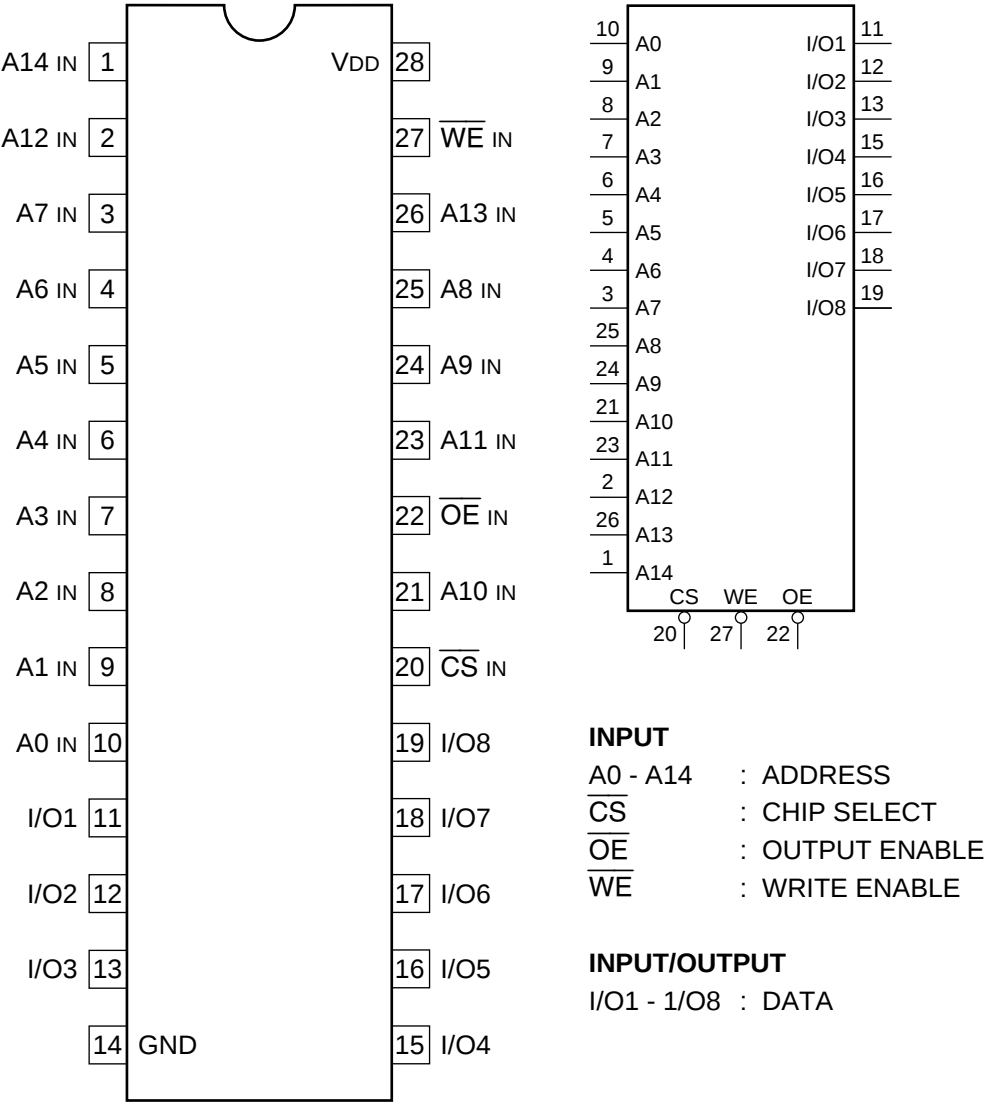


C-MOS 256 K (32 K × 8)-BIT SRAM

—TOP VIEW—



CS	OE	WE	OUTPUTS	FUNCTION
1	x	x	HI-Z	NO SELECTION
0	1	1	HI-Z	OUTPUT DISABLE
0	0	1	DOUT	READ
0	x	0	DIN	WRITE

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

