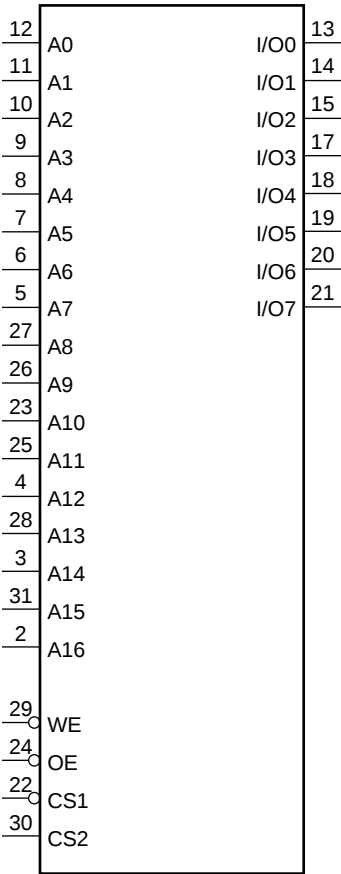
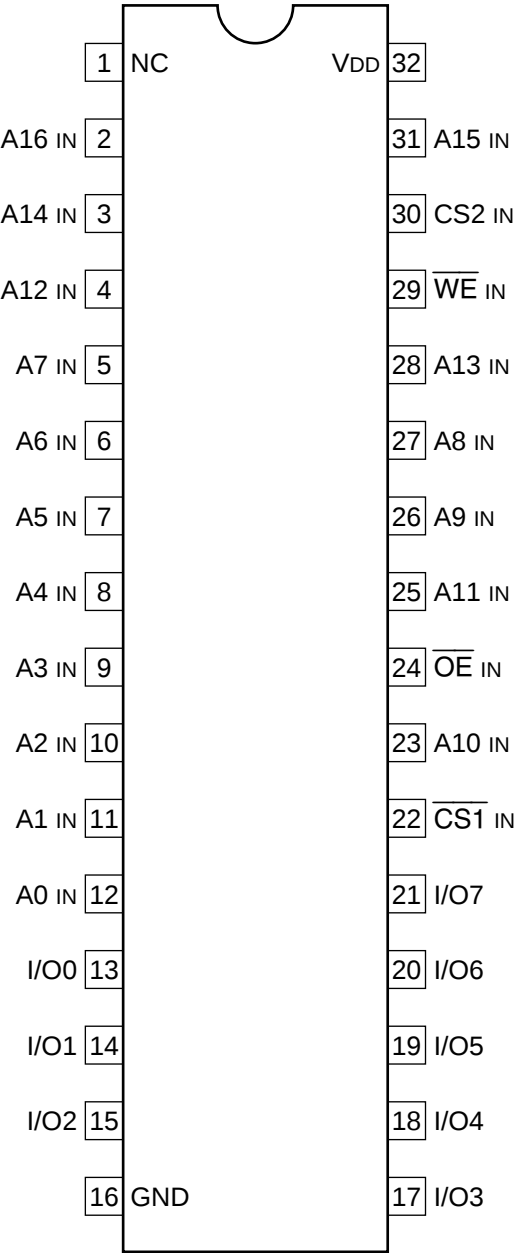


C-MOS 1 M (128 × 8)-BIT SRAM

—TOP VIEW—



INPUTS				I/O	FUNCTION
WE	CS1	CS2	OE		
x	1	x	x	HI-Z	STANDBY
x	x	0	x	HI-Z	STANDBY
1	0	1	1	HI-Z	OUTPUTS DISABLED
1	0	1	0	DATA OUT	READ DATA
0	0	1	x	DATA IN	WRITE DATA

1 : HIGH LEVEL
0 : LOW LEVEL
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

