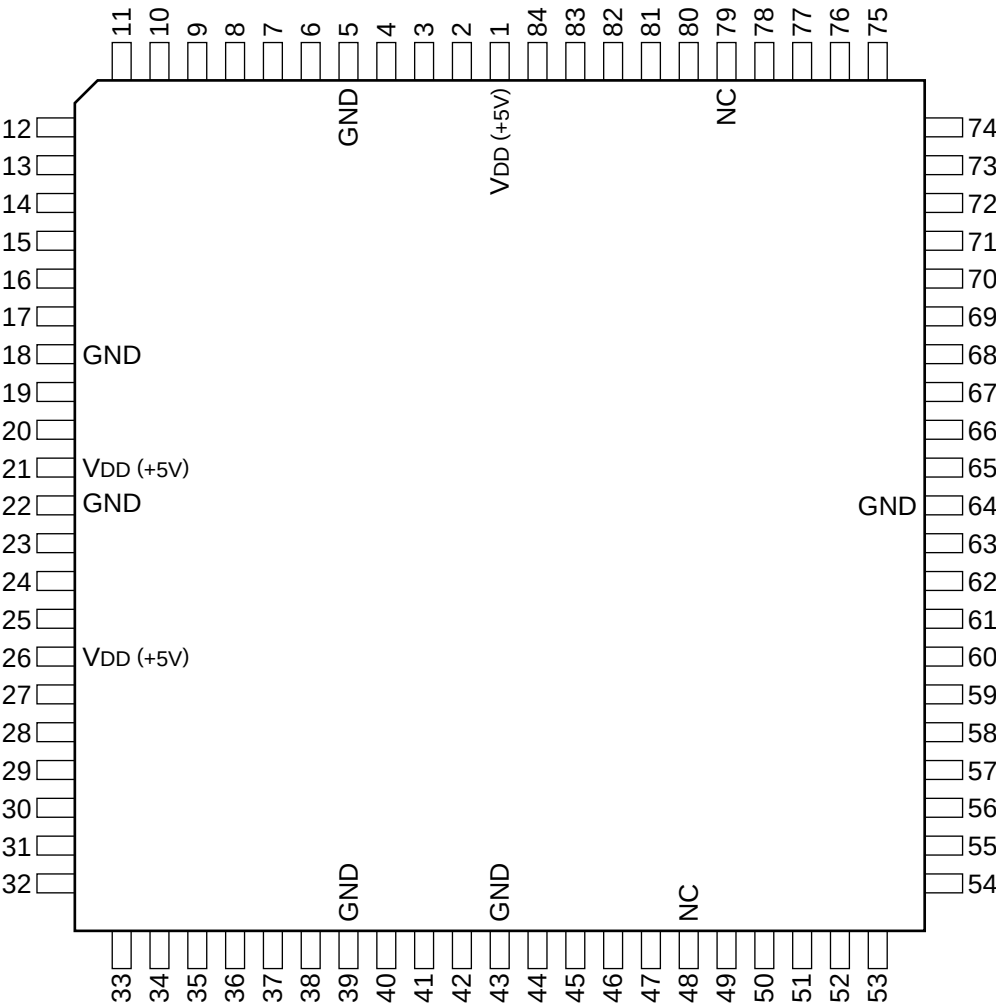

C-MOS HIGH-SPEED 4 K × 16 DUAL-PORT STATIC RAM
—TOP VIEW—



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	22	—	GND	43	—	GND	64	—	GND
2	I	$\overline{\text{OEL}}$	23	I/O	I/O0R	44	I	$\overline{\text{SEMR}}$	65	O	$\overline{\text{BUSYL}}$
3	I/O	I/O0L	24	I/O	I/O1R	45	I	$\overline{\text{CE}}_{\text{R}}$	66	O	$\overline{\text{INTL}}$
4	I/O	I/O1L	25	I/O	I/O2R	46	I	$\overline{\text{UB}}_{\text{R}}$	67	I	A0L
5	—	GND	26	—	V _{DD}	47	I	$\overline{\text{LB}}_{\text{R}}$	68	I	A1L
6	I/O	I/O2L	27	I/O	I/O3R	48	—	NC	69	I	A2L
7	I/O	I/O3L	28	I/O	I/O4R	49	I	A11R	70	I	A3L
8	I/O	I/O4L	29	I/O	I/O5R	50	I	A10R	71	I	A4L
9	I/O	I/O5L	30	I/O	I/O6R	51	I	A9R	72	I	A5L
10	I/O	I/O6L	31	I/O	I/O7R	52	I	A8R	73	I	A6L
11	I/O	I/O7L	32	I/O	I/O8R	53	I	A7R	74	I	A7L
12	I/O	I/O8L	33	I/O	I/O9R	54	I	A6R	75	I	A8L
13	I/O	I/O9L	34	I/O	I/O10R	55	I	A5R	76	I	A9L
14	I/O	I/O10L	35	I/O	I/O11R	56	I	A4R	77	I	A10L
15	I/O	I/O11L	36	I/O	I/O12R	57	I	A3R	78	I	A11L
16	I/O	I/O12L	37	I/O	I/O13R	58	I	A2R	79	—	NC
17	I/O	I/O13L	38	I/O	I/O14R	59	I	A1R	80	I	$\overline{\text{LBL}}$
18	—	GND	39	—	GND	60	I	A0R	81	I	$\overline{\text{UBL}}$
19	I/O	I/O14L	40	I/O	I/O15R	61	O	$\overline{\text{INT}}_{\text{R}}$	82	I	$\overline{\text{CEL}}$
20	I/O	I/O15L	41	I	$\overline{\text{OER}}$	62	O	$\overline{\text{BUSY}}_{\text{R}}$	83	I	$\overline{\text{SEML}}$
21	—	V _{DD}	42	I	R/W _R	63	I	M/S	84	I	R/W _L

INPUT

A0R - A11R, A0L - A11L ; ADDRESSES
 $\overline{\text{CE}}_{\text{R}}$, $\overline{\text{CE}}_{\text{L}}$; CHIP ENABLE
 $\overline{\text{LB}}_{\text{R}}$, $\overline{\text{LB}}_{\text{L}}$; LOWER BYTE SELECT
 M/S ; MASTER/SLAVE SELECT
 $\overline{\text{OER}}$, $\overline{\text{OEL}}$; OUTPUT ENABLE
 R/W_R, R/W_L ; READ/WRITE ENABLE
 $\overline{\text{SEMR}}$, $\overline{\text{SEML}}$; SEMAPHORE ENABLE
 $\overline{\text{UB}}_{\text{R}}$, $\overline{\text{UB}}_{\text{L}}$; UPPER BYTE SELECT

OUTPUT

$\overline{\text{BUSY}}_{\text{R}}$, $\overline{\text{BUSY}}_{\text{L}}$; BUSY FLAG
 $\overline{\text{INT}}_{\text{R}}$, $\overline{\text{INT}}_{\text{L}}$; INTERRUPT FLAG

INPUT/OUTPUT

I/O0R - I/O15R, I/O0L - I/O15L ; DATA INPUT/OUTPUT

