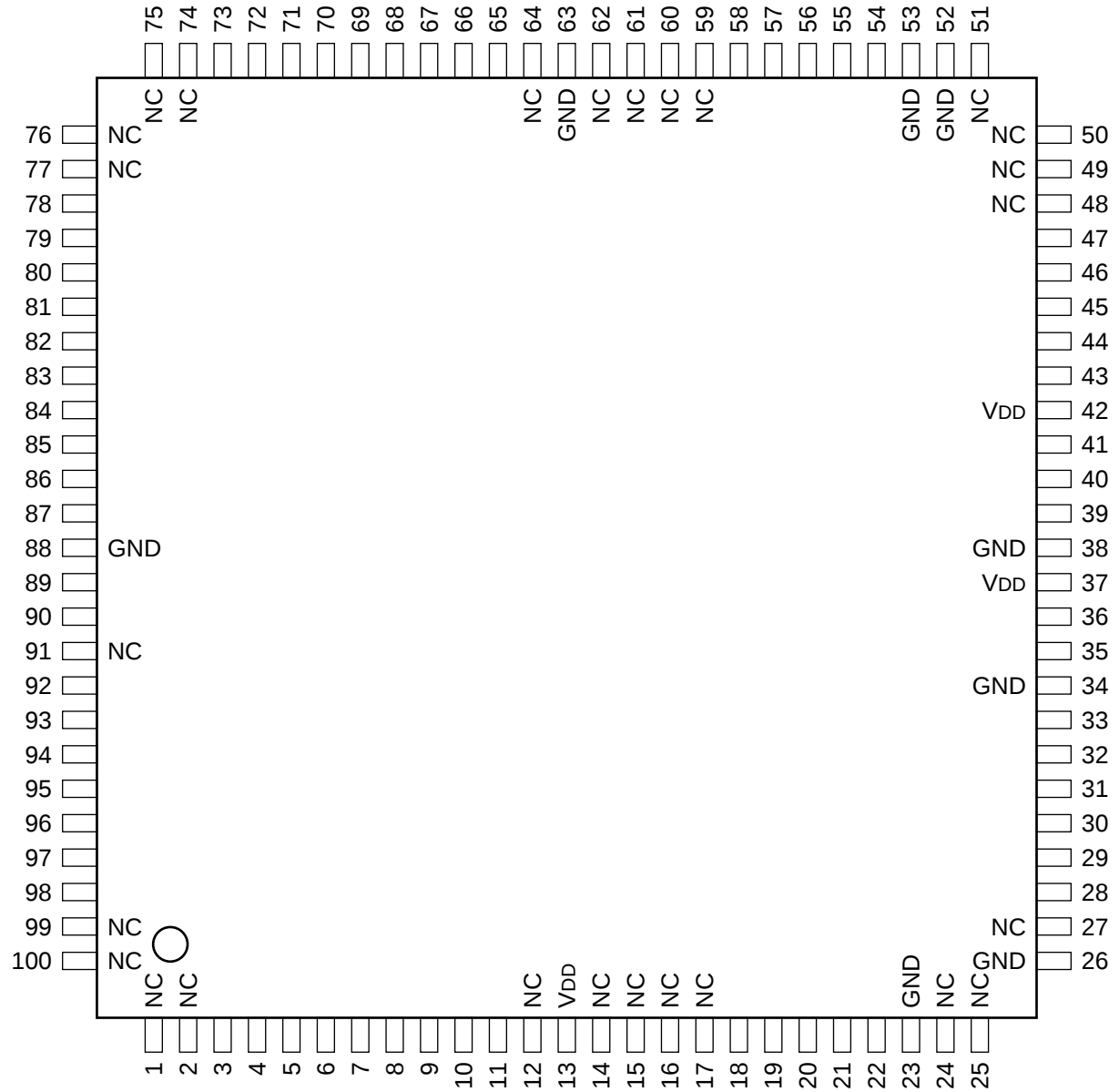


—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	26	—	GND	51	—	NC	76	—	NC
2	—	NC	27	—	NC	52	—	GND	77	—	NC
3	I	A7L	28	I/O	I/O7L	53	—	GND	78	I	A6R
4	I	A8L	29	I/O	I/O6L	54	I	$\overline{\text{OE}}_{\text{R}}$	79	I	A5R
5	I	A9L	30	I/O	I/O5L	55	I	$\text{R}/\overline{\text{W}}_{\text{R}}$	80	I	A4R
6	I	A10L	31	I/O	I/O4L	56	I	$\overline{\text{SE}}_{\text{MR}}$	81	I	A3R
7	I	A11L	32	I/O	I/O3L	57	I	$\text{CE}_{1\text{R}}$	82	I	A2R
8	I	A12L	33	I/O	I/O2L	58	I	$\text{CE}_{0\text{R}}$	83	I	A1R
9	I	A13L	34	—	GND	59	—	NC	84	I	A0R
10	I	A14L	35	I/O	I/O1L	60	—	NC	85	O	$\overline{\text{INT}}_{\text{R}}$
11	I	A15L	36	I/O	I/O0L	61	—	NC	86	O	$\overline{\text{BUSY}}_{\text{R}}$
12	—	NC	37	—	V _{DD}	62	—	NC	87	I	$\text{M}/\overline{\text{S}}$
13	—	V _{DD}	38	—	GND	63	—	GND	88	—	GND
14	—	NC	39	I/O	I/O0R	64	—	NC	89	O	$\overline{\text{BUSY}}_{\text{L}}$
15	—	NC	40	I/O	I/O1R	65	I	A15R	90	O	$\overline{\text{INT}}_{\text{L}}$
16	—	NC	41	I/O	I/O2R	66	I	A14R	91	—	NC
17	—	NC	42	—	V _{DD}	67	I	A13R	92	I	A0L
18	I	$\overline{\text{CE}}_{0\text{L}}$	43	I/O	I/O3R	68	I	A12R	93	I	A1L
19	I	$\text{CE}_{1\text{L}}$	44	I/O	I/O4R	69	I	A11R	94	I	A2L
20	I	$\overline{\text{SE}}_{\text{ML}}$	45	I/O	I/O5R	70	I	A10R	95	I	A3L
21	I	$\text{R}/\overline{\text{W}}_{\text{L}}$	46	I/O	I/O6R	71	I	A9R	96	I	A4L
22	I	$\overline{\text{OE}}_{\text{L}}$	47	I/O	I/O7R	72	I	A8R	97	I	A5L
23	—	GND	48	—	NC	73	I	A7R	98	I	A6L
24	—	NC	49	—	NC	74	—	NC	99	I	NC
25	—	NC	50	—	NC	75	—	NC	100	I	NC

INPUT

$A0L - A15L, A15R - A0R$: ADDRESS
 $\overline{CE0L}, CE1L, \overline{CE0R}, CE1R$: CHIP ENABLE
 $M/\overline{S}$: CONTROL/CONTROLLED SELECT
 $\overline{OEL}, \overline{OER}$: OUTPUT ENABLE
 $\overline{R/W_R}, \overline{R/W_L}$: READ/WRITE ENABLE
 $\overline{SEMR}, \overline{SEML}$: SEMAPHORE ENABLE

OUTPUT

$\overline{BUSY_R}, \overline{BUSY_L}$: BUSY FLAG
 $\overline{INT_L}, \overline{INT_R}$: INTERRUPT FLAG

INPUT/OUTPUT

$I/O0L - I/O7L, I/O0R - I/O7R$: DATA