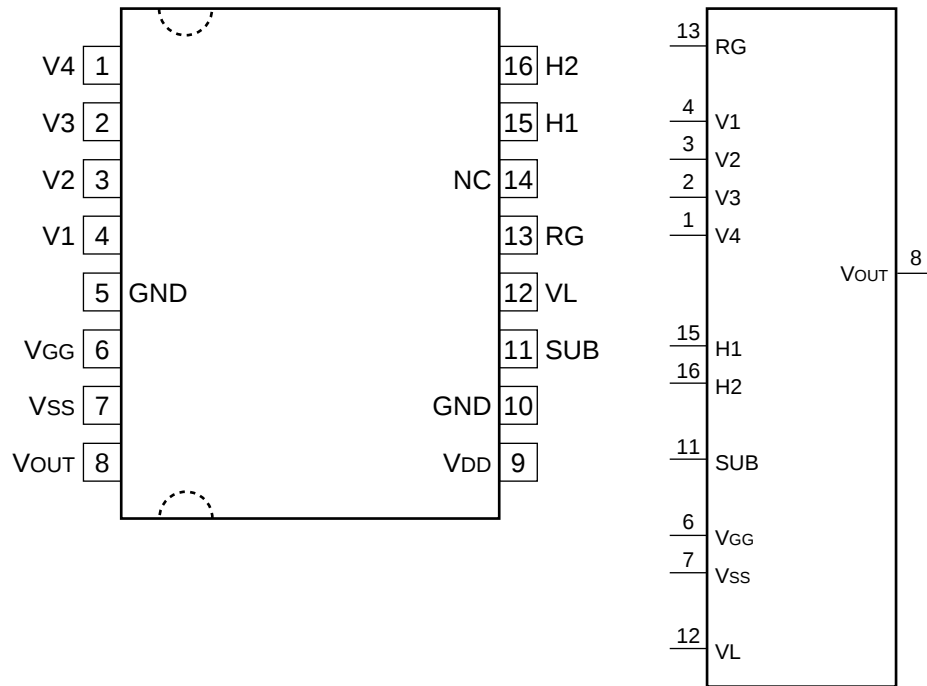


1/3-INCH CCD IMAGE BLOCK

—TOP VIEW—



- H1, H2 : HORIZONTAL REGISTER TRANSFER CLOCK
- RG : RESET GATE CLOCK
- SUB : OVERFLOW DRAIN
- VGG : OUTPUT AMP GATE BIAS
- VL : PROTECTION TRANSISTOR BIAS
- VSS : OUTPUT AMP SOURCE
- V1 - V4 : VERTICAL REGISTER TRANSFER CLOCK
- VOUT : SIGNAL OUTPUT

