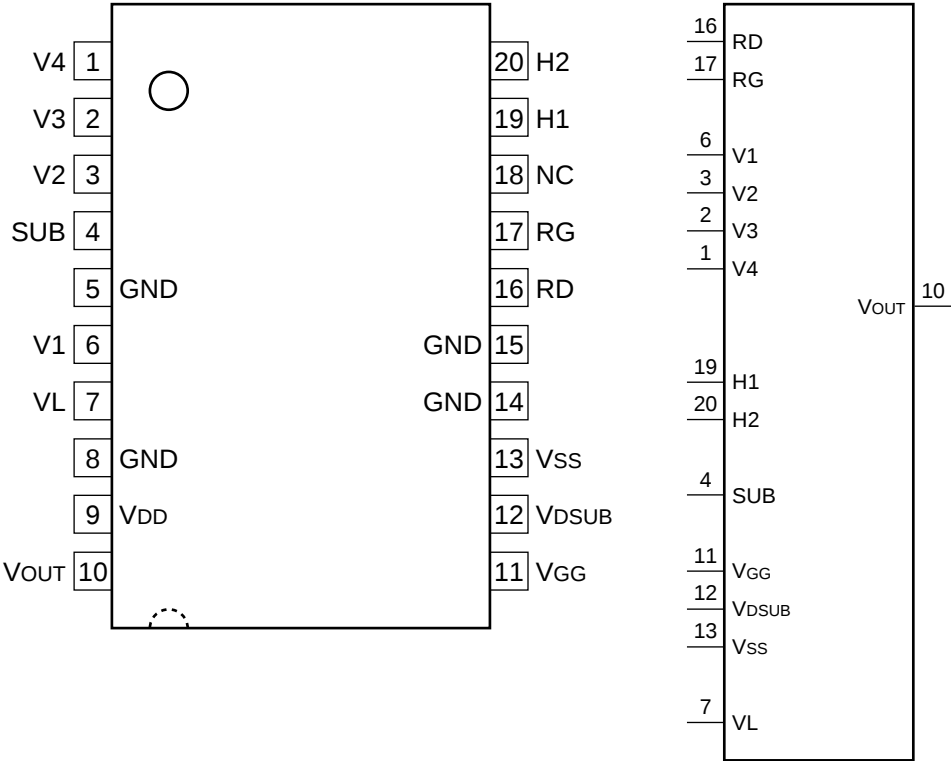


1/2-INCH CCD IMAGE BLOCK

—TOP VIEW—



- INPUT**
- H1, H2 : HORIZONTAL REGISTER TRANSFER CLOCK
 - RD : RESET DRAIN BIAS
 - RG : REST GATE CLOCK
 - SUB : OVERFLOW DRAIN
 - V1 - V4 : VERTICAL REGISTER TRANSFER CLOCK
 - VDSUB : BIAS AMP
 - VGG : OUTPUT AMP GATE BIAS
 - VL : PROTECTION TRANSISTOR BIAS
 - VSS : OUTPUT AMP SOURCE

- OUTPUT**
- VOUT : SIGNAL

