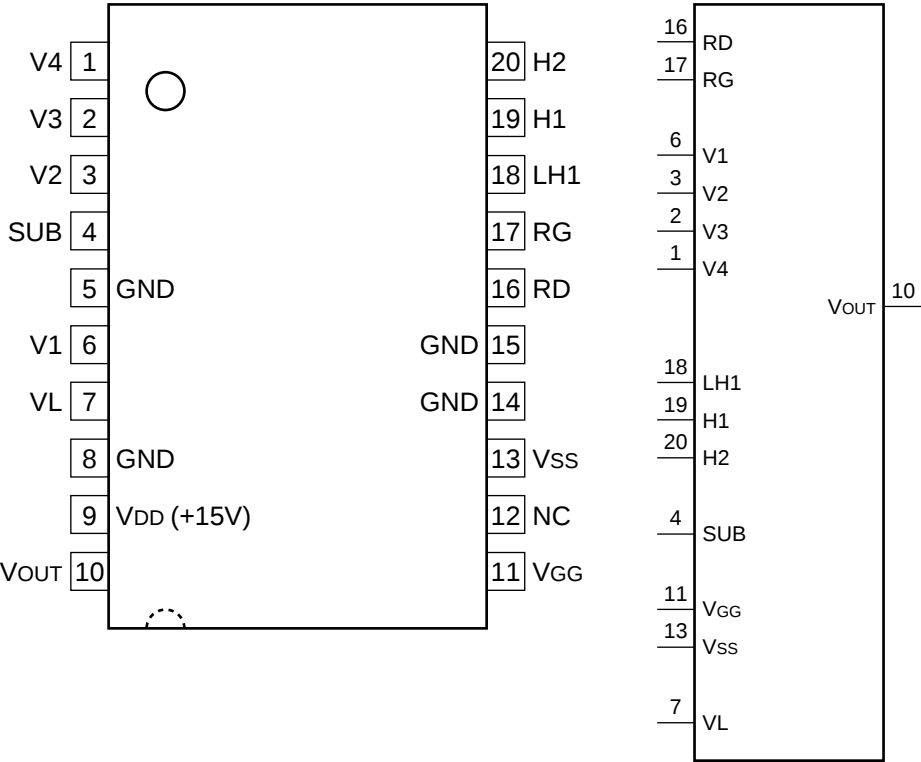

1/2-INCH CCD IMAGE BLOCK

—BOTTOM VIEW—



- H1, H2 ; HORIZONTAL REGISTER TRANSFER CLOCK
- LH1 ; HORIZONTAL REGISTER LAST STAGE TRANSFER CLOCK
- RD ; RESET DRAIN BIAS
- RG ; REST GATE CLOCK
- SUB ; OVERFLOW DRAIN
- VGG ; OUTPUT AMP GATE BIAS
- VL ; PROTECTION TRANSISTOR BIAS
- Vss ; OUTPUT AMP SOURCE
- V1 - V4 ; VERTICAL REGISTER TRANSFER CLOCK
- VOUT ; SIGNAL OUTPUT

