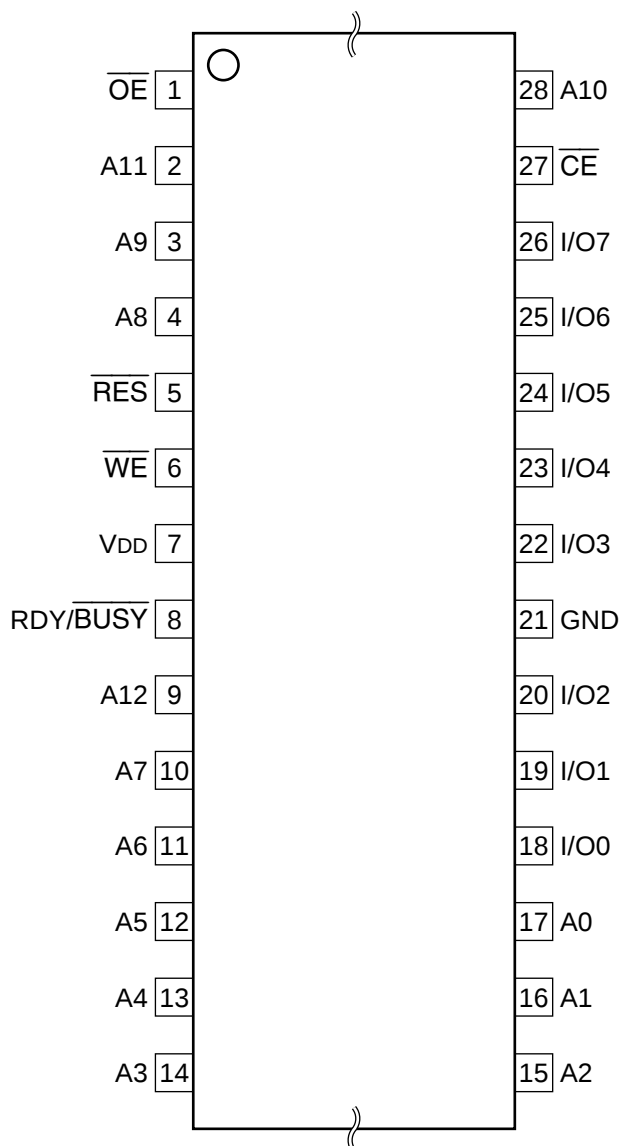


64 K (8,192 × 8)-BIT EEPROM

—TOP VIEW—

**INPUTS**

A0 - A12 : ADDRESS
 CE : CHIP ENABLE
 OE : OUTPUT ENABLE
 RDY/BUSY : REDY/BUSY
 RES : RESET
 WE : WRITE ENABLE

INPUT/OUTPUT

I/O0 - I/O7 : DATA

MODE \ PIN	CE	OE	WE	RDY/BUSY	RES	I/O
READ	0	0	1	HI-Z	1	DOUT
STANDBY	1	x	x	HI-Z	x	HI-Z
WRITE	0	1	0	HI-Z → Vol	1	DIN
DESELECT	0	1	1	HI-Z	1	HI-Z
WRITE INHIBIT	x	x	1	—	x	—
WRITE INHIBIT	x	0	x	—	x	—
DATA POLLING	0	0	1	Vol	1	DATA OUT
PROGRAM RESET	x	x	x	HI-Z	0	HI-Z

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE