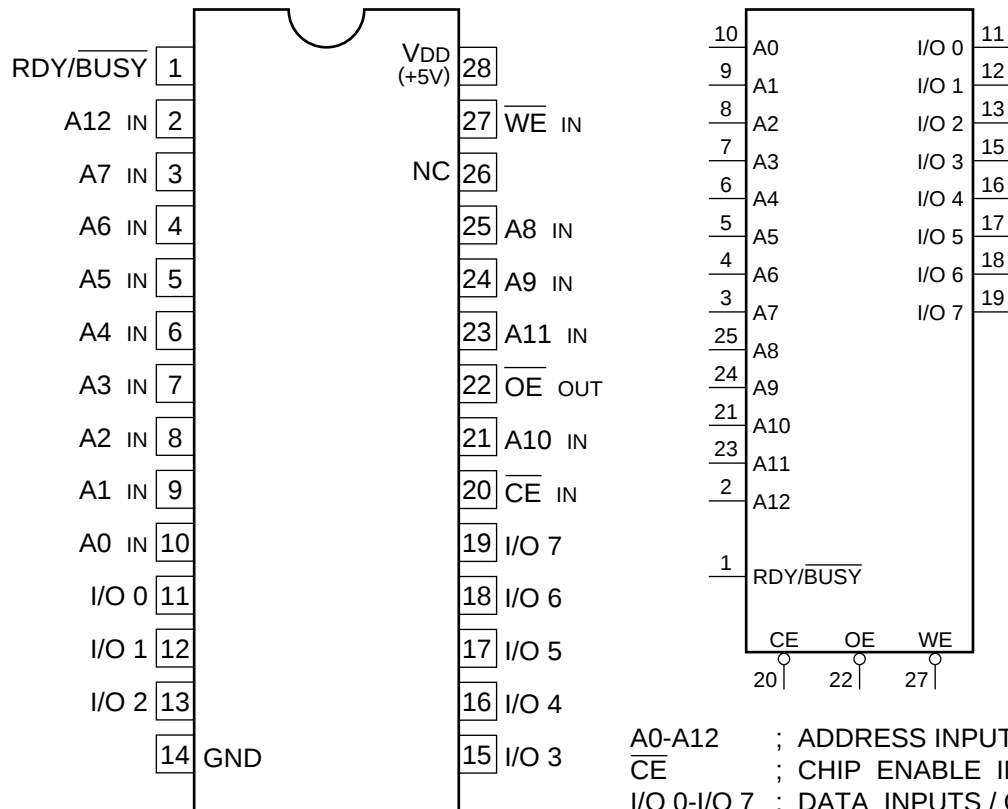
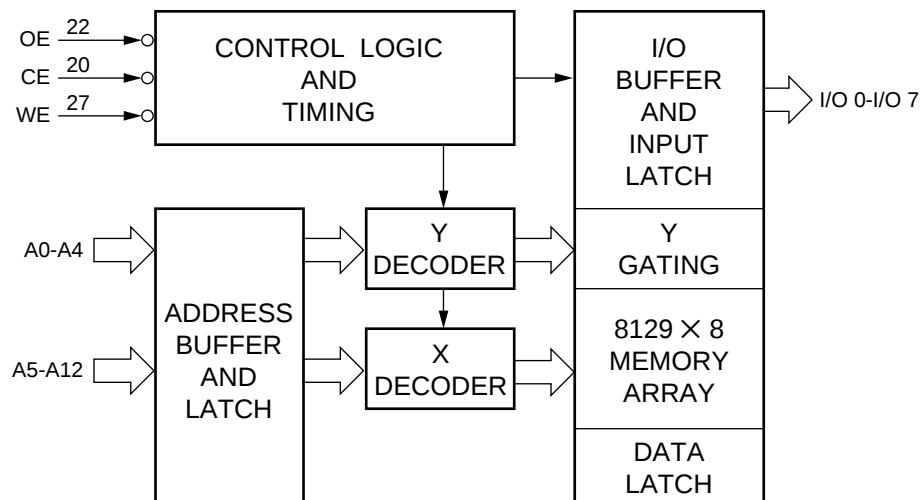

C-MOS 64K (8192 X 8)-BIT EEPROM

-TOP VIEW-



A0-A12 ; ADDRESS INPUTS
 CE ; CHIP ENABLE INPUT
 I/O 0-I/O 7 ; DATA INPUTS / OUTPUTS
 OE ; OUTPUT ENABLE
 RDY/BUSY ; READY / BUSY OUTPUT
 WE ; WRITE ENABLE INPUT



CE	OE	WE	RDY/ $\overline{\text{BUSY}}$	I/O TERMINAL	FUNCTION
0	0	1	HI-Z	D _{OUT}	READ
1	X	X	HI-Z	HI-Z	STANDBY
0	1	0	HI-Z $\rightarrow$ LOW	D _{IN}	WRITE
0	1	1	HI-Z	HI-Z	DESELECT
X	X	1	HI-Z	—	WRITE INH
X	0	X	HI-Z	—	WRITE INH
0	0	1	LOW	DATA OUT (I/O 7)	DATA POLLING

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

HI-Z ; HIGH IMPEDANCE