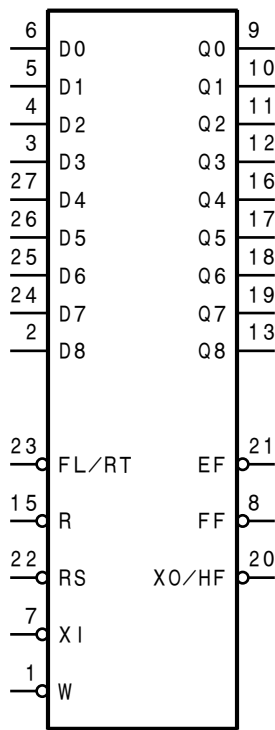
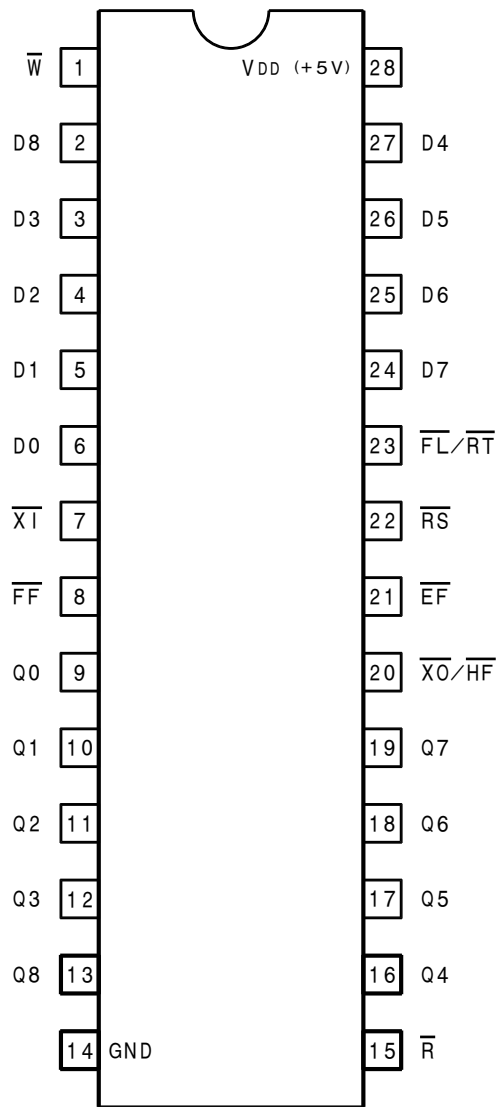

C-MOS 2Kx9-BIT PARALLEL IN-OUT FIFO MEMORY
-TOP VIEW-



INPUT
D0-D8 ; DATA INPUT
FL ; FIRST LOAD
R-bar ; READ ENABLE
RS-bar ; RESET
RT-bar ; RETRANSMIT
W-bar ; WRITE ENABLE
X1-bar ; EXPANSION IN

OUTPUT
EF-bar ; EMPTY FLAG
FF-bar ; FULL FLAG
HF-bar ; HALF FULL FLAG
Q0-Q8 ; DATA OUTPUT
X0-bar ; EXPANSION OUT

