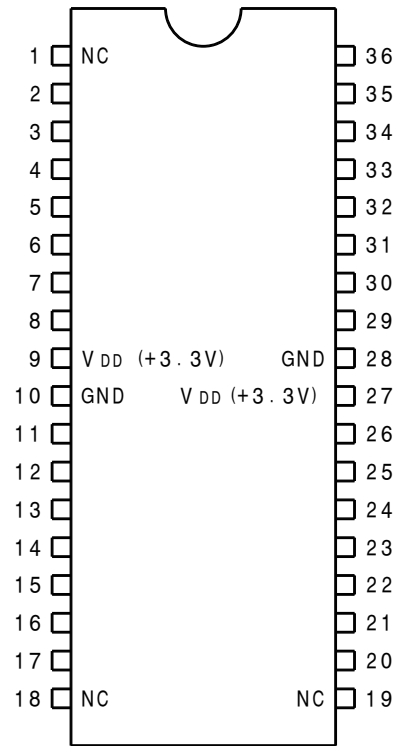

C-MOS 1M(128kx9)-BIT STATIC RAM

-TOP VIEW-

(V_{DD} =+3.3V)



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	-	NC	19	-	NC
2	I	A3	20	I	A12
3	I	A2	21	I	A11
4	I	A1	22	I	A10
5	I	A0	23	I	A9
6	I	$\overline{\text{CS}}$	24	I	A8
7	I/O	I/O1	25	I/O	I/O5
8	I/O	I/O2	26	I/O	I/O6
9	-	V _{DD}	27	-	V _{DD}
10	-	GND	28	-	GND
11	I/O	I/O3	29	I/O	I/O7
12	I/O	I/O4	30	I/O	I/O8
13	I	$\overline{\text{WE}}$	31	I/O	I/O9
14	I	A16	32	I	$\overline{\text{OE}}$
15	I	A15	33	I	A7
16	I	A14	34	I	A6
17	I	A13	35	I	A5
18	-	NC	36	I	A4

INPUT

A0-A16 ; ADDRESS INPUTS
 $\overline{\text{CS}}$; CHIP SELECT
 $\overline{\text{OE}}$; OUTPUT ENABLE INPUT
 $\overline{\text{WE}}$; WRITE ENABLE INPUT

INPUT/OUTPUT

I/O1-I/O9 ; DATA INPUTS/OUTPUTS

$\overline{\text{CS}}$	$\overline{\text{OE}}$	$\overline{\text{WE}}$	MODE	I/O
1	X	X	NON SELECT	HI-Z
0	1	1	OUTPUT DISABLE	HI-Z
0	0	1	READ	OUTPUT
0	X	0	WRITE	INPUT

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

