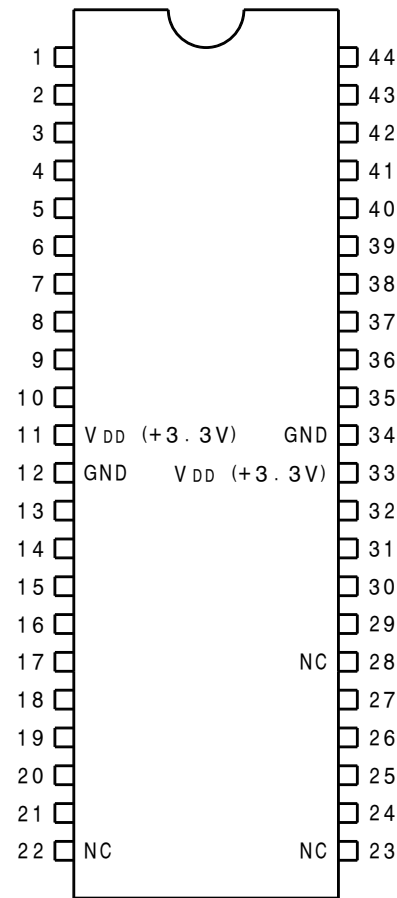
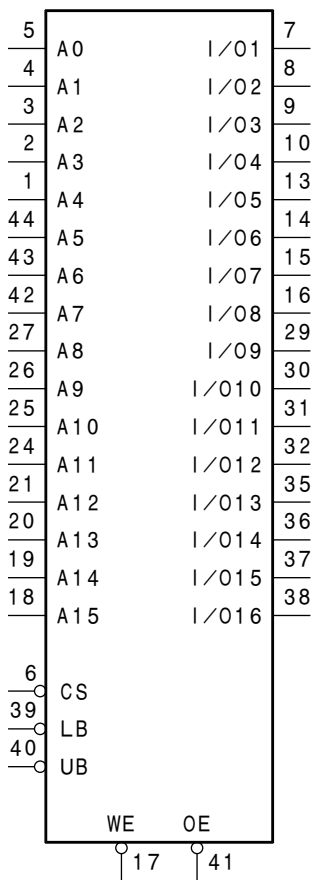

C-MOS 2M(65,536x16)-BIT STATIC RAM
-TOP VIEW-



(V_{DD} = +3.3V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	A4	12	-	GND	23	-	NC	34	-	GND
2	I	A3	13	I/O	I/O5	24	I	A11	35	I/O	I/O13
3	I	A2	14	I/O	I/O6	25	I	A10	36	I/O	I/O14
4	I	A1	15	I/O	I/O7	26	I	A9	37	I/O	I/O15
5	I	A0	16	I/O	I/O8	27	I	A8	38	I/O	I/O16
6	I	CS	17	I	WE	28	-	NC	39	I	LB
7	I/O	I/O1	18	I	A15	29	I/O	I/O9	40	I	UB
8	I/O	I/O2	19	I	A14	30	I/O	I/O10	41	I	OE
9	I/O	I/O3	20	I	A13	31	I/O	I/O11	42	I	A7
10	I/O	I/O4	21	I	A12	32	I/O	I/O12	43	I	A6
11	-	VDD	22	-	NC	33	-	VDD	44	I	A5



INPUT
A0-A15 ; ADDRESS DATA INPUTS
 $\overline{CS}$; CHIP SELECT
 $\overline{LB}$; BYTE SELECT (LOW BYTE)
 $\overline{UB}$; BYTE SELECT (UP BYTE)
 $\overline{WE}$; WRITE ENABLE INPUT
 $\overline{OE}$; OUTPUT ENABLE INPUT

INPUT/OUTPUT
I/O1-I/O8 ; DATA INPUTS/OUTPUTS (LOW BYTE)
I/O9-I/O16 ; DATA INPUTS/OUTPUTS (UP BYTE)

$\overline{CS}$	$\overline{OE}$	$\overline{WE}$	$\overline{LB}$	$\overline{UB}$	I/O1-I/O8 (LOW BYTE)	I/O9-I/O16 (UP BYTE)	MODE
1	X	X	X	X	HI-Z	HI-Z	NON SELECT
0	1	1	X	X	HI-Z	HI-Z	-
0	0	1	0	0	D OUT	D OUT	READ (LOW&UP)
0	0	1	0	1	D OUT	HI-Z	READ (LOW)
0	0	1	1	0	HI-Z	D OUT	READ (UP)
0	0	1	1	1	HI-Z	HI-Z	-
0	X	0	0	0	D IN	D IN	WRITE (LOW&UP)
0	X	0	0	1	D IN	HI-Z	WRITE (LOW)
0	X	0	1	0	HI-Z	D IN	WRITE (UP)
0	X	0	1	1	HI-Z	HI-Z	-

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

