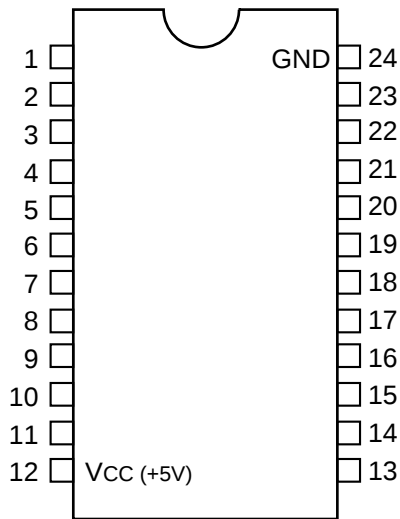
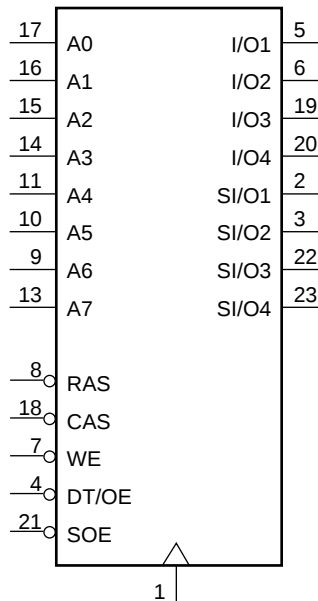


C-MOS 64K WORD x 4 BIT MULTI PORT RAM

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	SC	13	I	A7
2	I/O	SI/O1	14	I	A3
3	I/O	SI/O2	15	I	A2
4	—	$\overline{DT/OE}$	16	I	A1
5	I/O	I/O1	17	I	A0
6	I/O	I/O2	18	—	$\overline{CAS}$
7	—	$\overline{WE}$	19	I/O	I/O3
8	—	$\overline{RAS}$	20	I/O	I/O4
9	I	A6	21	—	$\overline{SOE}$
10	I	A5	22	I/O	SI/O3
11	I	A4	23	I/O	SI/O4
12	—	VCC	24	—	GND



A0-A7 ; ADDRESS INPUT
 CAS ; COLUMN ADDRESS STROBE INPUT
 DT/OE ; DATA TRANSMISSION/OUTPUT ENABLE INPUT
 I/O1-I/O4 ; RAM PORT DATA INPUT/OUTPUT
 RAS ; ROW ADDRESS STROBE INPUT
 SC ; SERIAL CLOCK INPUT
 SI/O1-SI/O4 ; SAM PORT DATA INPUT/OUTPUT
 SOE ; SAM PORT ENABLE INPUT/OUTPUT
 WE ; WRITE ENABLE INPUT

