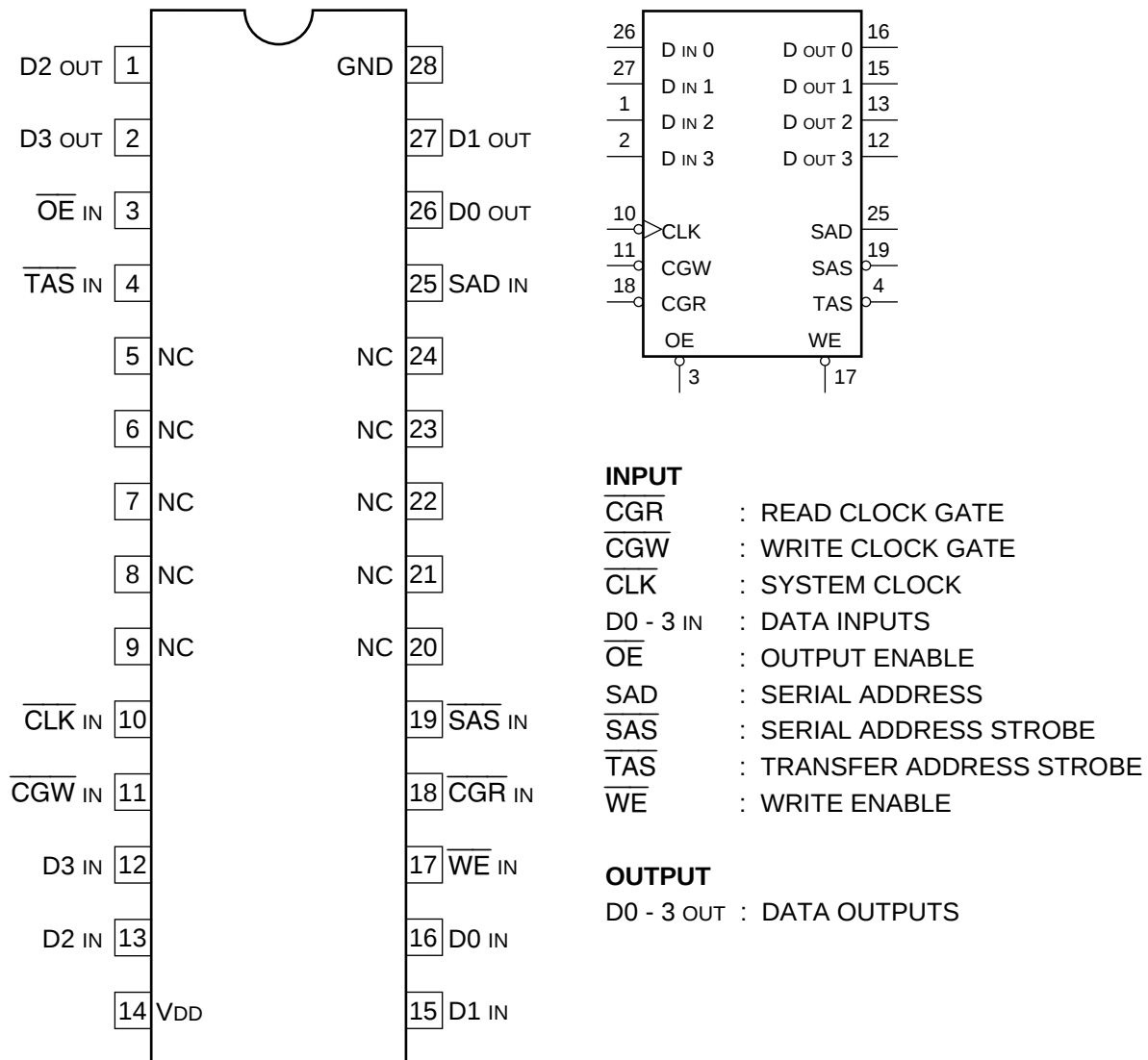


C-MOS 1 M (262,144 × 4)-BIT FRAME MEMORY

—TOP VIEW—

The System mode is selected by LSB-bits OF SAD. ($\overline{\text{MF}}$, $\overline{\text{WF}}$, $\overline{\text{RF}}$, AWO, ARO)

$\overline{\text{MF}}$	$\overline{\text{WF}}$	$\overline{\text{RF}}$	AWO	ARO	MODE
0	0	0	x	x	READ/WRITE ADDRESS TRANSFER ASYNCHRONOUS
0	0	1	x	x	WRITE ADDRESS TRANSFER ASYNCHRONOUS
0	1	0	x	x	READ ADDRESS TRANSFER ASYNCHRONOUS
0	1	1	x	x	—
1	0	0	x	x	READ/WRITE ADDRESS TRANSFER SYNCHRONOUS
1	0	1	x	x	WRITE ADDRESS TRANSFER SYNCHRONOUS
1	1	0	x	x	READ ADDRESS TRANSFER SYNCHRONOUS
1	1	1	1	1	SYSTEM RESET
1	1	1	0	0	PROHIBITION
1	1	1	0	1	
1	1	1	1	0	

MF : MODE FLAG

WF : WRITE FLAG

RF : READ FLAG

AW (0 - 12) : WRITE ADDRESS

AR (0 - 12) : READ ADDRESS

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

