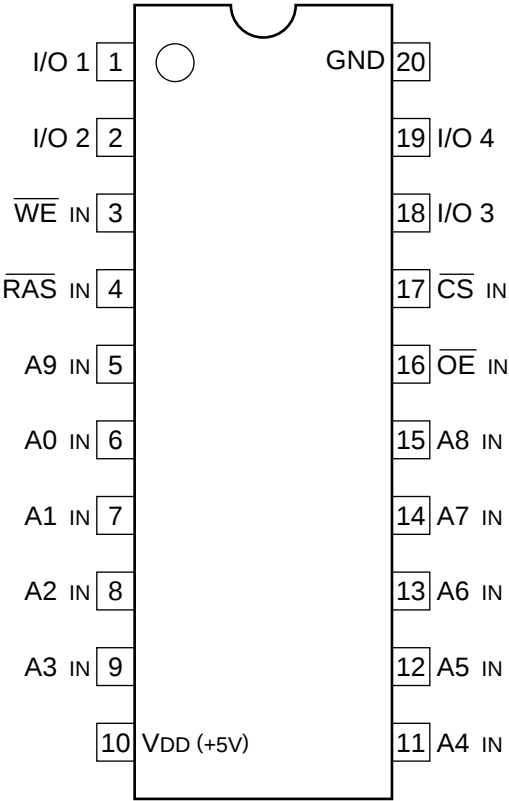

C-MOS 4M (1,048,576 × 4)-BIT DYNAMIC RAM

-TOP VIEW-



INPUT

- A0–A8 ; ADDRESS
- CS ; CHIP SELECT
- OE ; OUTPUT ENABLE
- RAS ; ROW ADDRESS STROBE
- WE ; WRITE ENABLE

INPUT/OUTPUT

- I/O1–I/O4 ; DATA

