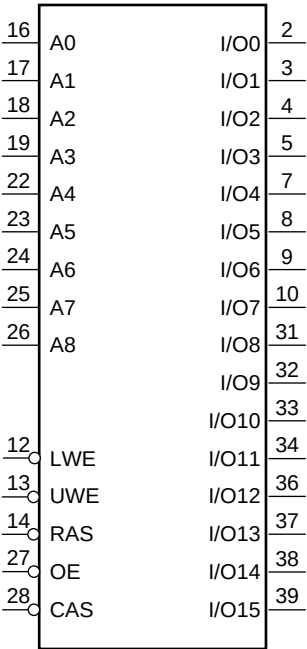
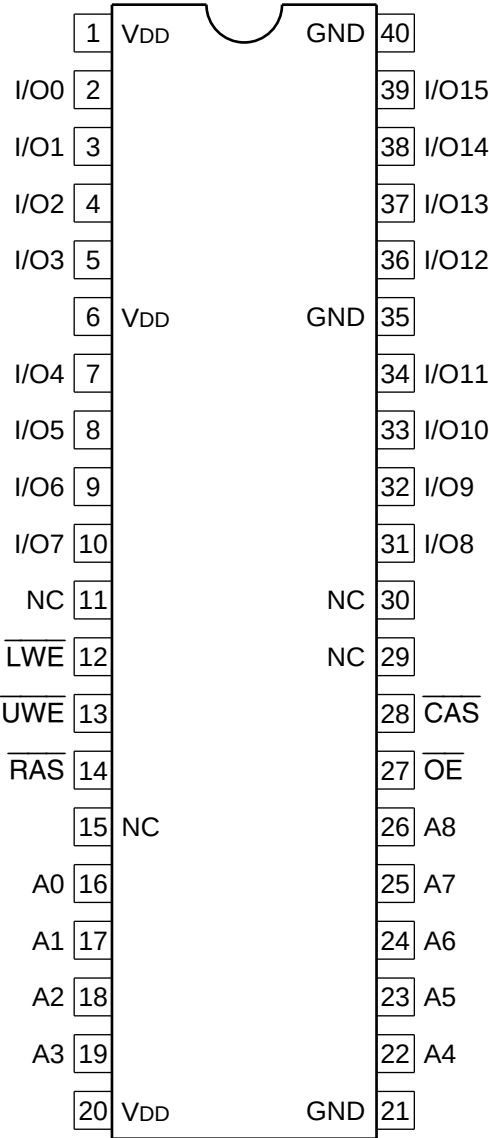


C-MOS 4 M (262,144 W × 16)-BIT DRAM

—TOP VIEW—



INPUT

A0 - A8 : ADDRESS INPUT

CAS : COLUMN ADDRESS STROBE

LWE : READ/WRITE INPUT

OE : OUTPUT ENABLE

RAS : ROW ADDRESS STROBE

UWE : READ/WRITE INPUT

INPUT/OUTPUT

I/O0 - I/O15 : DATA INPUT/OUTPUT

