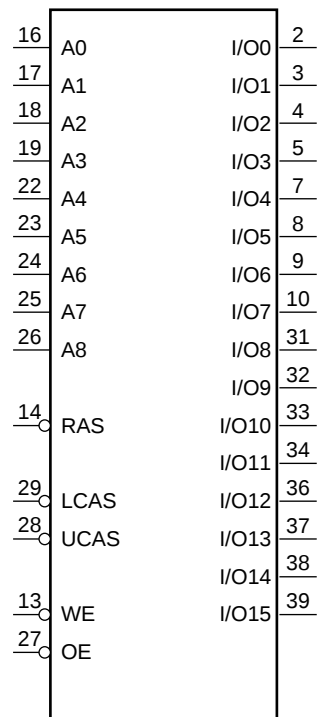
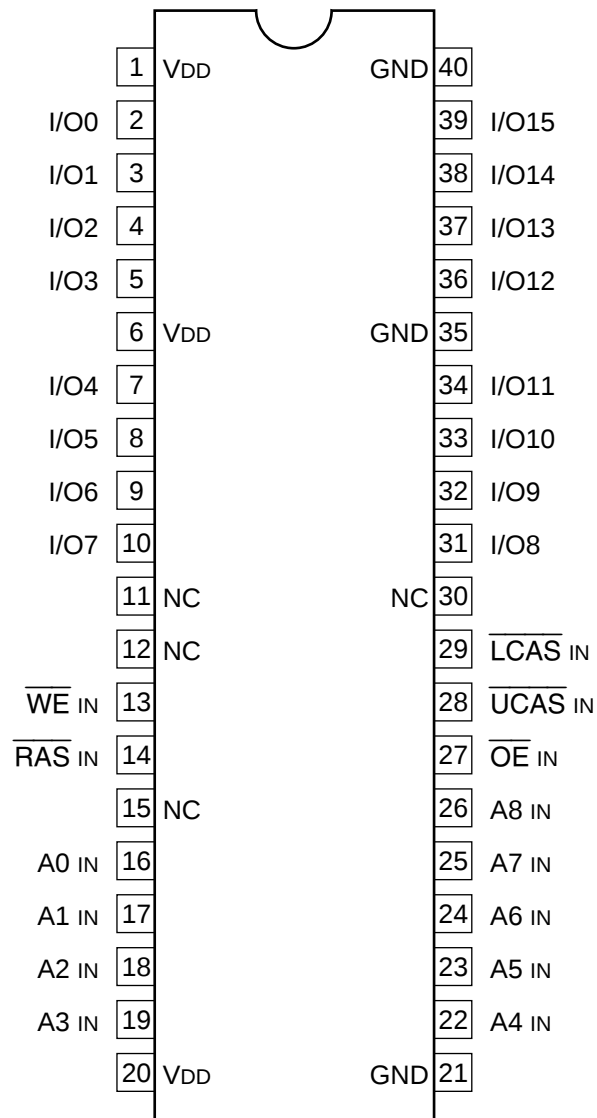


C-MOS 4 M (262,144 W × 16)-BIT DYNAMIC RAM

—TOP VIEW—

**INPUT**

A0 - A8 : ADDRESS
(ROW/REFRESH : A0 - A8,
COLUMN : A0 - A8)

LCAS : COLUMN ADDRESS STROBE

OE : OUTPUT ENABLE

RAS : ROW ADDRESS STROBE

UCAS : COLUMN ADDRESS STROBE

WE : READ/WRITE INPUT

INPUT/OUTPUT

I/O0 - I/O15 : DATA INPUT/OUTPUT

