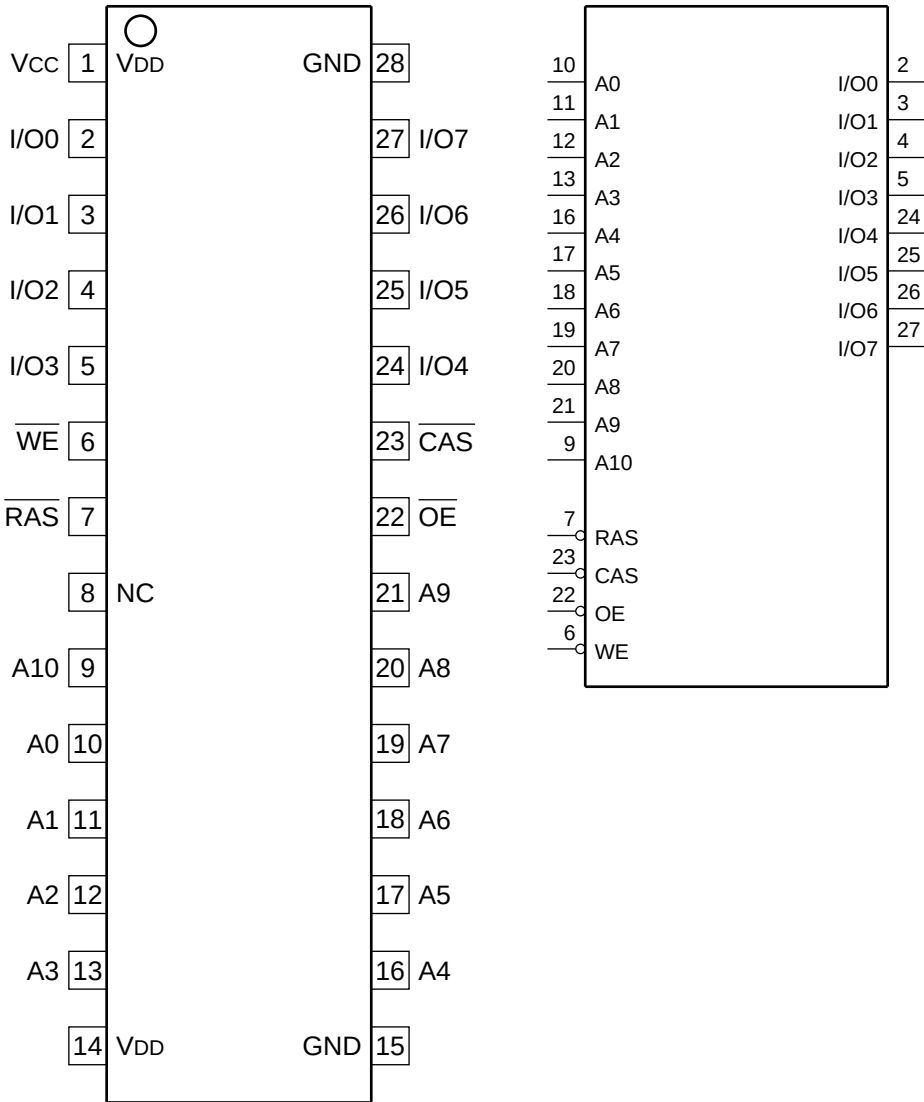


C-MOS (2,097,152-WORD × 8)-BIT DRAM

—TOP VIEW—



INPUT
A0 - A10 ; ADDRESS
 $\overline{\text{RAS}}$; ROW ADDRESS STROBE
 $\overline{\text{CAS}}$; COLUMN ADDRESS STROBE
 $\overline{\text{OE}}$; OUTPUT ENABLE
 $\overline{\text{WE}}$; WRITE ENABLE

INPUT/OUTPUT
I/O0 - I/O7 ; DATA

